# Bus Management for Windows Programmer's Reference Guide

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NOTES

# 1. Introducing Bus Management for Windows

This manual is intended for programmers using the Bus Management for Windows programming interface to develop enhanced mode Windows applications that control I/O modules via the VXI expansion interface on an EPC or a VXLink card. You are expected to have read the *EPConnect/VXI for DOS & Windows User's Guide* for an understanding of what is in EPConnect/VXI, how to install it, and how to use the Start-Up Resource Manager (SURM). You are not expected to have in-depth knowledge of Windows.

Bus Management for Windows is designed to execute under enhanced mode Windows only. It will not execute properly under Windows standard mode. It is also designed to execute on EPC-7 hardware or better. It will not execute properly on an EPC-2.

The Bus Management for Windows API provides a powerful interface for interacting with the VXI expansion interface on an EPC or a VXLink card. The Bus Management API offers considerable flexibility by providing a C/C++ dynamic link library (DLL) interface that obeys the MS Pascal binding conventions. By observing the same conventions, you can use EPConnect with other languages, such as Visual Basic.

This chapter introduces you to the RadiSys<sup>®</sup> Bus Management for Windows product. In it you will find the following:

- What is in this manual and how to use it
- What is Bus Management for Windows?
- Programming, Compiling and Linking
- What to do next

# 1.1 How This Manual is Organized

This manual has five chapters:

Chapter 1, Introduction, introduces Bus Management for Windows and this manual.

Chapter 2, *Function Descriptions*, describes the major categories of functions and gives complete descriptions of each function. The function descriptions also contain supporting examples or references to an example that demonstrates use of the function. Function descriptions are alphabetic by function name.

Chapter 3, *Advanced Topics*, provides information about byte swapping, interrupts, and the backward-compatibility library.

Chapter 4, *Support and Service*, describes how to contact RadiSys Technical Support for support and service.

# 1.2 What is Bus Management for Windows?

Bus Management for Windows consists of those portions of the EPConnect software package that are required by C/C++ and Visual BASIC programmers developing VXI control applications that execute in enhanced mode Windows. Figure 1-1 is a diagram of the Bus Management for Windows software architecture that shows how the architecture relates to the VXIbus hardware.

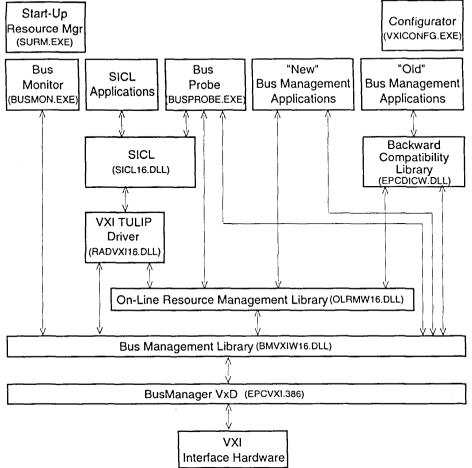


Figure 1-1. EPConnect/VXI for Windows Software Architecture

#### 1.2.1 Bus Management Library and BusManager VxD

The Bus Management library and BusManager VxD are at the foundation of EPConnect. They provide the lowest level interface to the VXIbus hardware through their function libraries. These functions allow you to:

- Control VXIbus word serial registers.
- Send word serial commands of all sizes.
- Transfer blocks of data to and from VXIbus devices, with BERR detection.
- Control EPC Slave memory
- Query EPC driver, firmware, and hardware version or type.

The Bus Management DLL supports ANSI "C" compilers such as Microsoft C/C++ and Borland C/C++, as well as Visual Basic.

The Bus Management Library is fully re-entrant.

#### 1.2.2 OLRM

The On-Line Resource Management library (**OLRMW16.DLL**) provides user applications with access to results of the resource management process, as well as retrieving status information from devices over the VXI bus. A C/C++ language interface is provided to access OLRM functions. OLRM accesses the VXI bus through the Bus Management library and the BusManager VxD.

#### 1.2.3 Backward-Compatibility Library

The Backward-Compatibility Library (EPCDICW.DLL) maintains compatibility for applications that were developed using previous versions of EPConnect. It provides a C/C++ interface that is compatible with the Bus Management for DOS API.

#### 1.2.4 SURM

The Start-Up Resource Manager (SURM) determines the physical content of the system and configures the devices. It is typically the first program to run after DOS boots. The SURM is the EPConnect implementation of the resource manager defined in the VXIbus specification. However, SURM extends the specification definition to include non-VXIbus devices, such as GPIB instruments. The SURM uses the **DEVICES** file to obtain device information not directly available from the devices. SURM accesses VXIbus devices in the system directly.

# 1.3 Programming, Compiling and Linking

This section contains information about programming with Bus Management for Windows. Included is a list of the header files provided, the programming interfaces, and compiling and linking hints.

#### 1.3.1 Header Files

Bus Management for Windows provides the following header files:

- **BMVXI.BAS** A Microsoft Visual Basic (Professional Edition) header file containing constant definitions and function declarations.
- **BUSMGR.H** A "C" header file containing the constant definitions, macro definitions, and function prototypes required to compile EPConnect applications using any Microsoft or Borland "C" or C++ compiler.
- **BUSMGR.INC** A copy of **BUSMGR.H** that's been converted so that it is suitable for inclusion into an assembly language source file.
- **EPC\_OBM.H** A "C" header file containing the constant definitions, macro definitions, structure definitions, and function prototypes required to compile Bus Management applications for DOS. This header file also provides backward compatibility for Bus Management for Windows applications written for releases preceding revision 4.0.

EPC\_OBM.H should never be included in a source file directly. BUSMGR.H includes EPC\_OBM.H.

EPC\_OBM.INC A copy of EPC\_OBM.H that has been converted so that it is suitable for inclusion into an assembly language source file.

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EPC\_OBM.INC should never be included in a source file directly. BUSMGR.INC includes EPC\_OBM.INC.

**EPCSTD.H** A "C" header file containing macro definitions to standardize non-ANSI, compiler-dependent keywords. By using the macros defined here, an application can compile successfully using any revision of Microsoft or Borland "C" or C++ compiler without modifying the source file.

EPCSTD.H should never be included in a source file directly. BUSMGR.H includes EPC\_OBM.H.

**EPCSTD.INC** A copy of **EPCSTD.H** that has been converted so that it is suitable for inclusion into an assembly language source file.

EPCSTD.INC should never be included in a source file directly. BUSMGR.INC includes EPCSTD.INC.

- **OLRM.H** A "C" header file containing the constant definitions, macro definitions, and function prototypes required to compile OLRM applications using any Microsoft or Borland C/C++ compiler.
- **OLRM.INC** A copy of **OLRM.H** that has been converted so that it is suitable for inclusion into an assembly language source file.
- **OBS\_OLRM.H** A "C" header file containing the constant definitions, macro definitions, and function prototypes required to compile OLRM applications for DOS. This header file also provides backward-compatibility for Bus Management for Windows applications written for releases preceding revision 4.0.

OBS\_OLRM.H should never be included in a source file directly. OLRM.H includes OBS\_OLRM.H.

#### OBS\_OLRM.INC

A copy of **OBS\_OLRM.H** that has been converted so that it is suitable for inclusion into an assembly language source file.

VMEREGS.H A "C" header file containing constant and macro definitions for accessing the EPC VMEbus control registers.

#### VMEREGS.INC

A copy of VMEREGS.H that has been converted so that it is suitable for inclusion into an assembly language source file.

All Bus Management for Windows header files contain an **#if/#endif** pair surrounding the contents of the header file so that the file can be included multiple times without causing compiler errors.

All Bus Management for Windows "C" header files also contain extern "C"{} bracketing for C++ compilers. Because extern "C" is strictly a C++ keyword, it is also bracketed and only visible when compiling under C++ and not standard "C."

#### 1.3.2 Programming Interface

Bus Management for Windows functions are accessible through interfaces for "C" and Visual BASIC languages. The following table shows the interface libraries and header files for each of the language interfaces.

| Language     | Library files               | Header files          |
|--------------|-----------------------------|-----------------------|
| MS "C"       | BMVXIW16.LIB<br>OLRMW16.LIB | BUSMGR.H<br>OLRM.H    |
| Borland "C"  | BMVXIW16.LIB<br>OLRMW16.LIB | BUSMGR.H<br>OLRM.H    |
| Visual Basic | BMVXIW16.LIB<br>OLRMW16.LIB | BMVXI.BAS<br>OLRM.BAS |

The use of these files is discussed in the following sections.

#### Calling Bus Management for Windows from MS "C" and QuickC

The "C" language interface is designed to work with Version 5.1 and later versions of the Microsoft "C" compiler and libraries. The libraries are created for the large memory model (far code and far data). This is sufficient for linking programs of any model size, due to the prototyping of all library functions in the header files. The include files provide strong type checking and convert near code and data to far code and data for programs using the small (near code and near data), compact (near code and far data), or medium (far code and near data) memory models.

#### Calling Bus Management for Windows from Borland C

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Bus Management for Windows was designed to work with "C" compilers adhering to the Microsoft "C" calling conventions. Both Microsoft and Borland "C" compilers work equally well.

#### Calling Bus Management for Windows from Visual Basic

Calling Bus Management for Windows functions from Visual Basic requires using the **BMVXI.BAS** and **OLRM.BAS** header files in your project.

To compile and link a program once your project is built, choose "Make .EXE File" from the File Menu.

For more information about calling "C" DLLs from Visual Basic, refer to the *Microsoft Visual Basic Programming System for Windows Programmer's Guide*.

#### **1.3.3 Compiling and Linking Applications**

**NOTE:** For specific compiler and/or linker options, refer to your vendor's documentation.

The following examples assume that EPConnect software has been installed in the C:\EPCONNEC directory.

When compiling applications, ensure that the Bus Management for Windows header files are in the compiler search path by doing one of the following:

- 1. Specify the entire header file pathname when including the header file in the source file.
- 2. Specify C:\EPCONNEC\INCLUDE as part of the header file search path parameter on the compiler invocation line.
- 3. Specify C:\EPCONNEC\INCLUDE as part of the header file search path environment variable.

Also, ensure that Bus Management for Windows libraries are in the linker search path by doing one of the following:

- 1. Specify the entire library pathname when linking object files.
- 2. Specify C:\EPCONNEC\LIB as part of the linker library search path.

## 1.4 What to do Next

To begin using Bus Management for Windows:

- 1. If Bus Management for Windows is not pre-installed on your system, install and configure it using the procedures in Chapter 2 of the *EPConnect/VXI* for DOS & Windows User's Guide.
- 2. Refer to the function descriptions in Chapter 2 of this manual for details about a function and/or its parameters to develop applications.
- 3. Refer to the sample code included with the Bus Management for Windows software under the C:\EPCONNEC\SAMPLES\BUSMGR.W31 directory.

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# 2. Function Descriptions

This chapter lists the Bus Management for Windows library functions by category and by name. It is for the programmer who needs a particular fact, such as what function performs a specific task or what a function's arguments are.

The first section lists the functions categorically by the task each performs. It also gives you a brief description of what each function does. The second section lists the functions alphabetically and describes each function in detail.

# 2.1 Functions by Category

The categorical listing provides an overview of the operations performed by the EPConnect functions. Included with each category is a description of the operations performed, a listing of the functions in the category, and a brief description of each function.

The categories of the Bus Management for Windows library functions include:

- Environment
- Bus Sessions
- Locking
- Memory Mapping
- Byte Order
- Events
- EPC Configuration
- Bus Control Lines
- Watchdog Timer
- Commander Support
- Servant Support

#### 2.1.1 Environment

Bus Management for Windows provides support that allows an application to query and verify the state of its environment.

The Bus Management for Windows library supplies two functions for environment support:

| <b>Function</b>      | Descriptio   | <u>on</u> |         |     |           |
|----------------------|--|-----------|---------|-----|-----------|
| EpcGetErrorString    | Queries a null-terminated string corresponding to an EPConnect return value. |           |         |     |           |
| EpcVerifyEnvironment | Verifies<br>environme  |           | queries | the | EPConnect |

#### 2.1.2 Sessions

Bus Management for Windows provides support for multiple simultaneous *sessions*. A session encapsulates shared operating system and interface hardware state in an environment where multiple applications may be accessing the interface hardware simultaneously.

Each session contains a set of attributes that define how resources are managed. Session attributes include:

- A locking timeout. A locking timeout defines how long the session will wait for shared EPC hardware to become unlocked. See the Locking section for more information.
- A list of memory mappings. A memory mapping defines where in the EPC's address space an access takes place and how data is accessed. See the **Memory Mapping** section for more information.
- An enabled event mask attribute. The enabled event mask attribute defines the set of events that the session can receive when each of the events' corresponding interrupt or error occurs. See the **Events** section for more information.
- An event handler attribute array. The event handler attribute array defines the functions that are called when the session receives events. The session maintains one entry in the event handler attribute array for each possible event. See the **Events** section for more information.

The Bus Management for Windows library supplies the following functions in support of sessions:

| <u>Function</u>   | Description                                     |
|-------------------|---|
| EpcCloseSession   | Destroys an open session.                       |
| EpcGetSessionData | Queries a session's application-specified data. |
| EpcOpenSession    | Creates a session                               |
| EpcSetSessionData | Defines a session's application-specified data. |

To use session functionality, an application must first call EpcOpenSession to create a session. Once a session exists, an application can access and manage the bus using any of the remaining Bus Management for Windows library functions. The application can define and query application-specific data using EpcSetSessionData and EpcGetSessionData. When the application is finished with a session, it should call EpcCloseSession to destroy the session. Failing to destroy an existing session before an application terminates may result in the loss of both virtual and physical resources.

#### 2.1.3 Locking

Bus Management for Windows provides support for *locking*. Locking gives a session exclusive access to shared interface hardware. Locking is used in multithreaded environments to prevent simultaneous, potentially conflicting hardware manipulation.

Locks can be nested. Bus Management for Windows maintains a global lock counter. At most one session may "own" the lock counter. Initially, the lock counter is zero, indicating that no session has locked the shared interface hardware. Locking acquires and increments the lock counter for a session. Unlocking decrements the lock counter for the same session. A non-zero lock counter indicates that shared interface hardware is locked.

When an application calls a Bus Management for Windows library function that obeys the locking paradigm, the function checks for an existing lock. If no lock exists or the specified session "owns" the lock, the function proceeds. Otherwise, the function suspends execution until the lock is released or the specified session's locking timeout expires. If the existing lock is not released before the specified session's locking timeout expires, the function returns EPC\_LOCKED.

The Bus Management for Windows library supplies the following functions in support of locking:

| <b>Function</b>      | Description                                      |  |
|----------------------|--|--|
| EpcGetLockingTimeout | Queries a session's locking timeout.             |  |
| EpcLockSession       | Locks shared interface hardware for a session.   |  |
| EpcSetLockingTimeout | Defines a session's locking timeout.             |  |
| EpcUnlockSession     | Unlocks shared interface hardware for a session. |  |

To use locking functionality, an application must first call EpcOpenSession to create a session. Once a session exists, an application can lock and unlock shared interface hardware for the session using EpcLockSession and EpcUnlockSession, respectively. When the application completes executing locked operations, it should unlock the session. Failing to unlock a session before an application terminates, either explicitly using EpcUnlockSession or implicitly using EpcCloseSession, may prevent other applications from accessing shared interface hardware. An application can also query and define the session's locking timeout using **EpcGetLockingTimeout** and **EpcSetLockingTimeout**.

Only Bus Management for Windows library functions that may make conflicting hardware accesses obey the locking paradigm:

| EpcAssertInterrupt    | EpcSetEpcLines            |
|-----------------------|---------------------------|
| EpcCmdReceiveWSBuffer | EpcSetEpcMODID            |
| EpcCmdSendWSBuffer    | EpcSetEpcTriggers         |
| EpcCmdSendWSCommand   | EpcSetMiscAttributes      |
| EpcDeassertInterrupt  | EpcSetSlaveMapping        |
| EpcLockSession        | EpcSetULA                 |
| EpcMapBusMemory       | EpcSrvEnableWsCommand     |
| EpcMapEpcTriggers     | EpcSrvReceiveWSCommand    |
| EpcMapSharedMemory    | EpcSrvSendProtocolEvent   |
| EpcPulseEpcLines      | EpcSrvSendWSProtocolError |
| EpcPulseEpcTriggers   | EpcSrvSendWSResponse      |
| EpcSetBusAttributes   | EpcValidateBusMapping     |

Note that the ability to directly map bus memory allows an application to circumvent the locking protections provided in Bus Management for Windows for VXIbus word serial, byte transfer, and event protocols. Each application is responsible for ensuring that it obeys all bus protocols when accessing bus memory directly.

Locking is not a substitute for a sound shared memory protocol. Locking does not protect against multiple processors making simultaneous accesses to the same memory.

#### 2.1.4 Memory Mapping

EPConnect provides support for *memory mappings*. A memory mapping defines where in the interface's physical address space a mapped access takes place and how data is accessed. Each session contains a list of memory mappings.

A memory mapping can map either bus memory or shared memory. Bus memory is VMEbus memory accessed using the interface's VMEbus hardware. Shared memory is an area of local memory that has a fixed size and a fixed physical location and is accessible via the VMEbus, thereby making it suitable for implementing shared memory communication protocols in a multiple processor system.

#### Memory Mapping Attributes

Each memory mapping contains a set of attributes that define where and how memory is accessed. Memory mapping attributes include:

- An address modifier attribute. The address modifier attribute defines whether the memory mapping maps to bus memory or shared memory. If the memory mapping maps to bus memory, the address modifier attribute also defines the mapping's VMEbus address space and VMEbus access mode.
- A byte ordering attribute. The byte ordering attribute defines whether Motorola or Intel byte ordering is assumed when the memory mapping is used to access data in widths greater than 8 bits. For a bus memory mapping, the byte ordering attribute specifies either Motorola or Intel byte ordering. For a shared memory mapping, the byte ordering attribute always specifies Intel byte ordering.
- A base address attribute. The base address attribute defines where the memory mapping begins. For a bus memory mapping, the base address attribute is an address in one of the VMEbus address spaces. For a shared memory mapping, the base address attribute is an address in the local address space.
- A size attribute. The size attribute defines the extent of a memory mapping, in bytes.

• A type attribute. The type attribute defines whether a mapping is a shared memory mapping, a bus memory mapping that uses statically configured bus window hardware, or a bus memory mapping that uses dynamically configured bus window hardware.

Statically configured bus window hardware corresponds to a fixed address modifier, byte ordering, and bus address range. A bus memory mapping that uses statically configured bus window hardware can access mapped bus memory at will.

Dynamically configured bus window hardware corresponds to a variable address modifier, byte ordering, and bus address ramge. A bus memory mapping that uses dynamically configured bus window hardware must configure its bus window hardware before accessing mapped bus memory.

The EPConnect Bus Management Library supplies the following functions in support of memory mappings:

| <b>Function</b>                | Description   |
|--------------------------------|---|
| EpcCopyData                    | Copy a block of data from consecutive memory locations to consecutive memory locations. |
| <b>EpcGetMappingAttributes</b> | Query a memory mapping's attributes.  |
| <b>EpcMapBusMemory</b>         | Create a bus memory mapping using a statically configured bus window.                   |
| <b>EpcMapBusMemoryExt</b>      | Create a bus memory mapping using a dynamically configured bus window.                  |
| EpcMapSharedMemory             | Create a shared memory mapping.   |
| EpcPopData                     | Pop a block of data from a single memory location to consecutive memory locations.      |
| EpcPushData                    | Push a block of data from consecutive memory locations to a single memory location.     |
| EpcUnmapBusMemory              | Destroy a bus memory mapping.   |
| EpcUnmapSharedMemory           | Destroy a shared memory mapping.  |
| EpcValidateBusMapping          | Validate a bus memory mapping that uses a dynamically configured bus window.            |

To use memory mapping functionality, an application must first call EpcOpenSession to open a bus session and either EpcMapBusMemory, EpcMapBusMemoryExt, or EpcMapSharedMemory to create a memory mapping. Once a memory mapping exists, an application can access the mapped memory either directly or by using EpcCopyData, EpcPopData, or EpcPushData. When the application is finished with a memory mapping, it should call either EpcUnmapBusMemory or EpcUnmapSharedMemory to destroy the mapping. Failing to destroy an existing memory mapping before an application terminates, either explicitly using EpcUnmapBusMemory or EpcUnmapSharedMemory or implicitly using EpcCloseSession, may result in the loss of both virtual and physical resources.

Direct access of mapped memory provides the maximum possible data transfer performance, but it does not automatically detect and handle misaligned data, potential bus errors, or hardware restrictions. Direct access requires that the application guarantee data alignment and bus error avoidance. Direct access using a bus memory mapping created with EpcMapBusMemoryExt requires using EpcValidateBusMapping before an access to insure that the dynamically configured bus window references the desired bus memory. Finally, direct access using a bus memory mapping created with EpcMapBusMemoryExt in a preemptively scheduled environment requires using EpcLockSession before EpcValidateBusMapping and EpcUnlockSession after the direct access to ensure that the dynamically configured bus window is not reconfigured by another thread during the direct access. Note that EpcCopyData, EpcPopData, and EpcPushData copy blocks of data while taking hardware restrictions, data alignment, and potential bus error considerations into account.

An application can use EpcGetMappingAttributes to query an existing memory mapping's attributes.

#### 2.1.5 Byte Order

The Bus Management for Windows library provides support for converting data between Intel and Motorola byte order through byte-swapping.

The Bus Management for Windows library supplies the following functions in support of byte order conversion:

| Function      | Description                    |
|---------------|--------------------------------|
| EpcSwapBuffer | Byte-swaps a buffer of values. |
| EpcSwap16     | Byte-swaps a 16-bit value.     |
| EpcSwap32     | Byte-swaps a 32-bit value.     |
| EpcSwap48     | Byte-swaps a 48-bit value.     |
| EpcSwap64     | Byte-swaps a 64-bit value.     |
| EpcSwap80     | Byte-swaps an 80-bit value.    |

#### 2.1.6 Events

EPConnect provides support for *events*. An event is an interrupt or error that occurs asynchronously with respect to normal program execution.

Each session contains a set of event attributes that define how the session handles events. Event attributes include:

- An enabled event mask attribute. The enabled event mask attribute defines the set of events that the session can receive when each of the events' corresponding interrupt or error occurs.
- An array of event handlers. The event handler array defines the functions that are called when the session receives events. The session maintains one entry in the event handler array for each possible event.

The EPConnect Bus Management Library supplies the following functions in support of events:

| Function              | Description  |
|-----------------------|--|
| EpcGetEventEnableMask | Queries a session's enabled event mask attribute.    |
| EpcGetEventHandler    | Queries an entry in a session's event handler array. |
| EpcSetEventEnableMask | Defines a session's enabled event mask attribute.    |
| EpcSetEventHandler    | Defines an entry in a session's event handler array. |
| EpcWaitForEvent       | Waits for an event to occur.                         |

To use event functionality, an application must first call **EpcOpenSession** to create a session. Once a session exists, an application can either wait for the desired events to occur using **EpcWaitForEvent** or it can define handlers for the desired events using **EpcSetEventHandler**. In either case, the application must enable reception of the events using **EpcSetEventEnableMask** to receive them.

When the application is finished receiving events, it should disable reception of the events. Failing to disable reception of events before an application terminates, either explicitly using **EpcSetEventEnableMask** or implicitly using **EpcCloseSession**, may result in a system crash the next time an event occurs.

An application can use **EpcGetEventEnableMask** and **EpcGetEventHandler** to query a session's current event attributes.

The Bus Management for Windows library supports all possible VXIbus events. In practice, however, event support is limited by the underlying interface hardware.

The table below describes the events:

| Event             | Description  |
|-------------------|--|
| EPC_MSG_INT       | Message interrupt (EPC-7 and EPC-8 only)               |
| EPC_VME1_INT      | VMEbus interrupt 1                                     |
|                   |  |
| EPC_VME7_INT      | VMEbus interrupt 7                                     |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                           |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0<br>interrupt (EPC-7 only)         |
| •••               |  |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7<br>interrupt (EPC-7 only)         |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                  |
| EPC_ACFAIL_ERR    | VMEbus power failure error                             |
| EPC_BERR_ERR      | VMEbus access error                                    |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                   |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration error (EPC-7 and EPC-8 only) |
| EPC_EXT_TRIG0_INT | External Trigger 0 interrupt<br>(VXLink only)          |
| EPC_EXT_TRIG1_INT | External Trigger 1 interrupt<br>(VXLink only)          |

#### 2.1.7 EPC Configuration

Bus Management for Windows provides support for maintaining global interface configuration attributes. The values of global interface configuration attributes affect all the behavior of the interface hardware for all sessions.

The Bus Management for Windows library supplies the following functions in support of interface configuration:

| <b>Function</b>      | Description   |  |  |  |
|----------------------|---|--|--|--|
| EpcGetBusAttributes  | Queries the interface's bus management attributes.                    |  |  |  |
| EpcGetMiscAttributes | Queries the interface's<br>miscellaneous configuration<br>attributes. |  |  |  |
| EpcGetSlaveMapping   | Queries the interface's slave memory mapping.                         |  |  |  |
| EpcGetULA            | Queries the interface's unique logical address.                       |  |  |  |
| EpcSetBusAttributes  | Defines the interface's bus management attributes.                    |  |  |  |
| EpcSetMiscAttributes | Defines the interface's miscellaneous configuration attributes.       |  |  |  |
| EpcSetSlaveMapping   | Defines the interface's slave memory mapping.                         |  |  |  |
| EpcSetULA            | Defines the interface's unique logical address.                       |  |  |  |

To use interface configuration functionality, an application must first call **EpcOpenSession** to create a session. Once a session exists, an application can define the global interface configuration attributes using **EpcSetBusAttributes**, **EpcSetSlaveMapping**, and **EpcSetULA**. An application can query the global interface configuration attributes using **EpcGetBusAttributes**, **EpcGetSlaveMapping**, and **EpcGetULA**.

#### 2.1.8 Bus Lines

Bus Management for Windows provides support for defining, querying, and pulsing the interface line state. It also provides support for monitoring actual bus line state.

In general, interface line state reflects the state of bits in the interface's line drive registers, while actual bus line state is an OR'd combination of the states of all devices on the bus. If the interface asserts a line, the actual bus line transitions from deasserted to asserted only if all other devices on the bus have previously deasserted the line. Likewise, if the interface deasserts a line, the actual bus line transitions from asserted to deasserted only if all devices on the bus have previously deasserted the line.

The Bus Management for Windows library supplies the following functions in support of bus lines:

| <b>Function</b>         | Description                                      |  |  |  |
|-------------------------|--|--|--|--|
| EpcAssertInterrupt      | Asserts a VME interrupt.                         |  |  |  |
| EpcDeassertInterrupt    | Deasserts a VME interrupt.                       |  |  |  |
| EpcGetBusInterrupts     | Queries actual bus VME interrupt line state.     |  |  |  |
| EpcGetBusLines          | Queries actual bus control line state.           |  |  |  |
| EpcGetBusMODID          | Queries actual bus MODID line state.             |  |  |  |
| EpcGetBusTriggers       | Queries actual bus trigger line state.           |  |  |  |
| EpcGetEpcInterrupt      | Queries interface VME interrupt assertion state. |  |  |  |
| EpcGetEpcLines          | Queries interface control line state.            |  |  |  |
| EpcGetEpcMODID          | Queries interface MODID line state.              |  |  |  |
| EpcGetEpcTriggers       | Queries interface trigger line state.            |  |  |  |
| EpcGetEpcTriggerMapping | Queries an interface trigger line mapping.       |  |  |  |
| EpcMapEpcTriggers       | Maps one interface trigger line to another.      |  |  |  |
| EpcPulseEpcLines        | Pulses interface control lines.                  |  |  |  |
| EpcPulseEpcTriggers     | Pulses interface trigger lines.                  |  |  |  |
| EpcSetEpcLines          | Defines the interface control line state.        |  |  |  |

| EpcSetEpcMODID    | Defines the interface MODID line state.   |
|-------------------|---|
| EpcSetEpcTriggers | Defines the interface trigger line state. |

To use bus control line functionality, an application must first call EpcOpenSession to create a session. Once a session exists, an application can define interface line state using EpcAssertInterrupt. EpcDeassertInterrupt. EpcSetEpcLines. EpcSetEpcMODID, or EpcSetEpcTriggers. An application can pulse interface lines using EpcPulseEpcLines or EpcPulseEpcTriggers. To query the interface line EpcGetEpcInterrupt, state. an application can use EpcGetEpcLines. EpcGetEpcMODID, EpcGetEpcTriggers, or EpcGetEpcTriggerMapping. Τo query actual bus line state, the application can use EpcGetBusInterrupts. EpcGetBusLines, EpcGetBusMODID, or EpcGetBusTriggers.

#### 2.1.9 Watchdog Timer

Bus Management for Windows provides watchdog timer services that allow an application to prevent interface lock-up under extraordinary circumstances.

If an EPC's watchdog timer is not reset within the current watchdog timer period, either a system reset occurs or a watchdog timer error event occurs. In the latter case, an application can enable the event and install an event handler to gracefully handle the error.

The EPC's watchdog timer is typically reset in sections of code that execute frequently and/or execute at regular time intervals.

The Bus Management for Windows library supplies a single function in support of the watchdog timer:

| <b>Function</b>  | Description |      |          |       |
|------------------|-------------|------|----------|-------|
| EpcWatchdogTimer | Modifies    | EPC  | watchdog | timer |
|                  | configurat  | ion. |          |       |

To use watchdog timer functionality, an application must first call **EpcOpenSession** to create a session. Once a session exists, an application can configure the EPC's watchdog timer using **EpcWatchdogTimer**.

An EPC's watchdog timer is a shared EPC hardware resource. However, Bus Management for Windows provides no functionality for controlling shared access to the watchdog timer. Multiple applications may simultaneously use watchdog timer functionality. However, EPConnect software cannot guarantee the result.

The purpose of the watchdog timer hardware is to allow an application to prevent EPC lock-up under extraordinary circumstances. Placing additional layers of software between an application and the watchdog timer hardware to control sharing of the resource would necessarily restrict an application's access to the watchdog timer, thereby violating its original purpose.

By default, an EPC is configured to use a long watchdog timer period and to generate a watchdog error event upon expiration. Assuming that no application attempts to modify these default watchdog timer settings, any number of applications may use the watchdog timer simultaneously. VXLink does not contain a watchdog timer. This function is valid only on an EPC-7 and EPC-8.

### 2.1.10 Commander Support

EPConnect provides support for using an EPC or VXlink card as a commander device in a VXIbus system.

The Bus Management for Windows library supplies the following functions in support of using an EPC as a commander device:

| Function              | Description                                      |
|-----------------------|--|
| EpcCmdReceiveWSBuffer | Receives a buffer of data from a servant device. |
| EpcCmdSendWSBuffer    | Sends a buffer of data to a servant device.      |
| EpcCmdSendWSCommand   | Sends a word serial command to a servant device. |

To use commander functionality, an application must first call **EpcOpenSession** to create a session. Once a session exists, an application can send 16-bit, 32-bit, or 48-bit word serial commands and receive responses using **EpcCmdSendWSCommand**. To quickly send multiple data bytes to a servant device, an application should use **EpcCmdSendWSBuffer**. To quickly receive multiple data bytes from a servant device, an application should use **EpcCmdReceiveWSBuffer**.

### 2.1.11 Servant Support

Bus Management for Windows provides support for using an EPC as a servant device in a VMEbus system.

The Bus Management for Windows library supplies the following functions in support of using an EPC as a servant device:

| Function                  | Description   |
|---------------------------|---|
| EpcSrvEnableWSCommand     | Enables word serial command reception.                        |
| EpcSrvReceiveWSCommand    | Receives a word serial command from the commander device.     |
| EpcSrvSendProtocolEvent   | Sends a protocol event to the commander device.               |
| EpcSrvSendWSProtocolError | Sends a word serial protocol error to the commander device.   |
| EpcSrvSendWSResponse      | Sends a word serial command response to the commander device. |

To use servant functionality, an application must first call EpcOpenSession to create a session. Once a session exists, an application can receive 16-bit or 32-bit word serial commands using EpcSrvEnableWSCommand and EpcSrvReceiveWSCommand and send responses to received word serial commands using EpcSrvSendWSResponse. An application can use EpcSrvSendProtocolEvent to send events and/or responses to a commander device via the commander device's signal register.

Servant functionality is supported on an EPC-7 and EPC-8 only. A VXLink interface does not support servant functionality.

### 2.2 Functions By Name

This section contains an alphabetical listing of the Bus Management for Windows library functions. Each listing describes the function, gives its invocation sequence and arguments, discusses its operation, and lists its returned values. Where usage of the function may not be clear, an example with comments is given.

### **EpcAssertInterrupt**

**Description** Asserts a VME interrupt.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcAssertInterrupt(unsigned long Session\_ID, unsigned long
Event\_Mask);

| Session_ID | Session_ID specifies a session.       |
|------------|---------------------------------------|
| Event_Mask | Event_Mask specifies a VME interrupt. |

Visual Basic Synopsis

Declare Function EpcAssertInterrupt% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Event\_Mask&)

**Remarks** EpcAssertInterrupt causes the interface to assert a VME interrupt.

Event\_Mask specifies the VME interrupt to assert. Valid values are:

Event\_Mask Description

EPC\_VME1\_INT VMEbus interrupt 1.

•••

EPC\_VME7\_INT VMEbus interrupt 7.

An interface acts as both a D08(O) and a D16 interrupter. For D08 interrupt acknowledge cycles, the interface uses its unique logical address as the status/ID value. For D16 interrupt acknowledge cycles, the interface uses the upper 8 bits of its response register for the upper 8 bits of the status/ID value and its unique logical address as the lower 8 bits of the status/ID value.

|          | EPC_INV_ASSERT         | The interface is already asserting a VMEbus interrupt.  |
|----------|------------------------|---|
|          | EPC_INV_MASK           | The parameter Event_Mask is invalid.  |
|          | EPC_INV_SESSION        | The specified Session_ID is invalid.  |
|          | EPC_INV_SW             | The BusManager device driver is not<br>present or there is a revision mismatch<br>between the Bus Management Library<br>and the BusManager VxD. |
|          | EPC_LOCKED             | Shared interface hardware is locked by another session.   |
|          | EPC_SUCCESS            | The function completed successfully.  |
| See Also | EncDeassertInterrunt F | EncGetEncInterrunt  |

**Return Value** The function returns a Bus Management return value:

### See Also EpcDeassertInterrupt, EpcGetEpcInterrupt, EpcSrvSendProtocolEvent.

```
Example
```

```
• Copyright 1994 by RadiSys Corporation. All rights reserved.
1.

    buslines.c -- Bus Management Library interface bus line functions sample code.

*/
#include "busmgr.h"
 • FUNCTION PROTOTYPES...
 */
short FAR
BusLinesSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
/*
* CODE...
•/
short FAR
BusLinesSample(void)
{
    char
                          err_string[ERROR_STRING_SZ];
    short
                          err_num;
   unsigned long
                          bus_int_mask;
   unsigned long
                          bus_line_mask;
    unsigned long
                          epc_int_mask;
```

```
unsigned long
                          epc_line_mask;
   unsigned long
                          session_id;
   struct EpcEnvironment environment;

    Verify the interfaceonnect environment.

     + /
   if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    {
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
    /*
     • Open a session.
     •/
    if ((err_num = EpcOpenSession(&session_id)) != EPC_SUCCESS)
    ł
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
     * Assert VMEbus interrupt #1, query bus and interface VMEbus interrupt
       * assertions, deassert VMEbus interrupt #1, and query bus and interface
VMEbus

    interrupt assertions again.

     *
     */
    EpcAssertInterrupt(session_id, EPC_VME1_INT);
    EpcGetBusInterrupts(session_id, &bus_int_mask);
    EpcGetEpcInterrupt(session_id, &epc_int_mask);
    WinPrintf("VMEbus interrupt 1 asserted.\n");
       WinPrintf("Bus interrupt mask = 0x%081X, interface interrupt mask =
0x%081X\n",
              bus_int_mask,
              epc_int_mask);
    EpcDeassertInterrupt(session_id);
    EpcGetBusInterrupts(session_id, &bus_int_mask);
    EpcGetEpcInterrupt(session_id, &epc_int_mask);
    WinPrintf("VMEbus interrupt 1 deasserted.\n");
        WinPrintf("Bus interrupt mask = 0x%081X, interface interrupt mask =
0x%081X\n",
              bus_int_mask,
              epc_int_mask);
    1+
     * Assert SYSFAIL, query bus and interface line assertions, pulse SYSFAIL, and
     " query bus and interface line assertions again.
     */
    EpcSetEpcLines(session_id, EPC_SYSFAIL);
    EpcGetBusLines(session_id, &bus_line_mask);
    EpcGetEpcLines(session_id, &epc_line_mask);
    WinPrintf("SYSFAIL asserted.\n");
    WinPrintf("Bus line mask = 0x%081X, interface line mask = 0x%081X\n",
```

## 2

### EpcAssertInterrupt

```
bus_line_mask,
        epc_line_mask);
EpcPulseEpcLines(session_id, EPC_SYSFAIL);
WinPrintf("SYSFAIL pulsed.\n");
WinPrintf("Bus line mask = 0x%08lX, interface line mask = 0x%08lX\n",
        bus_line_mask,
        epc_line_mask);
/*
 * Close the session and return.
 */
EpcCloseSession(session_id);
WinPrintf("SUCCESS: BusLinesSample() complete.\n");
return (EPC_SUCCESS);
```

}

2

### EpcCloseSession

**Description** Destroys an open session.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcCloseSession(unsigned long Session\_ID);

Session\_ID Session\_ID specifies an open session.

Visual Basic Synopsis

Declare Function EpcCloseSession% Lib "bmvxiw16.dll" (ByVal Session\_ID&)

**Remarks** EpcCloseSession closes an open session.

If the specified session has locked shared interface hardware, the hardware is unlocked before the function destroys the session.

If the specified session has one or more enabled events, the events are disabled before the function destroys the session. Also, all of the session's defined event handlers are removed.

If the specified session contains one or more memory mappings, the mappings are destroyed before the function destroys the session.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_SESSION | The specified Session_ID is invalid.  |
|-----------------|---|
| EPC_INV_SW      | The BusManager device driver is not<br>present or there is a revision mismatch<br>between the Bus Management Library<br>and the BusManager VxD. |
| EPC_INV_USAGE   | The session specified by <i>Session_ID</i> is currently in use by another thread.   |
| EPC_SUCCESS     | The function completed successfully.  |

See Also EpcOpenSession, EpcSetEventEnableMask, EpcUnlockSession, EpcUnmapBusMemory, EpcUnmapSharedMemory.

### Example

```
* Copyright 1994 by RadiSys Corporation. All rights reserved.
* /
1.
 * sessions.c -- Bus Management Library session functions sample code.
#include "busmgr.h"
* FUNCTION PROTOTYPES...
*/
short FAR
SessionsSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
* CODE...
 */
short FAR
SessionsSample(void)
ſ
   char
                          err_string[ERROR_STRING_SZ];
                         err_num;
   short
                          session_data;
   unsigned long
   unsigned long
                         session_id;
    struct EpcEnvironment environment;

    Verify the EPConnect environment.

     */
    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
```

### Bus Management for Windows Programmer's Reference

```
2
```

{

```
EpcGetErrorString(err_num, err_string);
    WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
              err_string,
              err_num);
    return (err_num);
}
1 * *
 *•Open a session.
*/
if ((err_num = EpcOpenSession(&session_id)) != EPC_SUCCESS)
{
    EpcGetErrorString(err_num, err_string);
    WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
              err_string,
              err_num);
    return (err_num);
}
/*
 **Define the session's application-specific data.
 * 7
EpcSetSessionData(session_id, session_id);
/**
 **Query the session's application-specific data.
 */
EpcGetSessionData(session_id, &session_data);
1*
 *•Close the session and return.
 * 7
EpcCloseSession(session_id);
WinPrintf("SUCCESS: SessionsSample() complete.\n");
return (EPC_SUCCESS);
```

}

### EpcCmdReceiveWSBuffer

**Description** Receives a buffer of data from a servant device.

C Synopsis

| #include "busmgr.h"                                      |   |  |
|--|---|--|
| unsigned long<br>short<br>unsigned long<br>unsigned long | g Session_ID,<br>t ULA,<br>FAR * Buffer_Ptr,<br>Buffer_Size,<br>Term_Character,               |  |
| Session_ID   | Session_ID specifies a session.   |  |
| ULA  | ULA specifies a servant device's unique logical address.                                      |  |
| Buffer_Ptr   | <i>Buffer_Ptr</i> specifies the location of a buffer where the received data will be placed.  |  |
| Buffer_Size  | <i>Buffer_Size</i> specifies the size of the buffer where the received data will be placed.   |  |
| Term_Character   | <i>Term_Character</i> specifies a termination character for the receive operation.            |  |
| Timeout  | <i>Timeout</i> specifies the number of milliseconds to wait while receiving a buffer of data. |  |

### Bus Management for Windows Programmer's Reference

Term\_Reason\_Ptr

Receive Size Ptr

2

*Term\_Reason\_Ptr* specifies a location where a bit mask defining the reason(s) for terminating the receive operation will be placed.

*Receive\_Size\_Ptr* specifies a location where the actual number of bytes received will be placed.

Visual Basic Synopsis

Declare Function EpcCmdReceiveWSBuffer% Lib "bmvxiw16.dll"

> (ByVal Session\_ID&, ByVal ULA%, ByVal Buffer\_Ptr\$, ByVal Buffer\_Size&, ByVal Term\_Character%, ByVal Timeout&, Term\_Reason\_Ptr&, Receive\_Size\_Ptr&)

**Remarks** EpcCmdReceiveWSBuffer receives up to *Buffer\_Size* bytes of data from the servant device specified by *ULA* and places them in the buffer pointed to by *Buffer\_Ptr*.

Term\_Character specifies an optional termination character for the receive operation. Valid termination character values are -1 and 0 through 255. A termination character value of -1 specifies that no termination character is defined. A termination character value of 0 through 255 specifies a termination character. If the function detects a termination character while it's receiving data, it places the termination character in the buffer and returns EPC\_SUCCESS.

If *Term\_Reason\_Ptr* is non-null and the function returns **EPC\_SUCCESS**, the location pointed by *Term\_Reason\_Ptr* contains a bit mask defining the reason(s) for terminating the receive operation. The bit mask is an OR'd combination of the following constants:

| <u>Constant</u> | Description   |
|-----------------|---|
| EPC_TERM_CHAR   | The function detected the specified termination character.    |
| EPC_TERM_EOI    | The function received a data byte with the EOI indicator set. |
| EPC_TERM_FULL   | The specified buffer is full.                                 |

The value of the location pointed to by *Term\_Reason\_Ptr* is undefined when the function does not return EPC\_SUCCESS.

If *Receive\_Size\_Ptr* is non-null, the location it points to always contains the number of data bytes actually received.

If the function detects a word serial protocol error while receiving data, it returns EPC\_WS\_PROTOCOL. To determine the protocol error, use EpcCmdSendWSCommand to send a READ PROTOCOL ERROR word serial command to the servant device and receive its response.

**EpcCmdReceiveWSBuffer** is intended for use by a commander device to quickly receive multiple data bytes from one of its servants via word serial commands. A servant device should use **EpcSrvReceiveWSCommand** to receive a word serial command from its commander and **EpcSrvSendWSResponse** to send a word serial command response to its commander.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR     | The parameter <i>Buffer_Ptr</i> is invalid.  |
|-----------------|--|
| EPC_INV_SESSION | The specified Session_ID is invalid.   |
| EPC_INV_SW      | The BusManager device driver is<br>not present or there is a revision<br>mismatch between the Bus<br>Management Library and the<br>BusManager VxD. |
| EPC_INV_TERMCHR | The parameter <i>Term_Character</i> is invalid.  |

### Bus Management for Windows Programmer's Reference

The parameter ULA is invalid. EPC\_INV\_ULA EPC\_LOCKED Shared interface hardware is locked by another session. EPC\_RECV\_BERR A bus error occurred receiving a word serial command response. A timeout occurred receiving a word EPC\_RECV\_TIMEOUT serial command response. EPC\_SEND\_BERR A bus error occurred sending a word serial command. EPC\_SEND\_TIMEOUT A timeout occurred sending a word serial command. EPC\_SUCCESS The function completed successfully. EPC\_WS\_PROTOCOL A word serial protocol error occurred.

See Also EpcCmdSendWSBuffer, EpcCmdSendWSCommand, EpcOpenSession, EpcSrvReceiveWSCommand, EpcSrvSendWSResponse.

### **EpcCmdSendWSBuffer**

| Description | Sends a buffer of data to a servant device. |
|-------------|---|
|-------------|---|

C Synopsis

#include "busmgr.h"

| short FAR PASCAL<br>EpcCmdSendWSBuf | fer( unsigned long<br>unsigned short<br>unsigned char FAR *<br>unsigned long<br>unsigned short<br>unsigned long<br>unsigned long FAR * | Buffer_Size,<br>EOI_Flag,<br>Timeout, |
|-------------------------------------|--|---------------------------------------|
| Session_ID                          | Session_ID specifies a sess  | ion.                                  |
| ULA                                 | ULA specifies a servant de logical address.  | vice's unique                         |
| Buffer_Ptr                          | Buffer_Ptr specifies the loc<br>containing the data to be se   |                                       |
| Buffer_Size                         | Buffer_Size specifies the net to be sent.  | umber of bytes                        |
| EOI_Flag                            | <i>EOI_Flag</i> specifies whether the EOI indicator should be set when the function sends the last byte from the specified buffer.     |                                       |
| Timeout                             | <i>Timeout</i> specifies the numbrilliseconds to wait while buffer of data.  |                                       |
| Send_Size_Ptr                       | Send_Size_Ptr specifies a the actual number of by placed.  |                                       |

# 2

Visual Basic Synopsis

Declare Function EpcCmdSendWSBuffer% Lib "bmvxiw16.dll" (ByVal Session\_ID&,

> ByVal ULA%, ByVal Buffer\_Ptr\$, ByVal Buffer\_Size&, ByVal EOI\_Flag%, ByVal Timeout&, Send\_Size\_Ptr&)

**Remarks** EpcCmdSendWSBuffer sends up to *Buffer\_Size* bytes of data from the buffer pointed to by *Buffer\_Ptr* to the servant device specified by *ULA*.

EOI\_Flag specifies whether the EOI indicator should be set when the function sends the last byte from the specified buffer. A nonzero EOI\_Flag value causes the function to set the EOI indicator when it sends the last byte from the buffer. A zero EOI\_Flag value causes the function to not set the EOI indicator when it sends the last byte from the specified buffer.

If Send\_Size\_Ptr is non-null, the location it points to always contains the number of data bytes actually sent.

If the function detects a word serial protocol error while sending data, it returns EPC\_WS\_PROTOCOL. To determine the protocol error, use EpcCmdSendWSCommand to send a READ PROTOCOL ERROR word serial command to the servant device and receive its response.

**EpcCmdSendWSBuffer** is intended for use by a commander device to quickly send multiple data bytes to one of its servants via word serial commands. A servant device should use **EpcSrvReceiveWSCommand** to receive a word serial command from its commander and **EpcSrvSendWSResponse** to send a word serial command response to its commander.

| <b>Return Value</b> | The function returns a Bus Management return value: |
|---------------------|---|
|---------------------|---|

| EPC_INV_PTR      | The parameter Buffer_Ptr is invalid.   |
|------------------|--|
| EPC_INV_SESSION  | The specified Session_ID is invalid.   |
| EPC_INV_SW       | The BusManager device driver is not<br>present or there is a revision<br>mismatch between the Bus<br>Management Library and the<br>BusManager VxD. |
| EPC_INV_ULA      | The parameter ULA is invalid.  |
| EPC_LOCKED       | Shared interface hardware is locked by another session.  |
| EPC_SEND_BERR    | A bus error occurred sending a word serial command.  |
| EPC_SEND_TIMEOUT | A timeout occurred sending a word serial command.  |
| EPC_SUCCESS      | The function completed successfully.   |
| EPC_WS_PROTOCOL  | A word serial protocol error occurred.   |
|                  |  |

See Also EpcCmdReceiveWSBuffer, EpcCmdSendWSCommand, EpcOpenSession, EpcSrvReceiveWSCommand, EpcSrvSendWSResponse.

### EpcCmdSendWSCommand

**Description** Sends a word serial command to a servant device.

C Synopsis

| short FAR PASC<br>EpcCmdSendWS | CAL<br>Command( unsigned long Session_ID,<br>unsigned short ULA,<br>void FAR * Command_Ptr,<br>void FAR * Response_Ptr,<br>unsigned short Size,<br>unsigned long Timeout); |
|--------------------------------|--|
| Session_ID                     | Session_ID specifies a session.  |
| ULA                            | <i>ULA</i> specifies a servant device's unique logical address.  |
| Command_Ptr                    | <i>Command_Ptr</i> specifies the location of a word serial command.  |
| Response_Ptr                   | <i>Response_Ptr</i> specifies a location where the response to the word serial command will be placed.   |
| Size                           | Size specifies the size of both the word serial command and the optional word serial command response.   |
| Timeout                        | <i>Timeout</i> specifies the number of milliseconds<br>to wait while sending the word serial<br>command and receiving the word serial<br>command response.                 |

Visual Basic Synopsis

Declare Function EpcCmdSendWSCommand% Lib "bmvxiw16.dll" (ByVal Session\_ID&,

> ByVal ULA%, Command\_Ptr As Any, Response\_Ptr As Any, ByVal Size%, ByVal Timeout&)

**Remarks** EpcCmdSendWSCommand optionally sends a word serial command, then optionally receives a word serial command response. If *Command\_Ptr* is not null, the function sends the word serial command at the location pointed to by *Command\_Ptr* to the servant device specified by *ULA*. Otherwise, the function skips sending a command. If *Response\_Ptr* is not null, the function then receives a word serial command response from the servant device specified by *ULA* and places it in the location pointed to by *Response\_Ptr*. Otherwise, the function returns without attempting to receive a response.

Size specifies the size of both the word serial command and the word serial command response:

| <u>Size</u> | Description  |
|-------------|--|
| EPC_16_BIT  | Send a 16-bit word serial command<br>and receive a 16-bit word serial<br>command response.                       |
| EPC_32_BIT  | Send a 32-bit long word serial<br>command and receive a 32-bit long<br>word serial command response.             |
| EPC_48_BIT  | Send a 48-bit extended long word<br>serial command and receive a 32-bit<br>long word serial command<br>response. |

If the function detects a word serial protocol error while sending a command or receiving a response, it returns EPC\_WS\_PROTOCOL. To determine the protocol error, use EpcCmdSendWSCommand to send a READ PROTOCOL ERROR word serial command to the servant device and receive its response.

**EpcCmdSendWSCommand** is intended for use by a commander device to send a word serial command to one of its servants and/or to receive a word serial command response from one of its servants. A servant device should use **EpcSrvReceiveWSCommand** to receive a word serial command from its commander and **EpcSrvSendWSResponse** to send a word serial command response to its commander.

Return Value The function returns a Bus Management return value:

| EPC_INV_SESSION  | The specified Session_ID is invalid.                             |  |
|------------------|--|--|
| EPC_INV_SIZE     | The parameter Size is invalid.                                   |  |
| EPC_INV_SW       | The BusManager device driver is not present.                     |  |
| EPC_INV_ULA      | The parameter ULA is invalid.                                    |  |
| EPC_LOCKED       | Shared interface hardware is locked by another session.          |  |
| EPC_RECV_BERR    | A bus error occurred receiving the word serial command response. |  |
| EPC_RECV_TIMEOUT | A timeout occurred receiving the word serial command response.   |  |
| EPC_SEND_BERR    | A bus error occurred sending the word serial command.            |  |
| EPC_SEND_TIMEOUT | A timeout occurred sending the word serial command.              |  |
| EPC_SUCCESS      | The function completed successfully.                             |  |
| EPC_WS_PROTOCOL  | A word serial protocol error occurred.                           |  |

See Also EpcOpenSession, EpcSrvReceiveWSCommand, EpcSrvSendWSResponse.

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### EpcCopyData

**Description** Copies a block of data.

C Synopsis

#include "busmgr.h"

### short FAR PASCAL

| EpcCopyData( | unsigned long       | Session_ID,       |
|--------------|---------------------|-------------------|
|              | void HUGE *         | Source_Ptr,       |
|              | void HUGE *         | Dest_Ptr,         |
|              | unsigned long       | Size,             |
|              | unsigned short      | Data_Width,       |
|              | unsigned long FAR * | Actual_Size_Ptr); |
|              |                     |                   |

| Session_ID      | Session_ID specifies a bus session.  |
|-----------------|--|
| Source_Ptr      | Source_Ptr specifies the address of a data buffer from which data will be copied.            |
| Dest_Ptr        | <i>Dest_Ptr</i> specifies the address of a data buffer into which data will be copied.       |
| Size            | <i>Size</i> specifies the number of data bytes to copy.                                      |
| Data_Width      | Data_Width specifies the number of data bits to copy per bus access.                         |
| Actual_Size_Ptr | Actual_Size_Ptr specifies a location where the actual number of bytes copied will be placed. |

Visual Basic Synopses

Declare Function BasicCopyEpcToVME% Lib "bmvxiw16.dll" ( ByVal Session\_ID&,

> Source\_Ptr As Any, ByVal Dest\_Ptr As Any, ByVal Size&, ByVal Data\_Width%, Actual\_Size\_Ptr&)

Declare Function BasicCopyVMEToEpc% Lib "bmvxiw16.dll" ( ByVal Session\_ID&,

> ByVal Source\_Ptr As Any, Dest\_Ptr As Any, ByVal Size&, ByVal Data\_Width%, Actual\_Size\_Ptr&)

Declare Function BasicCopyVMEToVME% Lib "bmvxiw16.dll"

> (ByVal Session\_ID&, ByVal Source\_Ptr As Any, ByVal Dest\_Ptr As Any, ByVal Size&, ByVal Data\_Width%, Actual\_Size\_Ptr&)

**Remarks** EpcCopyData efficiently copies blocks of data from consecutive memory locations to consecutive memory locations using the attributes of pointers *Source\_Ptr* and *Dest\_Ptr*. The intended use of the function is copying large blocks of data to or from consecutive bus locations.

The Size parameter should always express the number of bytes to be copied, regardless of the specified Data\_Width parameter. Passing a zero Size parameter results in no data being copied.

The following constants define valid values for the Data\_Width parameter:

| <u>Constant</u> | Description  |
|-----------------|--|
| EPC_8_BIT       | 8-bit data width   |
| EPC_8_BIT_ODD   | 8-bit data width, odd bytes only   |
| EPC_16_BIT      | 16-bit data width  |
| EPC_32_BIT      | 32-bit data width  |
| EPC_FASTCOPY    | To increase copy performance, don't check<br>for intermediate bus errors. This constant<br>cannot be used alone; it must be OR'd<br>with one of the preceding constants. |

The function returns the actual number of bytes copied in the location pointed to by Actual\_Size\_Ptr.

The function operates correctly using both unmapped pointers and memory mapped pointers for *Source\_Ptr* and *Dest\_Ptr*. EPC-to-EPC, EPC-to-VME, VME-to-EPC, and VME-to-VME copies all execute properly.

For a 16-bit or 32-bit copy to complete, no individual data element may span a segment boundary. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if *Data\_Width* is EPC\_16\_BIT and *Size* is greater than 64 Kbytes, both *Source\_Ptr* and *Dest\_Ptr* must be aligned on a 16-bit boundary for the copy operation to complete successfully

For a VME-to-VME copy to complete, both *Source\_Ptr* and *Dest\_Ptr* must correspond to VMEbus addresses aligned on an address boundary equivalent to the specified *Data\_Width*. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if both *Source\_Ptr* and *Dest\_Ptr* correspond to VMEbus memory and *Data\_Width* is EPC\_16\_BIT, then both *Source\_Ptr* and *Dest\_Ptr* must correspond to VMEbus addresses aligned on a 16-bit boundary for the copy to complete successfully.

For EPC-to-VME, VME-to-EPC, and VME-to-VME copies to complete when hardware byte-swapping occurs, *Size* must be a multiple of the specified *Data\_Width* and all VMEbus addresses must be aligned on an address boundary equivalent to the specified *Data\_Width*. Otherwise, the function returns an EPC\_INV\_SWAP error. For example, if *Source\_Ptr* corresponds to EPC memory, *Dest\_Ptr* corresponds to VMEbus memory, and *Data\_Width* is EPC\_16\_BIT, *Size* must be a multiple of two and *Dest\_Ptr* must correspond to a VMEbus address aligned on a 16-bit boundary for the copy to complete successfully.

To ensure that all accesses are the specified *Data\_Width*, the function handles non-aligned leading and trailing bytes as a special case. When transferring data from a non-aligned address, the function reads the nearest aligned chunk and extracts the non-aligned bytes. When transferring data to a non-aligned address, the function reads the nearest aligned chunk, copies the non-aligned bytes into the chunk, and replaces the chunk. Note that, for VMEbus transfers, this read-modify-write algorithm is executed in software -- it is not a read-modify-write bus cycle.

| Return Value | The function returns a Bus Management return value:                   |  |
|--------------|---|--|
|              | EPC_BERR  | A bus error occurred during the copy.  |
|              | EPC_INV_ALIGN   | A 16-bit or 32-bit data element spans<br>a segment boundary or both<br>Source_Ptr and Dest_Ptr are mapped<br>to VMEbus addresses and they are not<br>aligned on equivalent VMEbus<br>address boundaries. |
|              | EPC_INV_PTR   | One or more of <i>Source_Ptr</i> , <i>Dest_Ptr</i> , or <i>Actual_Size_Ptr</i> is invalid.   |
|              | EPC_INV_RANGE   | The address range defined by<br>Source_Ptr and Size and/or the<br>address range defined by Dest_Ptr<br>and Size contains bus addresses that<br>are not currently mapped.                                 |
|              | EPC_INV_SESSION   | The specified Session_ID is invalid.   |
|              | EPC_INV_SW  | The BusManager device driver is not present.   |
|              | EPC_INV_SWAP  | Source_Ptr and/or Dest_Ptr are<br>mapped to the VMEbus so that<br>hardware byte-swapping will occur,<br>but Size is not a multiple of<br>Data_Width and/or a VMEbus<br>address is misaligned.            |
|              | EPC_INV_WIDTH   | The Data_Width parameter is invalid.   |
|              | EPC_LOCKED  | Shared interface hardware is locked by another session.  |
|              | EPC_SUCCESS   | The function completed successfully.   |
| See Also     | EpcGetMappingAttribute<br>EpcMapSharedMemory,<br>EpcUnmapBusMemory, F |  |

### Example

```
    Copyright 1994 by RadiSys Corporation. All rights reserved.

 * /
/*
 * mapping.c -- Bus Management Library mapping functions sample code.
 */
#include "busmgr.h"
1*
 • FUNCTION PROTOTYPES...
 * /
short FAR
MappingSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
/*
 * CODE...
 */
short FAR
MappingSample(void)
{
    char
                           err_string[ERROR_STRING_SZ];
    short
                           err_num;
    unsigned short
                           bus_add_mod;
                           bus_byte_order;
    unsigned short
    unsigned short
                           ula;
    unsigned long
                           actual_size;
    unsigned long
unsigned long
                           bus_base;
                           bus_size;
    unsigned long
                           session_id;
    unsigned long
                           shared_base;
    unsigned long
                           shared_size;
    volatile void
                   HUGE *bus_ptr;
HUGE *shared_ptr;
    volatile void
    struct EpcEnvironment environment;
     * Verify the EPConnect environment.
    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    {
         EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                   err_string,
                   err_num);
        return (err_num);
    }
      * Open a session.
    if ((err_num = EpcOpenSession(&session_id)) != EPC_SUCCESS)
    {
         EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
```

2

err string,

```
2
```

```
err_num);
    return (err_num);
}
• Map all of Al6 space using Motorola byte ordering.
 */
if ((err_num = EpcMapBusMemory(session_id,
                                EPC_A16S,
                                EPC_MBO,
                                0x00000000,
                                0x00010000,
                               &bus_ptr)) != EPC_SUCCESS)
(
    EpcCloseSession(session_id);
    EpcGetErrorString(err_num, err_string);
    WinPrintf("FAILURE: EpcMapBusMemory() error == %s (%d).\n",
              err_string,
              err_num);
    return (err_num);
}
1.
• Query the bus mapping's attributes.
EpcGetMappingAttributes(session_id,
                        bus_ptr,
                        &bus_add_mod,
                        &bus_byte_order,
                        &bus_base,
                        &bus_size);
 • Map the EPC's shared memory buffer.
if ((err_num = EpcMapSharedMemory(session_id,
                                   &shared_base,
                                   &shared_size,
                                   &shared_ptr)) != EPC_SUCCESS)
{
    EpcCloseSession(session_id);
    EpcGetErrorString(err_num, err_string);
    WinPrintf("FAILURE: EpcMapSharedMemory() error == %s (%d).\n",
              err_string,
              err_num);
    return (err_num);
}
1.
 * Copy the EPC's A16 registers to the shared memory buffer in Motorola
 • byte order.
 •/
EpcGetULA(session_id, &ula);
EpcCopyData(session_id,
            (void HUGE *) ((char HUGE *) bus_ptr + 0xC000 + (ula << 6)),
            (void HUGE *) shared_ptr,
            0x00000040,
            EPC_16_BIT,
            &actual_size);
```

```
/*
 * Unmap A16 space.
 */
EpcUnmapBusMemory(session_id, bus_ptr);
/*
 * Unmap the shared memory buffer.
 */
EpcUnmapSharedMemory(session_id, shared_ptr);
/*
 * Close the session and return.
 */
EpcCloseSession(session_id);
WinPrintf("SUCCESS: MappingSample() complete.\n");
return (EPC_SUCCESS);
```

}

### EpcDeassertInterrupt

**Description** Deasserts a VME interrupt.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcDeassertInterrupt(unsigned long Session\_ID);

Session\_ID Session\_ID specifies a session.

Visual Basic Synopsis

Declare Function EpcDeassertInterrupt% Lib "bmvxiw16.dll" (ByVal Session\_ID&)

**Remarks** EpcDeassertInterrupt deasserts a currently asserted VME interrupt. If the interface is not currently asserting a VME interrupt, the function has no effect.

When an asserted VME interrupt is acknowledged by a device on the bus, it is automatically deasserted. No call to EpcDeassertInterrupt is necessary to deassert the VME interrupt.

### Warning:

Deasserting a VME interrupt without waiting for interrupt acknowledgment may cause certain hardware configurations to "lock up." Deasserting a VME interrupt should be executed with extreme care.

**Return Value** The function returns a Bus Management return value:

|          | EPC_INV_SESSION                         | The specified Session_ID is invalid.                    |
|----------|---|---|
|          | EPC_INV_SW                              | The BusManager device driver is not present.            |
|          | EPC_LOCKED                              | Shared interface hardware is locked by another session. |
|          | EPC_SUCCESS                             | The function completed successfully.                    |
| See Also | EpcAssertInterrupt, EpcGetEpcInterrupt. |   |

**Example** See EpcAssertInterrupt.

### **EpcGetBusAttributes**

Description Oueries the interface's bus management attributes. C Synopsis #include "busmgr.h" short FAR PASCAL **EpcGetBusAttributes** (unsigned long Session ID. unsigned short FAR \* Bus\_Enable\_Ptr, unsigned short FAR \* Bus Arb Mode Ptr. unsigned short FAR \* Bus\_Arb\_Priority\_Ptr, unsigned short FAR \* Bus\_Release\_Ptr); Session\_ID Session\_ID specifies a session. Bus\_Enable\_Ptr Bus\_Enable\_Ptr specifies a location where the interface's bus enable attribute will be placed. Bus\_Arb\_Mode\_Ptr Bus\_Arb\_Mode\_Ptr specifies a location where the interface's bus arbitration mode attribute will be placed. Bus\_Arb\_Priority\_Ptr specifies a Bus\_Arb\_Priority\_Ptr location where the interface's bus arbitration priority attribute will be placed. Bus\_Release\_Ptr Bus\_Release\_Ptr specifies a location where the interface's bus release mode attribute will be placed.

Visual Basic Synopsis

Declare Function

EpcGetBusAttributes% Lib "bmvxiw16.dll"

(ByVal Session\_ID&, Bus\_Enable\_Ptr%, Bus\_Arb\_Mode\_Ptr%, Bus\_Arb\_Priority\_Ptr%, Bus\_Release\_Ptr%)

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**Remarks** EpcGetBusAttributes queries the interface's bus management attributes and places them in the locations pointed to by *Bus\_Enable\_Ptr*, *Bus\_Arb\_Mode\_Ptr*, *Bus\_Arb\_Priority\_Ptr*, and *Bus\_Release\_Ptr*.

> The interface's bus enable attribute defines whether accesses made by the interface reach the bus. Possible values placed at *Bus\_Enable\_Ptr* are:

| <u>*Bus_Enable_Ptr</u> | Description  |
|------------------------|--|
| EPC_DISABLE_BUS        | Disable bus accesses for the interface. (EPC-7 and EPC-8 only) |
| EPC_ENABLE_BUS         | Enable bus accesses for the interface.                         |

The interface's bus arbitration mode defines how the interface arbitrates bus collisions. The value placed at *Bus\_Arb\_Mode\_Ptr* only has meaning if the interface has been designated the VXIbus slot-0 controller. Possible values placed at *Bus\_Arb\_Mode\_Ptr* are:

| <u>*Bus_Arb_Mode_Ptr</u> | <b>Description</b>           |
|--------------------------|------------------------------|
| EPC_PRIORITY             | Priority bus arbitration.    |
| EPC_ROUND_ROBIN          | Round-robin bus arbitration. |

The interface's bus arbitration priority defines the priority level at which the interface arbitrates for the bus. Possible values placed at *Bus\_Arb\_Priority\_Ptr* are:

| <u>*Bus_Arb_Priority_Ptr</u> | <b>Description</b>          |
|------------------------------|-----------------------------|
| EPC_PRIORITY0                | Bus arbitration priority 0. |
| EPC_PRIORITY1                | Bus arbitration priority 1. |
| EPC_PRIORITY2                | Bus arbitration priority 2. |
| EPC_PRIORITY3                | Bus arbitration priority 3. |

The interface's bus release mode determines when the interface requests and/or releases the bus. Possible values placed at *Bus\_Release\_Ptr* are:

| *Bus_Release_Ptr | Description                               |
|------------------|---|
| EPC_ROR          | "Release On Request" bus release mode.    |
| EPC_RONR         | "Release On No Request" bus release mode. |

Return Value The function returns a Bus Management return value:

| EPC_INV_PTR     | One or more of the parameters<br>Bus_Enable_Ptr, Bus_Arb_Mode_Ptr,<br>Bus_Arb_Priority_Ptr, and<br>Bus_Release_Ptr is invalid. |
|-----------------|--|
| EPC_INV_SESSION | The parameter Session_ID is invalid.   |
| EPC_INV_SW      | The BusManager device driver is not present.   |
| EPC_SUCCESS     | The function completed successfully.   |

See Also EpcOpenSession, EpcSetBusAttributes.

### Example

```
/*
 * Copyright 1994 by RadiSys Corporation. All rights reserved.
 */
 * epccfg.c -- Bus Management Library interface configuration functions sample
code.
 */
 *include *busmgr.h*
/*
 * FUNCTION PROTOTYPES...
 */
short FAR
EpcCfgSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
/*
 * CODE...
```

{

```
*/
short FAR
EpcCfgSample(void)
    char
                          err_string[ERROR_STRING_SZ];
    short
                          err_num;
    unsigned short
                          bus_enable;
    unsigned short
                          bus_arb_mode;
    unsigned short
                          bus_arb_priority;
    unsigned short
                          bus release:
    unsigned short
                          slave_space;
    unsigned short
                          ula;
    unsigned long
                          misc mask:
    unsigned long
                           session_id;
    unsigned long
                          slave_base;
    struct EpcEnvironment environment;
     • Verify the EPConnect environment.
     * /
    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    {
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
     • Open a session.
     * /
    if ((err_num = EpcOpenSession(&session_id)) != EPC_SUCCESS)
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
     * Query the interface's current configuration settings.
    EpcGetBusAttributes(session_id,
                         &bus_enable,
                         &bus_arb_mode,
                         &bus_arb_priority,
                         &bus_release);
    EpcGetSlaveMapping(session_id, &slave_space, &slave_base);
    EpcGetULA(session_id, &ula);
    EpcGetMiscAttributes(session_id, &misc_mask);
     /*

    Define the interface's configuration settings.

     * /
    EpcSetBusAttributes(session_id,
                         EPC_ENABLE_BUS,
                         EPC_PRIORITY,
                         EPC_PRIORITYO,
```

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# 2

```
EPC_ROR);
EpcSetSlaveMapping(session_id, EPC_A24, 0x00400000);
EpcSetULA(session_id, 0xF8);
EpcSetMiscAttributes(session_id, EPC_PASS | EPC_READY);
 * Restore the interface's original configuration settings.
 */
EpcSetBusAttributes(session_id,
                    bus_enable,
                    bus_arb_mode,
                    bus_arb_priority,
                    bus_release);
EpcSetSlaveMapping(session_id, slave_space, slave_base);
EpcSetULA(session_id, ula);
EpcSetMiscAttributes(session_id, misc_mask);
1*
 • Close the session and return.
 */
EpcCloseSession(session_id);
WinPrintf("SUCCESS: EpcCfgSample() complete.\n");
return (EPC_SUCCESS);
```

}

#### **EpcGetBusInterrupts**

**Description** Queries actual bus VME interrupt line state.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetBusInterrupts( unsigned long Session\_ID, unsigned long FAR \* Event\_Mask\_Ptr);

| Session_ID     | Session_ID specifies a session.  |
|----------------|--|
| Event_Mask_Ptr | <i>Event_Mask_Ptr</i> specifies a location where the actual bus VME interrupt line state will be placed. |

Visual Basic Synopsis

Declare Function EpcGetBusAttributes% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Event\_Mask\_Ptr&)

**Remarks EpcGetBusInterrupts** queries the actual bus VME interrupt line state and places it in the location pointed to by *Event\_Mask\_Ptr*.

The value pointed to by *Event\_Mask\_Ptr* is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding actual bus VME interrupt line is asserted. A clear bit indicates that the corresponding actual bus VME interrupt line is deasserted:

| <u>Constant</u> | Description      |
|-----------------|------------------|
| EPC_VME1_INT    | VME interrupt 1. |
| •••             |                  |
| EPC_VME7_INT    | VME interrupt 7. |

## Bus Management for Windows Programmer's Reference

|   | Return Value | The function returns a Bus Management return value:                              |   |
|---|--------------|--|---|
|   |              | EPC_INV_PTR  | The parameter <i>Event_Mask_Ptr</i> is invalid. |
| 2 |              | EPC_INV_SESSION  | The specified Session_ID is invalid.            |
|   |              | EPC_INV_SW   | The BusManager device driver is not present.    |
|   |              | EPC_SUCCESS  | The function completed successfully.            |
|   | See Also     | EpcAssertInterrupt, EpcDeassertInterrupt,<br>EpcGetEpcInterrupt, EpcOpenSession. |   |

**Example** See EpcAssertInterrupt.

# EpcGetBusLines

| Description      | Queries actual bus control line state.  |   |
|------------------|---|---|
| C Synopsis       |   |   |
|                  | #include ''busmgr.h''   |   |
|                  | short FAR PASCAL<br>EpcGetBusLines( unsigned long Session_ID,<br>unsigned long FAR * Line_Mask_Ptr);  |   |
|                  | Session_ID  | Session_ID specifies a session.   |
|                  | Line_Mask_Ptr   | <i>Line_Mask_Ptr</i> specifies a location where the actual bus control line state will be placed. |
| Visual Basic Syn | -   |   |
|                  | Declare Function<br>EpcGetBusLines% Lib "bmvxiw16.dll"  |   |
|                  |   | ByVal Session_ID&, Line_Mask_Ptr&)  |
| Remarks          | <b>EpcGetBusLines</b> queries the actual bus control line state and places it in the location pointed to by <i>Line_Mask_Ptr</i> .  |   |
|                  | The value pointed to by <i>Line_Mask_Ptr</i> is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding actual bus control line is asserted. A clear bit indicates that the corresponding actual bus control line is deasserted: |   |
|                  | <u>Constant</u>   | Description   |
|                  | EPC_ACFAIL  | ACFAIL.   |
|                  | EPC_SYSFAIL   | SYSFAIL.  |
|                  | EPC_SYSRESET  | SYSRESET. Supported on EPC-7  |

SYSRESET. Supported on EPC-7 and VXLink only.

The value pointed to by Line\_Mask\_Ptr reflects the actual bus control line state, not the interface control line state. Use EpcGetEpcLines to query the interface control line state.

| Return Value | The function returns a Bus Management return value:                  |  |
|--------------|--|--|
|              | EPC_INV_PTR  | The parameter <i>Line_Mask_Ptr</i> is invalid. |
|              | EPC_INV_SESSION  | The specified Session_ID is invalid.           |
|              | EPC_INV_SW   | The BusManager device driver is not present.   |
|              | EPC_SUCCESS  | The function completed successfully.           |
| See Also     | EpcGetEpcLines, EpcOpenSession, EpcPulseEpcLines,<br>EpcSetEpcLines. |  |
| Example      | See EpcAssertInterrup  | t.   |

#### EpcGetBusMODID

**Description** Queries the actual bus MODID line state.

C Synopsis

#include "busmgr.h"

short EpcGetBusMODID( unsigned long Session\_ID, unsigned long FAR \* MODID Mask Ptr);

| Session_ID     | Session_ID specifies a session.  |
|----------------|--|
| MODID_Mask_Ptr | <i>MODID_Mask_Ptr</i> specifies a location where the actual bus MODID line state will be placed. |

Visual Basic Synopsis

Declare Function EpcGetBusMODID%Lib"bmvxiw16.dll" (ByVal Session\_ID&,MODID\_Mask\_Ptr&)

**Remarks** EpcGetBusMODID queries the actual bus MODID line state and places it in the location pointed to by *MODID\_Mask\_Ptr*.

The value pointed to by *MODID\_Mask\_Ptr* is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding actual bus MODID line is asserted.

A clear bit indicates that the corresponding actual bus MODID line is deasserted:

ConstantDescriptionEPC\_SLOT\_MODIDMODID line for the interface's bus slot.

The value pointed to by *MODID\_Mask\_Ptr* reflects the actual bus MODID line state, not the interface MODID line state. Use **EpcGetEpcMODID** to query the interface MODID line state.

A device can always query the state of the MODID bus control line corresponding to its bus slot. The \*MODID\_Mask\_Ptr state bit EPC\_SLOT\_MODID always contains valid data.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_PTR     | The parameter <i>MODID_Mask_Ptr</i> is invalid. |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.            |
| EPC_INV_SW      | The Bus Manager device driver is not present.   |
| EPC_SUCCESS     | The function completed successfully.            |

See Also EpcGetEpcMODID, EpcOpenSession, EpcSetEpcMODID.

# **EpcGetBusTriggers**

**Description** Queries the actual bus trigger line state.

C Synopsis

#include "busmgr.h"

| Session_ID       | Session_ID specifies a session.  |
|------------------|--|
| Trigger_Mask_Ptr | <i>Trigger_Mask_Ptr</i> specifies a location where the actual bus trigger line state will be placed. |

Visual Basic Synopsis

Declare Function EpcGetBusTriggers% Lib "bmvxiw16.dll" (ByVal Session\_ID&,Trigger\_Mask\_Ptr&)

**Remarks EpcGetBusTriggers** queries the actual bus trigger line state and places it in the location pointed to by *Trigger\_Mask\_Ptr*.

The value pointed to by *Trigger\_Mask\_Ptr* is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding actual bus trigger line is asserted.

A clear bit indicates that the corresponding actual bus trigger line is deasserted:

| <u>Constant</u> | Description                            |  |
|-----------------|--|--|
| EPC_ECL_TRIG0   | ECL trigger 0 (EPC-7 only).            |  |
| EPC_ECL_TRIG1   | ECL trigger 1 (EPC-7 only).            |  |
| EPC_TTL_TRIG0   | TTL trigger 0 (EPC-7 and VXLink only). |  |
| •••             |  |  |
| EPC_TTL_TRIG7   | TTL trigger 7 (EPC-7 and VXLink only). |  |

The value pointed to by *Trigger\_Mask\_Ptr* reflects the actual bus trigger line state, not the interface trigger line state. Use **EpcGetEpcTriggers** to query the interface trigger line state.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_PTR         | The parameter <i>Trigger_Mask_Ptr</i> is invalid. |
|---------------------|---|
| EPC_INV_SESSION     | The specified Session_ID is invalid.              |
| EPC_INV_SW          | The Bus Manager device driver is not present.     |
| EPC_SUCCESS         | The function completed successfully.              |
| EngCotEngTriggorg E | noOpenSession EneDulesEneTriggers                 |

See Also EpcGetEpcTriggers, EpcOpenSession, EpcPulseEpcTriggers, EpcSetEpcTriggers.

#### EpcGetEpcInterrupt

**Description** Queries the interface VME interrupt assertion state.

C Synopsis

#include "busmgr.h"

| short FAR PASCAL    |                     |                  |
|---------------------|---------------------|------------------|
| EpcGetEpcInterrupt( | unsigned long       | Session_ID,      |
|                     | unsigned long FAR * | Event_Mask_Ptr); |

| Session_ID     | Session_ID specifies a session.   |
|----------------|---|
| Event_Mask_Ptr | <i>Event_Mask_Ptr</i> specifies a location where the currently asserted VME interrupt will be placed. |

Visual Basic Synopsis

Declare Function EpcGetEpcInterrupt% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Event\_Mask\_Ptr&)

**Remarks** EpcGetEpcInterrupt queries the VME interrupt currently asserted by the interface and places a it in the location pointed to by *Event\_Mask\_Ptr*.

The function places a constant at *Event\_Mask\_Ptr* specifying the VME interrupt currently asserted by the interface. Possible values are:

| <u>Constant</u> | Description   |
|-----------------|---|
| EPC_NO_INT      | The interface is not currently asserting a VME interrupt. |
| EPC_VME1_INT    | The interface is currently asserting VME interrupt 1.     |

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|              | EPC_VME7_INT  | The interface is currently asserting VME interrupt 7. |
|--------------|---|---|
| Return Value | The function returns a Bus Management return value:               |   |
|              | EPC_INV_PTR   | The parameter <i>Event_Mask_Ptr</i> is invalid.       |
|              | EPC_INV_SESSION   | The specified Session_ID is invalid.                  |
|              | EPC_INV_SW  | The BusManager device driver is not present.          |
|              | EPC_SUCCESS   | The function completed successfully.                  |
| See Also     | EpcAssertInterrupt, EpcDeassertInterrupt,<br>EpcGetBusInterrupts. |   |
| Example      | See EpcAssertInterrup   | t.  |

#### **EpcGetEpcLines**

**Description** Queries the interface control line state.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetEpcLines(unsigned long Session\_ID, unsigned long FAR \* Line\_Mask\_Ptr);

| Session_ID    | Session_ID specifies a session.  |
|---------------|--|
| Line_Mask_Ptr | <i>Line_Mask_Ptr</i> specifies a location where the interface control line state will be placed. |

Visual Basic Synopsis

Declare Function **EpcGetEpcLines%** Lib "bmvxiw16.dll" (ByVal Session\_ID&, Line\_Mask\_Ptr&)

**Remarks** EpcGetEpcLines queries the interface control line state and places it in the location pointed to by *Line\_Mask\_Ptr*.

The value pointed to by *Line\_Mask\_Ptr* is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding interface control line is asserted. A clear bit indicates that the corresponding interface control line is deasserted:

| <u>Constant</u> | <b>Description</b> |
|-----------------|--------------------|
| EPC_SYSFAIL     | SYSFAIL.           |
| EPC_SYSRESET    | SYSRESET.          |

The value pointed to by *Line\_Mask\_Ptr* reflects the interface control line state, not the actual bus control line state. Use **EpcGetBusLines** to query the actual bus control line state.

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| Return Value | The function returns a Bus Management return value:                  |  |
|--------------|--|--|
|              | EPC_INV_PTR  | The parameter <i>Line_Mask_Ptr</i> is invalid. |
|              | EPC_INV_SESSION  | The specified Session_ID is invalid.           |
|              | EPC_INV_SW   | The BusManager device driver is not present.   |
|              | EPC_SUCCESS  | The function completed successfully.           |
| See Also     | EpcGetBusLines, EpcOpenSession, EpcPulseEpcLines,<br>EpcSetEpcLines. |  |
| Example      | See EpcAssertInterrupt.  |  |

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### EpcGetEpcMODID

**Description** Queries the interface MODID line state.

C Synopsis

#include "busmgr.h"

Session\_ID

Session\_ID specifies a session.

MODID\_Mask\_Ptr

*MODID\_Mask\_Ptr* specifies a location where the interface MODID line state will be placed.

Visual Basic Synopsis

Declare Function EpcGetEpcMODID% Lib "bmvxiw16.dll" (ByVal Session\_ID&,MODID\_Mask\_Ptr&)

**Remarks** EpcGetEpcMODID queries the interface MODID line state and places it in the location pointed to by *MODID\_Mask\_Ptr*.

The value pointed to by *MODID\_Mask\_Ptr* is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding interface MODID line is asserted. A clear bit indicates that the corresponding interface MODID line is deasserted:

| <u>Constant</u> | Description                            |
|-----------------|--|
| EPC_MODID0      | MODID line 0 (EPC-7 and VXLink only).  |
| •••             |  |
| EPC_MODID12     | MODID line 12 (EPC-7 and VXLink only). |

The value pointed to by *MODID\_Mask\_Ptr* reflects the interface MODID line state, not the actual bus MODID line state. Use **EpcGetBusMODID** to query the actual bus MODID line state.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_PTR     | The parameter <i>MODID_Mask_Ptr</i> is invalid. |
|-----------------|---|
| EPC_INV_SESSION | The specified <i>Session_ID</i> is invalid.     |
| EPC_INV_SW      | The Bus Manager device driver is not present.   |
| EPC_SUCCESS     | The function completed successfully.            |

See Also EpcGetBusMODID, EpcOpenSession, EpcSetEpcMODID.

# **EpcGetEpcTriggerMapping**

**Description** Queries an interface trigger line mapping.

C Synopsis

#include "busmgr.h"

Session\_ID

In\_Trigger\_Mask

Session\_ID specifies a session.

*In\_Trigger\_Mask* specifies an interface trigger line.

Out\_Trigger\_Mask\_Ptr Out\_Trigger\_Mask\_Ptr specifies a location where a mask of interface trigger lines will be placed.

Visual Basic Synopsis

Declare Function EpcGetEpcTriggerMapping% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal In\_Trigger\_Mask&, Out\_Trigger\_Mask\_Ptr&)

**Remarks** EpcGetEpcTriggerMapping queries the interface trigger lines mapped to the specified *In\_Trigger\_Mask* and places a mask identifying them in the location pointed to by *Out\_Trigger\_Mask\_Ptr*.

The parameter *In\_Trigger\_Mask* is a constant specifying a single interface trigger line. The value placed at the location pointed to by *Out\_Trigger\_Mask\_Ptr* is an OR'd mask of constants identifying the interface trigger lines mapped to the specified input interface trigger line. The table below enumerates possible trigger mapping combinations for an EPC-7 interface:

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| <u>In Trigger Mask</u>             | *Out_Trigger_Mask_Ptr              | Description   |
|------------------------------------|------------------------------------|---|
| EPC_EXT_TRIG0                      | EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | External<br>trigger 0<br>mapped as<br>input to a<br>single TTL<br>trigger line. |
| EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | EPC_EXT_TRIG0                      | A single TTL<br>trigger line<br>mapped as<br>input to<br>external<br>trigger 0. |

The table below enumerates possible trigger mapping combinations for a VXLink interface:

| <u>In Trigger Mask</u>             | <u>*Out_Trigger_Mask_Ptr</u>       | <b>Description</b>  |
|------------------------------------|------------------------------------|---|
| EPC_EXT_TRIG0                      | 0x0000000                          | External<br>trigger 0<br>unmapped.  |
| EPC_EXT_TRIG0                      | EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | External<br>trigger 0<br>mapped as<br>input to a<br>single TTL<br>trigger line. |
| EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | EPC_EXT_TRIG1                      | A single TTL<br>trigger line<br>mapped as<br>input to<br>external<br>trigger 1. |

When an external trigger line is mapped as input to one or more interface trigger lines, asserting the external trigger line asserts all of the mapped interface trigger lines. Likewise, deasserting the external trigger line deasserts all of the mapped interface trigger lines.

When one or more interface trigger lines are mapped as input to an external trigger line, asserting one of the interface trigger lines asserts the mapped external trigger line. Likewise, deasserting one of the interface trigger lines deasserts the mapped external trigger line.

An EPC-7 interface provides a single bi-directional external trigger. The external trigger is always mapped; it cannot be unmapped. Specifying a mapping for external trigger 0 overrides the previous mapping. By default, TTL trigger 1 is mapped as an output to external trigger 0.

A VXLink interface provides two unidirectional external triggers. External trigger 0 is an input-only trigger and external trigger 1 is an output-only trigger. The external triggers can be independently mapped or unmapped. By default, both external triggers are unmapped.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_MASK    | The parameter In_Trigger_Mask is invalid.      |
|-----------------|--|
| EPC_INV_PTR     | The parameter Out_Trigger_Mask_Ptr is invalid. |
| EPC_INV_SESSION | The specified Session_ID is invalid.           |
| EPC_INV_SW      | The Bus Manager device driver is not present.  |
| EPC_SUCCESS     | The function completed successfully.           |

See Also EpcMapEpcTriggers, EpcOpenSession.

# **EpcGetEpcTriggers**

**Description** Query the interface trigger line state.

C Synopsis

#include "busmgr.h"

short EpcGetEpcTriggers

Trigger\_Mask\_Ptr

EpcGetEpcTriggers(unsigned long<br/>unsigned long FAR \* Trigger\_Mask\_Ptr);Session\_IDSession\_ID specifies a session.

*Trigger\_Mask\_Ptr* specifies a location where the EPC trigger line state will be placed.

Visual Basic Synopsis

Declare Function EpcGetEpcTriggers% Lib "bmvxiw16.dll" (ByVal Session\_ID&,Trigger\_Mask\_Ptr&)

**Remarks** EpcGetEpcTriggers queries the interface trigger line state and places it in the location pointed to by *Trigger\_Mask\_Ptr*.

The value pointed to by *Trigger\_Mask\_Ptr* is either zero or an OR'd mask of the following constants. A set bit indicates that the corresponding interface trigger line is asserted.

A clear bit indicates that the corresponding interface trigger line is deasserted:

| <u>Constant</u> | Description                           |
|-----------------|---------------------------------------|
| EPC_ECL_TRIG0   | ECL trigger 0 (EPC-7 only)            |
| EPC_ECL_TRIG1   | ECL trigger 1 (EPC-7 only)            |
| EPC_TTL_TRIG0   | TTL trigger 0 (EPC-7 and VXLink only) |
|                 |                                       |
| EPC_TTL_TRIG7   | TTL trigger 7 (EPC-7 and VXLink only) |

The value pointed to by *Trigger\_Mask\_Ptr* reflects the interface trigger line state, not the actual bus trigger line state. Use **EpcGetBusTriggers** to query the actual bus trigger line state.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_PTR     | The parameter <i>Trigger_Mask_Ptr</i> is invalid. |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.              |
| EPC_INV_SW      | The Bus Manager device driver is not present.     |
| EPC_SUCCESS     | The function completed successfully.              |
|                 |   |

See Also EpcGetBusTriggers, EpcOpenSession, EpcPulseEpcTriggers, EpcSetEpcTriggers.

# **EpcGetErrorString**

**Description** Queries a null-terminated string corresponding to a Bus Management return value.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetErrorString( short Return\_Value char FAR\* Buffer\_Ptr);

| Return_Value | <i>Return_Value</i> specifies a Bus Management return value.  |
|--------------|---|
| Buffer_Ptr   | <i>Buffer_Ptr</i> specifies the location of a buffer where the null-terminated string will be placed. |

Visual Basic Synopsis

Declare Function EpcGetErrorString% Lib "bmvxiw16.dll" (ByVal Return\_Value&, Buffer\_Ptr&)

**Remarks** EpcGetErrorString places a null-terminated ASCII character string describing a Bus Management return value in the buffer pointed to by *Buffer\_Ptr*.

*Return\_Value* specifies a Bus Management return value. Specifying an invalid value results in the function returning a pointer to the string "Unknown EPConnect Return Value".

The buffer pointed to by *Buffer\_Ptr* must be at least ERROR\_STRING\_SZ bytes long.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR | The parameter <i>Buffer_Ptr</i> is invalid. |
|-------------|---|
| EPC_SUCCESS | The function completed successfully.        |

#### Example

```
• Copyright 1994 by RadiSys Corporation. All rights reserved.
*/
 • environ.c -- Bus Management Library environment functions sample code.
#include "busmgr.h"
 * FUNCTION PROTOTYPES...
*/
short FAR
EnvironmentSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
 • CODE...
 */
short FAR
EnvironmentSample(void)
{
                          err_string[ERROR_STRING_SZ];
    char
    short
                          err_num;
    struct EpcEnvironment environment;
     • Verify the EPConnect environment.
     */
    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    {
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    3
    WinPrintf("SUCCESS: EnvironmentSample() complete.\n");
    return (EPC_SUCCESS);
}
```

### EpcGetEventEnableMask

**Description** Queries a session's enabled event mask attribute.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetEventEnableMask

> (unsigned long Session\_ID, unsigned long FAR \* Event\_Mask\_Ptr);

 Session\_ID
 Session\_ID
 specifies a session.

 Event\_Mask\_Ptr
 Event\_Mask\_Ptr specifies a location where the enabled event mask attribute of the specified session will

be placed.

Visual Basic Synopsis

Declare Function EpcGetEventEnableMask% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Event\_Mask\_Ptr&)

**Remarks** EpcGetEventEnableMask places the specified session's enabled event mask attribute in the location pointed to by Event\_Mask\_Ptr.

> An enabled event mask attribute is a bit mask where each bit corresponds to an event. A zero in a bit position specifies that the corresponding event's reception is disabled. A one in a bit position specifies that the corresponding event's reception is enabled.

The mask is either zero or an OR'd combination of the following constants:

| Event             | Description   |
|-------------------|---|
| EPC_MSG_INT       | Message interrupt (EPC-7 and EPC-8 only)                  |
| EPC_VME1_INT      | VMEbus interrupt 1  |
|                   |   |
| EPC_VME7_INT      | VMEbus interrupt 7  |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                              |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0 interrupt<br>(EPC-7 only)            |
| •••               |   |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7 interrupt<br>(EPC-7 only)            |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                     |
| EPC_ACFAIL_ERR    | VMEbus power failure error                                |
| EPC_BERR_ERR      | VMEbus access error                                       |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                      |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration error<br>(EPC-7 and EPC-8 only) |
| EPC_EXT_TRIG0_INT | External trigger 0 interrupt<br>(VXLink only)             |
| EPC_EXT_TRIG1_INT | External trigger 1 interrupt<br>(VXLink only)             |

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| Return Value | The function returns a Bus Management return value: |   |  |
|--------------|---|---|--|
|              | EPC_INV_PTR   | The parameter <i>Event_Mask_Ptr</i> is invalid. |  |
|              | EPC_INV_SESSION                                     | The specified Session_ID is invalid.            |  |
|              | EPC_SUCCESS   | The function completed successfully.            |  |
|              | EPC_INV_SW  | The BusManager device driver is not present.    |  |

See Also EpcSetEventEnableMask, EpcOpenSession.

#### Example

```
• Copyright 1994 by RadiSys Corporation. All rights reserved.

    events.c -- Bus Management Library events functions sample code.

#include "busmgr.h"
 • CONSTANTS...
                                 4096
#define STACK_SIZE
FUNCTION PROTOTYPES...
 */
short FAR
EventsSample(void);
void FAR LOADDS
EventHandler(unsigned long Session_ID,
             unsigned long Event_Mask,
             unsigned long Event_Data);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
* GLOBAL DATA...
*/
unsigned char EventStack[STACK_SIZE] = { 0 };
• CODE...
 */
short FAR
EventsSample(void)
ſ
    char
                          err_string[ERROR_STRING_SZ];
    short
                          err_num;
    unsigned long
                          event_data;
    unsigned long
                          event_mask;
    unsigned long
                          session_id;
    void
                     FAR *event_stack;
    void
                     (FAR *event_handler) (unsigned long,
                                          unsigned long,
                                          unsigned long);
    struct EpcEnvironment environment;
    /*

    Verify the EPConnect environment.

    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    {
```

```
EpcGetErrorString(err_num, err_string);
       WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                 err_string,
                 err_num);
        return (err_num);
   }
     • Open a session.
    if ((err_num = EpcOpenSession(&session_id)) != EPC_SUCCESS)
    (
        EpcGetErrorString(err_num, err_string);
       WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
                 err_string,
                 err_num);
        return (err_num);
    }
     Define the session's event handler for VMEbus interrupt 1.
   EpcSetEventHandler(session_id,
                       EPC_VME1_INT,
                       (void (FAR *) (unsigned long,
                                     unsigned long,
                                     unsigned long)) EventHandler,
                       (void FAR *) &EventStack[STACK_SIZE]);
    * Define the session's event enable mask to enable VMEbus interrupt 1.
    EpcSetEventEnableMask(session_id, EPC_VME1_INT);
     • Query the session's event handler for VMEbus interrupt 1.
    EpcGetEventHandler(session_id, EPC_VME1_INT, &event_handler, &event_stack);

    Query the session's event enable mask.

    EpcGetEventEnableMask(session_id, &event_mask);
     * Wait up to one second (1000 ms) for VMEbus interrupt 1 to occur.
    EpcWaitForEvent(session_id, 1000, EPC_VME1_INT, &event_mask, &event_data);
     * Close the session and return.
    EpcCloseSession(session_id);
    WinPrintf("SUCCESS: EventsSample() complete.\n");
    return (EPC_SUCCESS);
void FAR LOADDS
```

}

# **EpcGetEventHandler**

**Description** Queries an entry in a session's event handler array.

C Synopsis

#include "busmgr.h"

#### short FAR PASCAL EpcGetEventHandler

| (unsigned long<br>unsigned long<br>void (FAR * FAR *<br>void FAR * FAR * | Session_ID,<br>Event_Mask,<br>Event_Handler_Ptr)(unsigned long,<br>unsigned long, unsigned long),<br>Stack_Ptr_Ptr);    |
|--|---|
| Session_ID   | Session_ID specifies a session.   |
| Event_Mask   | Event_Mask specifies an event.  |
| Event_Handler_Ptr  | <i>Event_Handler_Ptr</i> specifies a location where the specified session's specified event handler will be placed.     |
| Stack_Ptr_Ptr  | Stack_Ptr_Ptr specifies a location<br>where the specified session's<br>specified event handler stack will<br>be placed. |

Visual Basic Synopsis

#### Declare Function EpcGetEventHandler% Lib "bmvxiw16.dll"

(ByVal Session\_ID&, ByVal Event\_Mask&, Event\_Handler\_Ptr As Any, Stack\_Ptr\_Ptr As Any)

# **Remarks** EpcGetEventHandler places the specified session's specified event handler address and event handler stack pointer in the locations pointed to by *Event\_Handler\_Ptr* and *Stack\_Ptr\_Ptr*.

The *Event\_Mask* parameter is a bit mask where each bit corresponds to an event. The *Event\_Mask* parameter should be one of the following constants:

| Event             | Description   |
|-------------------|---|
| EPC_MSG_INT       | Message interrupt (EPC-7 and EPC-8 only)                  |
| EPC_VME1_INT      | VMEbus interrupt 1  |
|                   |   |
| EPC_VME7_INT      | VMEbus interrupt 7  |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                              |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0<br>interrupt (EPC-7 only)            |
|                   |   |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7<br>interrupt (EPC-7 only)            |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                     |
| EPC_ACFAIL_ERR    | VMEbus power failure error                                |
| EPC_BERR_ERR      | VMEbus access error                                       |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                      |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration<br>error (EPC-7 and EPC-8 only) |
| EPC_EXT_TRIG0_INT | External trigger 0 interrupt<br>(VXLink only)             |
| EPC_EXT_TRIG1_INT | External trigger 1 interrupt<br>(VXLink only)             |

If the session has no event handler defined for the specified event, the function places NULL in the locations pointed to by *Event\_Handler\_Ptr* and *Stack\_Ptr\_Ptr*.

# **Bus Management for Windows Programmer's Reference**

|   | Return Value | The function returns a Bus Management return value: |   |
|---|--------------|---|---|
| 2 |              | EPC_INV_MASK  | <i>Event_Mask</i> contains more than one event or contains an event that is not valid for this EPC. |
|   |              | EPC_INV_PTR   | One or both of the parameters<br><i>Event_Handler_Ptr</i> and <i>Stack_Ptr_Ptr</i> is invalid.      |
|   |              | EPC_INV_SESSION                                     | The specified Session_ID is invalid.  |
|   |              | EPC_SUCCESS   | The function completed successfully.  |
|   | See Also     | EpcSetEventHandler, E                               | pcOpenSession.  |
|   | Example      | See EpcGetEventEnable                               | Mask.   |

# **EpcGetLockingTimeout**

**Description** Queries a session's locking timeout.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetLockingTimeout( unsigned long Session\_1D, unsigned long FAR \* Timeout\_Ptr);

| Session_ID  | Session_ID specifies a session.   |
|-------------|---|
| Timeout_Ptr | <i>Timeout_Ptr</i> specifies a location where the specified session's locking timeout will be placed. |

Visual Basic Synopsis

Declare Function EpcGetLockingTimeout% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Timeout\_Ptr&)

**Remarks EpcGetLockingTimeout** queries the specified session's locking timeout and places it in the location pointed to by *Timeout\_Ptr*.

Upon successful function completion, *Timeout\_Ptr* contains the session's locking timeout, in milliseconds.

By default, a session has a locking timeout of zero milliseconds. When the session encounters a locking conflict, an EPC\_LOCKED error is returned immediately.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR     | The parameter <i>Timeout_Ptr</i> is invalid. |
|-----------------|--|
| EPC_INV_SESSION | The specified Session_ID is invalid.         |
| EPC_SUCCESS     | The function completed successfully.         |

See Also EpcLockSession, EpcOpenSession, EpcSetLockingTimeout.

#### Example

```
    /*
    Copyright 1994 by RadiSys Corporation. All rights reserved.

• locking.c -- Bus Management Library locking functions sample code.
#include "busmar.h"
• FUNCTION PROTOTYPES...
short FAR
LockingSample(void);
int FAR
WinPrintf(char FAR *Format Ptr, ...);
1 *
* CODE...
*/
short FAR
LockingSample(void)
{
    char
                          err_string [ERROR_STRING_SZ];
    short
                         err num:
   unsigned long
                        session_id1;
    unsigned long
                          session_id2;
    unsigned long
                          timeout;
    struct EpcEnvironment environment;
     • Verify the EPConnect environment.
    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    £
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
    /*
     • Open two sessions.
     */
    if ((err_num = EpcOpenSession(&session_id1)) != EPC_SUCCESS ||
        (err_num = EpcOpenSession(&session_id2)) != EPC_SUCCESS )
    {
        EpcCloseSession(session_id1);
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
    /*
```

```
* Define the second session's locking timeout to be one second (1000 ms).
 */
EpcSetLockingTimeout(session_id2, 1000);
 * Query the second session's locking timeout.
 */
EpcGetLockingTimeout(session_id2, &timeout);
 * Lock shared interface hardware.
 * NOTES:
 *
       1.
          The EpcLockSession() call for the second session fails after a
 *
           one second (1000 ms) timeout, since shared interface hardware is
 *
           already locked by the first session.
 */
EpcLockSession(session_id1);
EpcLockSession(session_id2);
 * Unlock shared interface hardware with both sessions.
 +/
EpcUnlockSession(session_id1);
 Close the sessions and return.
 +/
EpcCloseSession(session_id1);
EpcCloseSession(session_id2);
WinPrintf("SUCCESS: LockingSample() complete.\n");
return (EPC_SUCCESS);
```

}

# **EpcGetMappingAttributes**

Description Queries a memory mapping's attributes.

C Synopsis

#include "busmgr.h"

#### short FAR PASCAL **EpcGetMappingAttributes**

| (unsigned long<br>volatile void<br>unsigned short<br>unsigned short<br>unsigned long<br>unsigned long | HUGE *<br>FAR *<br>FAR *<br>FAR *<br>FAR * | Session_ID,<br>Mapped_Ptr,<br>Address_Mod_Ptr,<br>Byte_Ordering_Ptr,<br>Base_Address_Ptr,<br>Size_Ptr);                        |
|---|--|--|
| Session_ID  | Se   | ession_ID specifies a bus session.   |
| Mapped_Ptr  |  | <i>Tapped_Ptr</i> specifies a pointer to the ase of a memory mapping.  |
| Address_Mod_Ptr   | lo<br>at                                   | ddress_Mod_Ptr specifies a<br>scation where the address modifier<br>tribute of the specified memory<br>sapping will be placed. |
| Byte_Ordering_Ptr   | lo<br>at                                   | yte_Ordering_Ptr specifies a<br>location where the byte ordering<br>tribute of the specified memory<br>lapping will be placed. |
| Base_Address_Ptr  | lc<br>at                                   | ase_Address_Ptr specifies a<br>ocation where the base address<br>tribute of the specified memory<br>happing will be placed.    |
| Size_Ptr  | th<br>m                                    | <i>ize_Ptr</i> specifies a location where<br>he size attribute of the specified<br>hemory mapping, in bytes, will be<br>laced. |

Visual Basic Synopsis Declare Function

EpcGetMappingAttributes% Lib "bmvxiw16.dll"

(ByVal Session\_ID&, ByVal Mapped\_Ptr As Any, Address\_Mod\_Ptr%, Byte\_Ordering\_Ptr%, Base\_Address\_Ptr&, Size\_Ptr&)

**Remarks** EpcGetMappingAttributes places the specified memory mapping's attributes in the locations pointed to by Address\_Mod\_Ptr, Byte\_Ordering\_Ptr, Base\_Address\_Ptr, and Size\_Ptr, respectively.

The location pointed to by *Address\_Mod\_Ptr* can contain the following values:

| Constant   | Description  |
|------------|--|
| EPC_A16N   | VMEbus A16 non-supervisory address modifier.         |
| EPC_A16S   | VMEbus A16 supervisory address modifier.             |
| EPC_A24ND  | VMEbus A24 non-supervisory data address modifier.    |
| EPC_A24SD  | VMEbus A24 supervisory data address modifier.        |
| EPC_A24NP  | VMEbus A24 non-supervisory program address modifier. |
| EPC_A24SP  | VMEbus A24 supervisory program address modifier.     |
| EPC_A32ND  | VMEbus A32 non-supervisory data address modifier.    |
| EPC_A32SD  | VMEbus A32 supervisory data address modifier.        |
| EPC_A32NP  | VMEbus A32 non-supervisory program address modifier. |
| EPC_A32SP  | VMEbus A32 supervisory program address modifier.     |
| EPC_SHARED | Shared memory address modifier.                      |

The location pointed to by *Byte\_Ordering\_Ptr* can have the following values:

| <u>Constant</u> | Description                     |
|-----------------|---------------------------------|
| EPC_IBO         | Intel (80X86) byte ordering.    |
| EPC_MBO         | Motorola (68XXX) byte ordering. |

For shared memory mappings, the value in the location pointed to by *Byte\_Ordering\_Ptr* is always **EPC\_IBO**.

The values in the locations pointed to by *Base\_Address\_Ptr* and *Size\_Ptr* define a range of addresses  $\alpha$ , where:

\*Base\_Address\_Ptr <=  $\alpha$  <= \*Base\_Address\_Ptr + \*Size\_Ptr - 1;

For bus memory mappings, the value in the location pointed to by *Base\_Address\_Ptr* specifies a physical VMEbus address.

For shared memory mappings, the value in the location pointed to by *Base\_Address\_Ptr* specifies a physical PC address. To determine the corresponding physical VMEbus address, the value should be added to the base address of the interface's slave memory. Use **EpcGetSlaveMapping** to determine the base address of the interface's slave memory.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_MAP     | The specified <i>Mapped_Ptr</i> is invalid.   |
|-----------------|---|
| EPC_INV_PTR     | One or more of the parameters<br>Address_Mod_Ptr,<br>Byte_Ordering_Ptr,<br>Base_Address_Ptr, or Size_Ptr is<br>invalid. |
| EPC_INV_SESSION | The specified Session_ID is invalid.  |
| EPC_SUCCESS     | The function completed successfully.  |

- See Also EpcGetSlaveMapping, EpcMapBusMemory, EpcMapSharedMemory, EpcOpenSession.
- **Example** See EpcCopyData.

### EpcGetMiscAttributes

|   | Description      | Queries the interface's miscellaneous configuration attributes.   |          |  |  |
|---|------------------|---|----------|--|--|
| 2 | C Synopsis       | #include ''busmgr.h''   |          |  |  |
|   |                  | short FAR PASCAL<br>EpcGetMiscAttributes( unsigned long Session_ID,<br>unsigned long FAR * Misc_Mask_Ptr);  |          |  |  |
|   |                  | Session_ID  | Session_ | _ID specifies a sessio   | n.   |
|   |                  | Misc_Mask_Ptr   | interfac | lask_Ptr specifies a lo<br>e's miscellaneous<br>es will be placed.   | ocation where the configuration                                |
|   | Visual Basic Syn | Declare Function  |          | Lib "bmvxiw16.dll" (<br>_Ptr&)   | ByVal  |
|   | Remarks          | <b>EpcGetMiscAttributes</b> queries the interface's miscellaneous configuration attributes and places them in the location pointed to by <i>Misc_Mask_Ptr</i> .   |          |  |  |
|   |                  | The location pointed to by <i>Misc_Mask_Ptr</i> contains either a zero or<br>an OR'd bit mask of the following constants, where a set bit<br>indicates that the corresponding miscellaneous interface attribute is<br>asserted. |          |  |  |
|   |                  | A clear bit indicates that the corresponding miscellaneous interface attribute is deasserted:   |          |  |  |
|   |                  | Constant Description  |          |  |  |
|   |                  | EPC_DIR   |          | Word serial byte tra<br>bit. Asserting the b<br>interface is ready to<br>its commander devi<br>EPC-7 and EPC-8 c | it indicates that the<br>receive data from<br>ce. Supported on |

| EPC_DOR               | Word serial byte transfer protocol<br>DOR bit. Asserting the bit indicates<br>that the interface is ready to send data<br>to its commander device. Supported<br>on EPC-7 and EPC-8 only.   |
|-----------------------|--|
| EPC_ERR               | Word serial protocol ERR* bit.<br>Asserting the bit indicates to the<br>commander device that the interface<br>has detected a word serial protocol<br>error. Supported on EPC-7 and EPC-8<br>only.   |
| EPC_LOCK              | VXIbus message-based device<br>LOCKED* bit. Asserting the bit<br>indicates that the commander has<br>locked access to the interface from<br>local sources (IEEE-488 local lockout).<br>Supported on EPC-7 and EPC-8 only.                              |
| EPC_MULTIPLE_<br>LOCK | Word serial protocol extension<br>multiple commander lock bit. When<br>asserted, the first commander to read<br>the asserted bit from interface's<br>Response register can safely send a<br>word serial command. Supported on<br>EPC-7 and EPC-8 only. |
| EPC_PASS              | Device initialization PASSED bit.<br>Asserting the bit indicates that the<br>interface has passed self-test.   |
| EPC_PIPELINE_BUSY     | Bus hardware pipeline busy bit. When<br>asserted, the bit indicates that the<br>interface is executing a pipelined write<br>to the bus.  |
| EPC_READY             | Device initialization READY bit.<br>Asserting the bit indicates that the<br>interface is ready to begin normal<br>operation.   |

| EPC_RRDY                             | Word serial protocol Read Ready bit.<br>Asserting the bit indicates to a<br>commander device that the interface<br>has a word serial response in its<br>message register. Supported on EPC-7<br>and EPC-8 only. |
|--------------------------------------|---|
| EPC_RSRC_MGR                         | Resource manager execution bit.<br>Asserting the bit indicates that resource<br>manager execution is complete.  |
| EPC_STICKY_BERR                      | "Sticky" bus error bit. When asserted,<br>the bit indicates that a bus error has<br>occurred since the bit was last<br>deasserted.  |
| EPC_SYSFAIL_OUT                      | SYSFAIL output enable bit. When<br>asserted, the interface can assert<br>SYSFAIL. When deasserted, the<br>interface cannot assert SYSFAIL.  |
| EPC_SYSRESET_IN                      | SYSRESET input enable bit. When<br>asserted, asserting SYSRESET resets<br>the interface. When deasserted,<br>asserting SYSRESET does not reset<br>the interface.  |
| EPC_TTL_LATCH0<br><br>EPC_TTL_LATCH7 | TTL trigger latch bits. When asserted,<br>a bit indicates that the interface has<br>latched the corresponding TTL trigger<br>interrupt. Supported on EPC-7 only.  |
| EPC_WATCHDOG                         | Watchdog timer expiration bit. When<br>asserted, the bit indicates that a<br>watchdog timeout error has occurred<br>since the watchdog timer was last reset.<br>Supported on EPC-7 and EPC-8 only.              |
| EPC_WRDY                             | Word serial protocol Write Ready bit.<br>Asserting the bit indicates to a<br>commander device that the interface is<br>ready to receive a word serial<br>command. Supported on EPC-7 and<br>EPC-8 only.         |

| Return Value | The function returns a Bus Management return value: |  |
|--------------|---|--|
|              | EPC_INV_PTR   | The parameter <i>Misc_Mask_Ptr</i> is invalid. |
|              | EPC_INV_SESSION                                     | The parameter Session_ID is invalid.           |
|              | EPC_INV_SW  | The BusManager device driver is not present.   |
|              | EPC_SUCCESS   | The function completed successfully.           |
| See Also     | EpcOpenSession, EpcSetMiscAttributes.               |  |
| Example      | See EpcGetBusAttributes.                            |  |

#### **EpcGetSessionData**

**Description** Queries a session's application-specified data.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetSessionData( unsigned long Session\_ID, unsigned long FAR \* Session\_Data\_Ptr);

| Session_ID       | Session_ID specifies an open session.  |
|------------------|--|
| Session_Data_Ptr | Session_Data_Ptr specifies a location where the session's application-specified data will be placed. |

Visual Basic Synopsis

Declare Function EpcGetSessionData% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Session\_Data\_Ptr&)

**Remarks** EpcGetSessionData queries the specified session's application-specified data and places it in the location pointed to by *Session\_Data\_Ptr*.

The application-specified data is a 4-byte quantity.

Typically, an application uses **EpcSetSessionData** to store a pointer to one of its data structures. Later, the application uses **EpcGetSessionData** to quickly retrieve the pointer during performance-critical operations (like event handling).

| Return Value | The function returns a Bus Management return value: |  |
|--------------|---|--|
|              | EPC_INV_SESSION                                     | The specified Session_ID is invalid.       |
|              | EPC_INV_PTR   | The Session_Data_Ptr parameter is invalid. |
|              | EPC_SUCCESS   | The function completed successfully.       |
| See Also     | EpcOpenSession, EpcSetSessionData.                  |  |
| Example      | See EpcCloseSession.                                |  |

#### **EpcGetSlaveMapping**

**Description** Queries the interface's slave memory mapping.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcGetSlaveMapping

| (unsigned long   |       | Session_ID,       |
|------------------|-------|-------------------|
| unsigned short I | FAR * | Addess_Space_Ptr, |
| unsigned long I  | FAR * | Base_Addess_Ptr); |

Session\_IDSession\_ID specifies a session.Addess\_Space\_PtrAddess\_Space\_Ptr specifies a<br/>location where the interface's slave<br/>memory address space will be<br/>placed.Base\_Addess\_PtrBase\_Addess\_Ptr specifies a<br/>location where the interface's slave<br/>memory base address will be<br/>placed.

Visual Basic Synopsis

Declare Function EpcGetSlaveMapping% Lib "bmvxiw16.dll"

( ByVal Session\_ID&, Address\_Space\_Ptr%, Base\_Address\_Ptr&)

**Remarks** EpcGetSlaveMapping queries the mapping of the interface's slave memory and places the result in the locations pointed to by *Addess\_Space\_Ptr* and *Base\_Addess\_Ptr*. Possible values at *Addess\_Space\_Ptr* and *Base\_Addess\_Ptr* are dependent on the interface type:

| <u>Interface</u><br><u>Type</u> | *Addess_Space_Ptr | <u>*Base_Addess_Ptr</u>                |
|---------------------------------|-------------------|--|
| EPC-4                           | EPC_DISABLED      | N/A                                    |
|                                 | EPC_A24           | 0x00000000, 0x00400000,,<br>0x00C00000 |
|                                 | EPC_A32           | 0x18000000, 0x19000000,,<br>0x1F000000 |
| EPC-5                           | EPC_DISABLED      | N/A                                    |
|                                 | EPC_A24           | 0x00000000, 0x00400000,,<br>0x00C00000 |
|                                 | EPC_A32           | 0x18000000, 0x19000000,,<br>0x1F000000 |
| EPC-7                           | EPC_DISABLED      | N/A                                    |
|                                 | EPC_A24           | 0x00000000, 0x00400000,,<br>0x00C00000 |
|                                 | EPC_A32           | 0x00000000, 0x01000000,,<br>0xFF000000 |
| EPC-8                           | EPC_DISABLED      | N/A                                    |
| VXLink                          | EPC_DISABLED      | N/A                                    |

A24 base addresses are aligned on a 4 Mbyte boundary, and only the first 4 Mbytes of the interface's slave memory is mapped to the bus. A32 base addresses are aligned on a 16 Mbyte boundary, and only the first 16 Mbytes of the interface's slave memory is mapped to the bus.

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| Return Value | The function returns a Bus Management return value: |  |
|--------------|---|--|
|              | EPC_INV_PTR   | One or more of the parameters<br>Addess_Space_Ptr and Base_Addess_Ptr<br>is invalid. |
|              | EPC_INV_SESSION                                     | The parameter Session_ID is invalid.   |
| -            | EPC_INV_SW  | The BusManager device driver is not present.   |
|              | EPC_SUCCESS   | The function completed successfully.   |
| See Also     | EpcOpenSession, EpcSe                               | etSlaveMapping.  |
| Example      | See EpcGetBusAttribut                               | es.  |

#### **EpcGetULA**

**Description** Queries the interface's unique logical address.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcGetULA(unsigned long Session\_ID, unsigned short FAR
\*ULA\_Ptr);

| Session_ID | Session_ID specifies a session.   |
|------------|---|
| ULA_Ptr    | ULA_Ptr specifies a location where the interface's unique logical address will be placed. |

Visual Basic Synopsis

Declare Function EpcGetULA% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ULA\_Ptr%)

**Remarks EpcGetULA** queries the interface's unique logical address and places the result in the locations pointed to by *ULA\_Ptr*. Possible unique logical addresses are 0x00 through 0xFF.

Return Value The function returns a Bus Management return value:

|          | EPC_INV_PTR                | The parameters ULA_Ptr is invalid.           |
|----------|----------------------------|--|
|          | EPC_INV_SESSION            | The parameter Session_ID is invalid.         |
|          | EPC_INV_SW                 | The BusManager device driver is not present. |
|          | EPC_SUCCESS                | The function completed successfully.         |
| See Also | EpcOpenSession, EpcSetULA. |  |

Example See EpcGetBusAttributes.

#### EpcLockSession

**Description** Locks shared interface hardware for a session.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcLockSession(unsigned long Session\_ID);

Session\_ID Session\_ID specifies a session.

Visual Basic Synopsis

Declare Function EpcLockSession% Lib "bmvxiw16.dll" (ByVal Session\_ID&)

**Remarks** EpcLockSession locks shared interface hardware for the specified *Session\_ID*.

Locking gives a session exclusive access to shared interface hardware. Locking is used in multithreaded environments to prevent simultaneous, potentially conflicting hardware accesses.

Locks can be nested. EPConnect maintains a global lock counter. The global lock counter can be "owned" by at most one session. Initially, the lock counter is zero, indicating that no session has locked shared interface hardware. EpcLockSession acquires and increments the lock counter for a session. EpcUnlockSession decrements the lock counter for the same session. A non-zero lock counter indicates that shared interface hardware is locked.

When an application calls a Bus Management Bus Management Library function that obeys the locking paradigm, the function checks for an existing lock. If no lock exists or the specified session "owns" the lock, the function proceeds. Otherwise, the function suspends execution until the lock is released or the specified session's locking timeout expires. If the existing lock is not released before the specified session's locking timeout expires, the function returns EPC\_LOCKED. Use EpcGetLockingTimeout and EpcSetLockingTimeout to query and define a session's locking timeout.

The following EPConnect Bus Management Library functions obey locks:

| EpcAssertInterrupt    | EpcSetEpcLines            |
|-----------------------|---------------------------|
| EpcCmdReceiveWSBuffer | EpcSetEpcMODID            |
| EpcCmdSendWSBuffer    | EpcSetEpcTriggers         |
| EpcCmdSendWSCommand   | EpcSetMiscAttributes      |
| EpcDeassertInterrupt  | EpcSetSlaveMapping        |
| EpcLockSession        | EpcSetULA                 |
| EpcMapBusMemory       | EpcSrvEnableWsCommand     |
| EpcMapEpcTriggers     | EpcSrvReceiveWSCommand    |
| EpcMapSharedMemory    | EpcSrvSendProtocolEvent   |
| EpcPulseEpcLines      | EpcSrvSendWSProtocolError |
| EpcPulseEpcTriggers   | EpcSrvSendWSResponse      |
| EpcSetBusAttributes   | EpcValidateBusMapping     |

Return Value The function returns a Bus Management return value:

| EPC_INV_SESSION | The specified <i>Session_ID</i> is invalid.             |  |
|-----------------|---|--|
| EPC_INV_SW      | The BusManager device driver is not present.            |  |
| EPC_LOCKED      | Shared interface hardware is locked by another session. |  |
| EPC_SUCCESS     | The function completed successfully.                    |  |

- See Also EpcGetLockingTimeout, EpcOpenSession, EpcSetLockingTimeout, EpcUnlockSession.
- Example See EpcGetLockingTimeout.

#### **EpcMapBusMemory**

# **Description** Creates a bus memory mapping using statically configured bus window hardware.

C Synopsis

#include "busmgr.h"

| short FAR PASCAL |  |                    |  |
|------------------|--|--------------------|--|
| EpcMapBusMemor   | y( unsigned long   | Session_ID,        |  |
|                  | unsigned short   | Address_Mod,       |  |
|                  | unsigned short   | Byte_Ordering,     |  |
|                  | unsigned long  | Base_Address,      |  |
|                  | unsigned long  | Size,              |  |
|                  | void HUGE * FAR '  | * Mapped_Ptr_Ptr); |  |
| Session_ID       | Session_ID specifies a l   | ous session.       |  |
| Address_Mod      | Address_Mod specifies attribute of the desired a   |                    |  |
| Byte_Ordering    | Byte_Ordering specifies attribute of the desired in |                    |  |
| Base_Address     | Base_Address specifies attribute of the desired a  |                    |  |
| Size             | Size specifies the size as desired memory mapping  |                    |  |
| Mapped_Ptr_Ptr   | Mapped_Ptr_Ptr points<br>a pointer to the base of<br>will be placed.   |                    |  |

Visual Basic Synopsis

Declare Function EpcMapBusMemory% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Address\_Mod%, ByVal Byte\_Ordering%, ByVal Base\_Address&, ByVal Size&, Mapped\_Ptr\_Ptr As Any)

2

**Remarks EpcMapBusMemory** creates a memory mapping with the specified attributes using statically configured bus window hardware and places a pointer to the base of the memory in the location pointed to by *Mapped\_Ptr\_Ptr*.

The following constants define valid values for the *Address\_Mod* parameter:

| Constant  | Description  |
|-----------|--|
| EPC_A16N  | VMEbus A16 non-supervisory address modifier.         |
| EPC_A16S  | VMEbus A16 supervisory address modifier.             |
| EPC_A24ND | VMEbus A24 non-supervisory data address modifier.    |
| EPC_A24SD | VMEbus A24 supervisory data address modifier.        |
| EPC_A24NP | VMEbus A24 non-supervisory program address modifier. |
| EPC_A24SP | VMEbus A24 supervisory program address modifier.     |
| EPC_A32ND | VMEbus A32 non-supervisory data address modifier.    |
| EPC_A32SD | VMEbus A32 supervisory data address modifier.        |
| EPC_A32NP | VMEbus A32 non-supervisory program address modifier. |
| EPC_A32SP | VMEbus A32 supervisory program address modifier.     |

The following constants define valid values for the *Byte\_Ordering* parameter:

| <u>Constant</u> | <b>Description</b>              |
|-----------------|---------------------------------|
| EPC_IBO         | Intel (80X86) byte ordering.    |
| EPC_MBO         | Motorola (68000) byte ordering. |

EPC hardware provides a number of statically configured bus windows. The table below enumerates the bus memory mapping attributes supported by an EPC's statically configured bus window hardware:

| <u>Address_Mod</u> | <u>Byte_Odrering</u> | <u>Base_Address Range</u> | <u>Size Range</u>     |
|--------------------|----------------------|---------------------------|-----------------------|
| EPC_A16S           | EPC_MBO<br>EPC_IBO   | 0x00 to 0x0000FFFF        | 0x00010000 to<br>0x01 |
| EPC_A24SD          | EPC_MBO<br>EPC_IBO   | 0x00 to 0x00FFFFFF        | 0x01000000 to<br>0x01 |
| EPC_A32SD          | EPC_MBO<br>EPC_IBO   | 0x00 to 0x3FFFFFFF        | 0x40000000 to<br>0x01 |

The *Base\_Address* and *Size* parameters define a range of addresses  $\alpha$ , where:

 $Base\_Address \le \alpha \le Base\_Address + Size - 1;$ 

The function rounds the specified *Base\_Address* down to the nearest 4-byte boundary. The function also limits the size of the mapping according to the specified *Base\_Address* and the bus window's maximum accessible bus address.

EPC and VXLink hardware provides one or more dynamically configured bus memory windows. Use **EpcMapBusMemoryExt** to map bus memory using dynamically configured bus memory window hardware.

| Return Value | The function returns a Bus Management return value:   |   |  |
|--------------|---|---|--|
|              | EPC_INV_ADDMOD  | The specified <i>Address_Mod</i> parameter is invalid.  |  |
|              | EPC_INV_BORDER  | The specified <i>Byte_Ordering</i> parameter is invalid.  |  |
|              | EPC_INV_PTR   | The specified <i>Mapped_Ptr_Ptr</i> parameter is invalid.   |  |
|              | EPC_INV_RANGE   | The specified <i>Base_Address</i> and <i>Size</i><br>Parameters define a bus address<br>range that contains invalid addresses<br>for the specified <i>Address_Mod</i><br>parameter and/or this interface. |  |
|              | EPC_INV_SESSION   | The specified Session_ID is invalid.  |  |
|              | EPC_INV_SW  | The BusManager device driver is not present.  |  |
|              | EPC_OS_ERROR  | An operating system error occurred.   |  |
|              | EPC_OUT_OF_RSRCS  | The underlying operating system<br>currently contains insufficient<br>resources to create the specified<br>mapping.   |  |
|              | EPC_SUCCESS   | The function completed successfully.  |  |
| See Also     | EpcCopyData, EpcGetMappingAttributes,<br>EpcMapBusMemoryExt, EpcOpenSession, EpcPopData,<br>EpcPushData, EpcUnmapBusMemory. |   |  |
| Example      | See EpcCopyData.  |   |  |

#### **EpcMapBusMemoryExt**

**Description** Creates a bus memory mapping using dynamically configured bus window hardware.

C Synopsis

| short FAR PASCAL<br>EpcMapBusMemory | YExt(unsigned long Session_1D,<br>unsigned short Address_Mod,<br>unsigned short Byte_Ordering,<br>unsigned long Base_Address,<br>volatile void HUGE * FAR * |
|-------------------------------------|---|
| Mapped_Ptr                          | <u>~_</u> <i>Ptr</i> );   |
| Session_ID                          | Session_ID specifies a bus session.   |
| Address_Mod                         | Address_Mod specifies the address modifier attribute of the desired memory mapping.   |
| Byte_Ordering                       | Byte_Ordering specifies the byte ordering attribute of the desired memory mapping.  |
| Base_Address                        | <i>Base_Address</i> specifies base address attribute of the desired memory mapping.   |
| Mapped_Ptr_Ptr                      | Mapped_Ptr_Ptr points to a location<br>where a pointer to the base of the<br>desired memory will be placed.   |

Visual Basic Synopsis

Declare Function

EpcMapBusMemoryExt% Lib "bmvxiw16.dll" (ByVal Session 1D&

(ByVal Session\_ID&, ByVal Address\_Mod%, ByVal Byte\_Ordering%, ByVal Base\_Address&, ByVal Size&, Mapped\_Ptr\_Ptr As Any) **Remarks EpcMapBusMemoryExt** creates a memory mapping with the specified attributes using statically configured bus window hardware and places a pointer to the base of the memory in the location pointed to by *Mapped\_Ptr\_Ptr*.

The following constants define valid values for the *Address\_Mod* parameter:

| <u>Constant</u> | Description  |
|-----------------|--|
| EPC_A16N        | VMEbus A16 non-supervisory address modifier.         |
| EPC_A16S        | VMEbus A16 supervisory address modifier.             |
| EPC_A24ND       | VMEbus A24 non-supervisory data address modifier.    |
| EPC_A24SD       | VMEbus A24 supervisory data address modifier.        |
| EPC_A24NP       | VMEbus A24 non-supervisory program address modifier. |
| EPC_A24SP       | VMEbus A24 supervisory program address modifier.     |
| EPC_A32ND       | VMEbus A32 non-supervisory data address modifier.    |
| EPC_A32SD       | VMEbus A32 supervisory data address modifier.        |
| EPC_A32NP       | VMEbus A32 non-supervisory program address modifier. |
| EPC_A32SP       | VMEbus A32 supervisory program address modifier.     |

The following constants define valid values for the *Byte\_Ordering* parameter:

| <u>Constant</u> | Description                     |
|-----------------|---------------------------------|
| EPC_IBO         | Intel (80X86) byte ordering.    |
| EPC_MBO         | Motorola (68000) byte ordering. |

EPC and VXLink hardware provide one or more 64-Kbyte dynamically configured bus windows. The table below enumerates the bus memory mapping attributes supported by the dynamically configured bus window hardware:

| <u>Address_Mod</u>                               | <u>Byte_Ordering</u> | <u>Base_Address</u> Range                  |
|--|----------------------|--|
| EPC_A16N<br>EPC_A16S                             | EPC_MBO<br>EPC_IBO   | 0x00000000                                 |
| EPC_A24ND<br>EPC_A24NP<br>EPC_A24SD<br>EPC_A24SP | EPC_MBO<br>EPC_IBO   | 0x000000000,<br>0x00010000,,<br>0x00FF0000 |
| EPC_A32ND<br>EPC_A32NP<br>EPC_A32SD<br>EPC_A32SP | EPC_MBO<br>EPC_IBO   | 0x000000000,<br>0x00010000,,<br>0xFFFF0000 |

The function rounds the specified *Base\_Address* down to the nearest bus window size boundary (64 Kbytes) and sets the size of the mapping to the size of the bus window (64 Kbytes). Mapping an address range larger than the bus window size requires multiple mappings. Also, mapping an address range that spans a bus window size boundary requires multiple mappings.

EPC hardware also provides a number of statically configured bus memory windows. Use **EpcMapBusMemory** to map bus memory using statically configured bus memory window hardware.

| Return Value | The function returns a Bus Management return value:  |   |
|--------------|--|---|
|              | EPC_INV_ADDMOD   | The specified <i>Address_Mod</i> parameter is invalid.  |
|              | EPC_INV_BORDER   | The specified Byte_Ordering parameter is invalid.   |
|              | EPC_INV_PTR  | The specified <i>Mapped_Ptr_Ptr</i> parameter is invalid.   |
|              | EPC_INV_SESSION  | The specified Session_ID is invalid.  |
|              | EPC_OS_ERROR An operating system e   | An operating system error occurred.   |
|              | EPC_OUT_OF_RSRCS   | The underlying operating system<br>currently contains insufficient<br>resources to create the specified<br>mapping. |
|              | EPC_SUCCESS  | The function completed successfully.  |
| See Also     | EpcCopyData, EpcGetMappingAttributes,<br>EpcMapBusMemory, EpcOpenSession, EpcPopData,<br>EpcPushData, EpcUnmapBusMemory. |   |
| Example      | See EpcCopyData.   |   |

#### **EpcMapEpcTriggers**

**Description** Maps one interface trigger line to another.

C Synopsis

#include "busmgr.h"

short EpcMapEpcTriggers( unsigned long Session\_ID, unsigned long In\_Trigger\_Mask, unsigned long Out\_Trigger\_Mask);

Session\_ID

In\_Trigger\_Mask

Out\_Trigger\_Mask

interface trigger line. *Out\_Trigger\_Mask* specifies output interface trigger lines.

In Trigger Mask specifies an input

Session\_ID specifies a session.

Visual Basic Synopsis

Declare Function EpcMapEpcTriggers% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal In\_Trigger\_Mask&, ByVal Out\_Trigger\_Mask&)

**Remarks** EpcMapEpcTriggers maps the interface trigger lines specified by *Out\_Trigger\_Mask* as outputs of the interface trigger line specified by *In\_Trigger\_Mask*.

The parameters *In\_Trigger\_Mask* is a constant specifying an input interface trigger line. The parameter *Out\_Trigger\_Mask* is an OR'd mask of constants specifying output interface trigger lines.

The table below enumerates valid trigger mapping combinations for an EPC-7 interface:

| <u>In Trigger Mask</u>             | <u>Out_Trigger_Mask</u>            | <b>Description</b>  |
|------------------------------------|------------------------------------|---|
| EPC_EXT_TRIG0                      | EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | Maps external<br>trigger 0 as input to<br>a single TTL trigger<br>line. |
| EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | EPC_EXT_TRIG0                      | Maps a single TTL<br>trigger line as input<br>to external trigger 0.    |

The table below enumerates valid trigger mapping combinations for a VXLink interface:

| <u>In Trigger Mask</u>             | <u>Out_Trigger_Mask</u>            | <b>Description</b>  |
|------------------------------------|------------------------------------|---|
| EPC_EXT_TRIG0                      | 0x0000000                          | Unmaps external<br>trigger 0.   |
| EPC_EXT_TRIG0                      | EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | Maps external<br>trigger 0 as input to<br>a single TTL trigger<br>line. |
| 0x0000000                          | EPC_EXT_TRIG1                      | Unmaps external<br>trigger 1.   |
| EPC_TTL_TRIG0<br><br>EPC_TTL_TRIG7 | EPC_EXT_TRIG1                      | Maps a single TTL<br>trigger line as input<br>to external trigger 0.    |

When an external trigger line is mapped as input to one or more interface trigger lines, asserting the external trigger line asserts all of the mapped interface trigger lines. Likewise, deasserting the external trigger line deasserts all of the mapped interface trigger lines. When one or more interface trigger lines are mapped as input to an external trigger line, asserting one of the interface trigger lines asserts the mapped external trigger line. Likewise, deasserting one of the interface trigger lines deasserts the mapped external trigger line.

An EPC-7 interface provides a single bi-directional external trigger. The external trigger is always mapped; it cannot be unmapped. Specifying a mapping for external trigger 0 overrides the previous mapping. By default, TTL trigger 1 is mapped as an output to external trigger 0.

A VXLink interface provides two unidirectional external triggers. External trigger 0 is an input-only trigger and external trigger 1 is an output-only trigger. The external triggers can be independently mapped or unmapped. By default, both external triggers are unmapped.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_MASK    | Either In_Trigger_Mask or<br>Out_Trigger_Mask is invalid. |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.                      |
| EPC_INV_SW      | The Bus Manager device driver is not present.             |
| EPC_LOCKED      | Shared interface hardware is locked by another session.   |
| EPC_SUCCESS     | The function completed successfully.                      |

See Also EpcGetEpcTriggerMapping, EpcOpenSession.

#### **EpcMapSharedMemory**

**Description** Creates a shared memory mapping.

#### C Synopsis

#include "busmgr.h"

#### short FAR PASCAL EpcMapSharedMemory

| (unsigned long      | Session_ID,         |
|---------------------|---------------------|
| unsigned long FAR * | * Base_Address_Ptr, |
| unsigned long FAR * | * Size_Ptr,         |
| void HUGE * FAR *   | * Mapped_Ptr_Ptr);  |

| Session_ID       | Session_ID specifies a bus session.  |
|------------------|--|
| Base_Address_Ptr | <i>Base_Address_Ptr</i> points to a location where<br>the base address attribute of the shared<br>memory mapping will be placed. |
| Size_Ptr         | <i>Size_Ptr</i> points to a location where the size attribute of the shared memory mapping, in bytes, will be placed.            |
| Mapped_Ptr_Ptr   | <i>Mapped_Ptr_Ptr</i> points to a location where a pointer to the base of the desired memory will be placed.                     |

Visual Basic Synopsis

Declare Function EpcMapSharedMemory% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Base\_Address\_Ptr&, Size\_Ptr&, Mapped\_Ptr\_Ptr As Any)

**Remarks** EpcMapSharedMemory creates a shared memory mapping and places the base address attribute of the memory mapping, the size attribute of the memory mapping, and a pointer to the base of the memory in the locations pointed to by *Base\_Address\_Ptr, Size\_Ptr,* and *Mapped\_Ptr\_Ptr*, respectively.

The values in the locations pointed to by *Base\_Address\_Ptr* and *Size\_Ptr* define a range of addresses  $\alpha$ , where:

#### \*Base\_Address\_Ptr <= $\alpha$ <= \*Base\_Address\_Ptr + \*Size\_Ptr - 1;

The value in the location pointed to by *Base\_Address\_Ptr* specifies a physical local address. To determine the corresponding physical VMEbus address, the value should be added to the base address of the slave memory. Use **EpcGetSlaveMapping** to determine the base address of the slave memory.

A shared memory area is a global resource. EpcMapSharedMemory and EpcUnmapSharedMemory map and unmap the entire shared memory area. Once a session maps the shared memory area, it cannot be mapped again until the original session unmaps it.

An interface must contain dual-ported slave memory to support a shared memory area. Only the EPC-7 supports shared memory area functionality.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR      | One or more of the <i>Base_Address_Ptr</i> ,<br><i>Size_Ptr</i> , and <i>Mapped_Ptr_Ptr</i><br>parameters is invalid. |
|------------------|---|
| EPC_INV_SESSION  | The specified Session_ID is invalid.  |
| EPC_INV_SW       | The BusManager device driver is not present.  |
| EPC_LOCKED       | Shared interface hardware is locked by another bus session.   |
| EPC_OS_ERROR     | An operating system error occurred.   |
| EPC_OUT_OF_RSRCS | The underlying operating system<br>currently contains insufficient<br>resources to create the specified<br>mapping.   |
| EPC_SUCCESS      | The function completed successfully.  |

See Also EpcCopyData, EpcGetMappingAttributes, EpcGetSlaveMapping, EpcOpenSession, EpcUnmapSharedMemory.

**Example** See EpcCopyData.

## EpcOpenSession

| Description      | Creates a session.   |   |
|------------------|--|---|
| C Synopsis       | #include ''busmgr.h''  |   |
|                  | short FAR PASCAL<br>EpcOpenSession( un   | signed long FAR *Session_ID_Ptr);   |
|                  |  | Session_ID_Ptr points to a location where a handle to the session will be placed.                     |
| Visual Basic Syr | nopsis   |   |
|                  | Declare Function<br>EpcOpenSession% Li   | b "bmvxiw16.dll" (Session_ID_Ptr&)  |
| Remarks          | <b>EpcOpenSession</b> creates a session and places a handle to t session in the location pointed to by <i>Session_ID_Ptr</i> . |   |
|                  |  | reated session does not lock shared interface<br>enabled events, installed event handlers, or         |
| Return Value     | The function returns a Bus Management return value:  |   |
|                  | EPC_INV_PTR  | The specified <i>Session_ID_Ptr</i> parameter is invalid.   |
|                  | EPC_INV_SW   | The BusManager device driver is not present.  |
|                  | EPC_OUT_OF_RSR   | CS The underlying operating system<br>currently contains insufficient<br>resources to open a session. |
|                  | EPC_SUCCESS  | The function completed successfully.  |
| See Also         |  | LockSession, EpcMapBusMemory,<br>ory, EpcSetEventEnableMask,  |

**Example** See EpcCloseSession.



#### **EpcPopData**

Pops a block of data from a single memory location to consecutive memory locations.

C Synopsis

#include "busmgr.h"

| EpcPopData(   | unsigned long *<br>void HUGE *<br>void HUGE *<br>unsigned long<br>unsigned short<br>unsigned long FAR * | Session_ID,<br>Source_ptr,<br>Dest_ptr,<br>Size,<br>Data_Width<br>Actual_Size_Ptr); |
|---------------|---|---|
| Session_ID    | Session_ID spec   | cifies a bus location.  |
| Source_Ptr    |   | cifies the address of a FIF<br>ch data will be popped.                              |
| Dest_Ptr      | -   | fies the address of a da<br>h data will be popped.                                  |
| Size          | <i>Size</i> specifies th pop.   | he number of data bytes   |
| Data_Width    | Data_Width sp<br>bits to pop per b  | ecifies the number of da  |
| Actual_Size_P |   | r specifies a location whe<br>per of bytes popped will                              |

**Remarks** EpcPopData efficiently pops blocks of data from a single memory location to consecutive memory locations using the attributes of pointes *Source\_Ptr* and *Dest\_Ptr*. The intended use of the function is popping large blocks of data from a FIFO queue.

The Size parameter should always express the number of bytes to be popped, regardless of the specified Data\_Width parameter. Passing a zero Size parameter results in no data being popped.

**Remarks** The following constants define valid values for the *Data\_Width* parameter:

| <u>Constant</u> | Description   |
|-----------------|---|
| EPC_8_BIT       | 8-bit data width  |
| EPC_8_BIT_ODD   | 8-bit data width, odd bytes only  |
| EPC_16_BIT      | 16-bit data width   |
| EPC_32_BIT      | 32-bit data width   |
| EPC_FASTCOPY    | To increase pop performance, don't<br>check for intermediate bus errors. This<br>constant can not be used alone; it must<br>be OR'd with one of the preceding<br>constants. |

The function returns the actual number of bytes popped in the location pointed to by *Actual\_Size\_Ptr*.

The function operates ocrrectly using both unmapped pointers and memory mapped pointers for Source\_Ptr and Dest\_Ptr. Local-to-local, local-to-VME, VME-to-local, and VME-to-VME pops all execute properly.

For a pop to complete, any *Source\_Ptr* or *Dest\_Ptr* that corresponds to a VMEbus addresses must be aligned on an address boundary equivalent to the specified *Data\_Width*. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if both *Source\_Ptr* and *Dest\_Ptr* correspond to VMEbus memory and *Data\_Width* is EPC\_16\_BIT, then both *Source\_Ptr* and *Dest\_Ptr* must correspond to VMEbus addresses aligned on a 16-bit boundary for the pop to complete successfully.

For a 16-bit or 32-bit pop to complete under DOS or Windows, no individual data element may span a segment boundary. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if *Data\_Width* is EPC\_16\_BIT and *Size* is greater than 64 Kbytes, both *Source\_Ptr* and *Dest\_Ptr* must be aligned on a 16-bit boundary for the pop operation to complete successfully.

| Return Value                            | The function returns an EPConnect return value:   |  |
|---|---|--|
|   | EPC_BERR  | A bus error occurred during the pop.   |
|   | EPC_INV_ALIGN   | Size is not a multiple of Data_Width,<br>Source_Ptr is mapped to a VMEbus<br>address and is not aligned on a<br>Data_Width boundary, Dest_Ptr is<br>mapped to a VMEbus address and is<br>not aligned on a Data_Width<br>boundary, or a 16-bit or 32-bit data<br>element spans a segment boundary or. |
|   | EPC_INV_PTR   | One or more of <i>Source_Ptr</i> ,<br><i>Dest_Ptr</i> , or <i>Actual_Size_Ptr</i> is<br>invalid.   |
|   | EPC_INV_RANGE   | The address range defined by<br>Source_Ptr and Data_Width and/or<br>the address range defined by<br>Dest_Ptr and Size contains bus<br>addresses that are not currently<br>mapped.  |
|   | EPC_INV_SESSION   | The specified Session_ID is invalid.   |
| EPC_INV_SW The BusManager d<br>present. | The BusManager device driver is not present.  |  |
|   | EPC_LOCKED  | Shared interface hardware is locked by another session.  |
|   | EPC_INV_WIDTH   | The Data_Width parameter is invalid.   |
|   | EPC_SUCCESS   | The function completed successfully.   |
| See Also                                | EpcCopyData, EpcGetMappingAttributes,<br>EpcMapBusMemory, EpcMapSharedMemory,<br>EpcOpenSession, EpcPushData, EpcUnmapBusMemory,<br>EpcUnmapSharedMemory. |  |
| Example                                 | See EpcCopyData.  |  |

#### EpcPulseEpcLines

Description Pulses EPC control lines.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcPulseEpcLines(unsigned long Session\_ID, unsigned long
Line\_Mask);

Session\_ID

Session\_ID specifies a session.

Line\_Mask

*Line\_Mask* specifies a mask of EPC control lines.

Visual Basic Synopsis

Declare Function EpcPulseEpcLines% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Line\_Mask&)

**Remarks EpcPulseEpcLines** pulses (asserts and deasserts as an atomic operation) the EPC control lines specified by *Line\_Mask*.

*Line\_Mask* is an OR'd mask of the following constants, where a set bit indicates that the function should pulse the corresponding interface control line:

| Constant     | Description |
|--------------|-------------|
| EPC_SYSFAIL  | SYSFAIL.    |
| EPC_SYSRESET | SYSRESET.   |

The function directly affects the interface control line state. Interface control line state reflects the state of bits in the interface's control line drive registers. Actual bus control line state is an OR'd combination of the states all devices on the bus. If the interface asserts a control line, the actual bus control line transitions from deasserted to asserted only if all other devices on the bus have previously deasserted the line. Likewise, if the interface deasserts a control line, the actual bus control line transitions from asserted to deasserted only if all devices on the bus have previously deasserted to the bus have previously deasserted the line.

When pulsing the SYSRESET interface control line, the function leaves the line asserted for at least 200 milliseconds (in accordance with bus specifications). Whether pulsing the SYSRESET actual bus control line resets an EPC-7 or EPC-8 depends on the value of the interface's EPC\_SYSRESET\_IN miscellaneous attribute bit (see EpcSetMiscAttributes). (EPC-7 and EPC-8 only)

Whether pulsing the SYSFAIL interface control line pulses the SYSFAIL actual bus control line depends on the value of the interface's EPC\_SYSFAIL\_OUT miscellaneous attribute bit (see EpcSetMiscAttributes).

To pulse SYSFAIL on an EPC-7, EPC-8, or VXLink interface, the function deasserts then asserts the interface's EPC\_PASS miscellaneous attribute bit (see EpcSetMiscAttributes). After pulsing SYSFAIL, the interface's EPC\_PASS miscellaneous attribute remains asserted.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_MASK    | The parameter <i>Line_Mask</i> is invalid.              |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.                    |
| EPC_INV_SW      | The BusManager device driver is not present.            |
| EPC_LOCKED      | Shared interface hardware is locked by another session. |
| EPC_SUCCESS     | The function completed successfully.                    |

See Also EpcGetBusLines, EpcGetEpcLines, EpcOpenSession, EpcSetEpcLines, EpcSetMiscAttributes.

Example See EpcAssertInterrupt.

#### **EpcPulseEpcTriggers**

**Description** Pulses interface trigger lines.

C Synopsis

#include "busmgr.h"

short
EpcPulseEpcTriggers(unsigned long Session\_ID, unsigned long
Trigger\_Mask);

Session\_ID

Trigger\_Mask

Session\_ID specifies a session.

*Trigger\_Mask* specifies a mask of interface trigger lines.

Visual Basic Synopsis

Declare Function **EpcPulseEpcTriggers**% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Trigger\_Mask&)

**Remarks** EpcPulseEpcTriggers pulses (asserts and deasserts as an atomic operation) the interface trigger lines specified by *Trigger\_Mask*.

*Trigger\_Mask* is an OR'd mask of the following constants, where a set bit indicates that the function should pulse the corresponding interface trigger line:

| Constant      | Description                            |
|---------------|--|
| EPC_ECL_TRIG0 | ECL trigger 0 (EPC-7 only).            |
| EPC_ECL_TRIG1 | ECL trigger 1 (EPC-7 only).            |
| EPC_TTL_TRIG0 | TTL trigger 0 (EPC-7 and VXLink only). |
| •••           |  |
| EPC_TTL_TRIG7 | TTL trigger 7 (EPC-7 and VXLink only). |

The function directly affects the interface trigger line state. Interface trigger line state reflects the state of bits in the interface's trigger line drive registers. Actual bus trigger line state is an OR'd combination of the states all devices on the bus. If the interface asserts a trigger line, the actual bus trigger line transitions from deasserted to asserted only if all other devices on the bus have previously deasserted the line. Likewise, if the interface deasserted to deasserted to deasserted the bus trigger line transitions from asserted to deasserted the line.

#### **Return Value** The function returns a EPConnect return value:

|    | EPC_INV_MASK         | The parameter Trigger_Mask is invalid.                  |
|----|----------------------|---|
|    | EPC_INV_SESSION      | The specified Session_ID is invalid.                    |
|    | EPC_INV_SW           | The BusManager device driver is not present.            |
|    | EPC_LOCKED           | Shared interface hardware is locked by another session. |
|    | EPC_SUCCESS          | The function completed successfully.                    |
| 50 | EncGetBusTriggers En | cGetEncTriggers EncOpenSession                          |

See Also EpcGetBusTriggers, EpcGetEpcTriggers, EpcOpenSession, EpcSetEpcTriggers.

#### **EpcPushData**

Pushes a block of data from consecutive memory locations to a single memory location.

C Synopsis

|    | single memory  | location.      |                             |   |
|----|----------------|----------------|-----------------------------|---|
| is |                |                |                             |   |
|    | #include ''bus | mgr.h''        |                             |   |
|    | short FAR PA   | SCAL           |                             |   |
|    | EpcPushData    | (unsigned long |                             | Session_ID,   |
|    |                | void HUGE *    |                             | Source_Ptr,   |
|    |                | void HUGE *    | Ł                           | Dest_Ptr,   |
|    |                | unsigned long  | 7                           | Size,   |
|    |                | unsigned shor  | t                           | Data_Width,   |
|    |                |                |                             | Actual_Size_Ptr);   |
|    | Session_ID     |                | Session                     | _ID specifies a bus session.  |
|    | Source_Ptr     |                |                             | _Ptr specifies the address of a ffer from which data will be                    |
|    | Dest_Ptr       |                |                             | tr specifies the address of a ueue to which data will be                        |
|    | Size           |                | <i>Size</i> spe<br>bytes to | ecifies the number of data<br>p push.   |
|    | Data_Width     |                |                             | <i>Vidth</i> specifies the number of s to push per bus access.                  |
|    | Actual_Size_   | Ptr            | where                       | <i>Size_Ptr</i> specifies a location the actual number of bytes will be placed. |

**Remarks** EpcPushData efficiently pushes blocks of data from consecutive memory locations to a single memory location using the attributes of pointers *Source\_Ptr* and *Dest\_Ptr*. The intended use of the function is pushing large blocks of data to a FIFO queue.

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The Size parameter should always express the number of bytes to be pushed, regardless of the specified Data\_Width parameter. Passing a zero Size parameter results in no data being pushed.

The following constants define valid values for the *Data\_Width* parameter:

| <u>Constant</u> | Description   |  |
|-----------------|---|--|
| EPC_8_BIT       | 8-bit data width  |  |
| EPC_8_BIT_ODD   | 8-bit data width, odd bytes only  |  |
| EPC_16_BIT      | 16-bit data width   |  |
| EPC_32_BIT      | 32-bit data width   |  |
| EPC_FASTCOPY    | To increase push performance, don't<br>check for intermediate bus errors.<br>This constant cannot be used alone;<br>it must be OR'd with one of the<br>preceding constants. |  |

The function returns the actual number of bytes pushed in the location pointed to by Actual\_Size\_Ptr.

The function operates correctly using both unmapped pointers and memory mapped pointers for *Source\_Ptr* and *Dest\_Ptr*. local-to-local, local-to-VME, VME-to-local, and VME-to-VME pushes all execute properly.

For a push to complete, the specified *Size* must be aligned on a boundary equivalent to the specified *Data\_Width*. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if *Data\_Width* is EPC\_16\_BIT, then *Size* must be a multiple of two for the push to complete successfully. If *Data\_Width* is EPC\_32\_BIT, then *Size* must be a multiple of four for the push to complete successfully.

For a push to complete, any *Source\_Ptr* or *Dest\_Ptr* that corresponds to a VMEbus addresses must be aligned on an address boundary equivalent to the specified *Data\_Width*. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if both *Source\_Ptr* and *Dest\_Ptr* correspond to VMEbus memory and *Data\_Width* is EPC\_16\_BIT, then both *Source\_Ptr* and *Dest\_Ptr* must correspond to VMEbus addresses aligned on a 16-bit boundary for the push to complete successfully.

For a 16-bit or 32-bit push to complete under DOS or Windows, no individual data element may span a segment boundary. Otherwise, the function returns an EPC\_INV\_ALIGN error. For example, if *Data\_Width* is EPC\_16\_BIT and *Size* is greater than 64 Kbytes, both *Source\_Ptr* and *Dest\_Ptr* must be aligned on a 16-bit boundary for the push operation to complete successfully.

**Return Value** The function returns a EPConnect return value:

| EPC_BERR        | A bus error occurred during the push.   |
|-----------------|---|
| EPC_INV_ALIGN   | Size is not a multiple of Data_Width,<br>Source_Ptr is mapped to a VMEbus<br>address and is not aligned on a<br>Data_Width boundary, Dest_Ptr is<br>mapped to a VMEbus address and is<br>not aligned on a Data_Width<br>boundary, or a 16-bit or 32-bit data<br>element spans a segment boundary. |
| EPC_INV_PTR     | One or more of <i>Source_Ptr</i> ,<br><i>Dest_Ptr</i> , or <i>Actual_Size_Ptr</i> is<br>invalid.  |
| EPC_INV_RANGE   | The address range defined by<br>Source_Ptr and Size and/or the<br>address range defined by Dest_Ptr<br>and Data_Width contains bus<br>addresses that are not currently<br>mapped.   |
| EPC_INV_SESSION | The specified Session_ID is invalid.  |

| EPC_INV_SW    | The BusManager device driver is not present.            |
|---------------|---|
| EPC_LOCKED    | Shared interface hardware is locked by another session. |
| EPC_INV_WIDTH | The Data_Width parameter is invalid.                    |
| EPC_SUCCESS   | The function completed successfully.                    |

- See Also EpcCopyData, EpcGetMappingAttributes, EpcMapBusMemory, EpcMapSharedMemory, EpcOpenSession, EpcPopData, EpcUnmapBusMemory, EpcUnmapSharedMemory.
- **Example** See EpcCopyData.

#### **EpcSetBusAttributes**

| Description | Defines the interface's bus management attributes. |  |
|-------------|--|--|
| C Synopsis  |  |  |
|             | #include "busmgr.l                                 | a''  |
|             | short FAR PASCA<br>EpcSetBusAttribut               | L<br>es( unsigned long Session_ID,<br>unsigned short Bus_Enable,<br>unsigned short Bus_Arb_Mode,<br>unsigned short Bus_Arb_Priority,<br>unsigned short Bus_Release); |
|             | Session_ID   | Session_ID specifies a session.  |
|             | Bus_Enable   | <i>Bus_Enable</i> specifies the interface's bus enable attribute.  |
|             | Bus_Arb_Mode                                       | <i>Bus_Arb_Mode</i> specifies the interface's bus arbitration mode.  |
|             | Bus_Arb_Priority                                   | <i>Bus_Arb_Priority</i> specifies the interface's bus arbitration priority.  |
|             | Bus_Release  | <i>Bus_Release</i> specifies the interface's bus release mode.   |

Visual Basic Synopsis

Declare Function EpcSetBusAttributes% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Bus\_Enable%, ByVal Bus\_Arb\_Mode%, ByVal Bus\_Arb\_Priority%, ByVal Bus\_Release%)

**Remarks EpcSetBusAttributes** defines the interface's bus management attributes.

*Bus\_Enable* specifies the interface's bus enable attribute. The interface's bus enable attribute determines whether accesses made by the interface reach the bus. Valid *Bus\_Enable* values are:

| <u>Bus Enable</u> | Description   |
|-------------------|---|
| EPC_DISABLE_BUS   | Disable bus accesses for the interface (Supported on EPC-7 and EPC-8 only). |
| EPC_ENABLE_BUS    | Enable bus accesses for the interface.                                      |

*Bus\_Arb\_Mode* specifies the interface's bus arbitration mode. The interface's bus arbitration mode defines how the interface arbitrates bus collisions. The interface's bus arbitration mode only affects bus accesses if the interface has been designated the VMEbus slot-1 controller or VXIbus slot-0 controller. Valid *Bus\_Arb\_Mode* values are:

| <u>Bus_Arb_Mode</u> | Description                  |
|---------------------|------------------------------|
| EPC_PRIORITY        | Priority bus arbitration.    |
| EPC_ROUND_ROBIN     | Round-robin bus arbitration. |

*Bus\_Arb\_Priority* specifies the interface's bus arbitration priority. The interface's bus arbitration priority defines the priority level at which the interface arbitrates for the bus. Possible values placed at *Bus\_Arb\_Priority* are:

| <u>Bus Arb Priority</u> | <b>Description</b>          |
|-------------------------|-----------------------------|
| EPC_PRIORITY0           | Bus arbitration priority 0. |
| EPC_PRIORITY1           | Bus arbitration priority 1. |
| EPC_PRIORITY2           | Bus arbitration priority 2. |
| EPC_PRIORITY3           | Bus arbitration priority 3. |

Bus\_Release specifies the interface's bus release mode. The interface's bus release mode determines when the interface requests and/or releases the bus. Valid Bus\_Release values are:

| <u>Bus_Release</u> | <b>Description</b>                        |
|--------------------|---|
| EPC_ROR            | "Release On Request" bus release mode.    |
| EPC_RONR           | "Request On No Request" bus release mode. |

**Return Value** The function returns a Bus Management return value:

| EPC_INV_ARB_MODE | The parameter <i>Bus_Arb_Mode</i> is invalid.           |
|------------------|---|
| EPC_INV_ARB_PRIO | The parameter <i>Bus_Arb_Priority</i> is invalid.       |
| EPC_INV_ENABLE   | The parameter <i>Bus_Enable</i> is invalid.             |
| EPC_INV_RELEASE  | The parameter <i>Bus_Release</i> is invalid.            |
| EPC_INV_SESSION  | The parameter <i>Session_ID</i> is invalid.             |
| EPC_INV_SW       | The BusManager device driver is not present.            |
| EPC_INV_TIMEOUT  | An invalid timeout was encountered.                     |
| EPC_LOCKED       | Shared interface hardware is locked by another session. |
| EPC_SUCCESS      | The function completed successfully.                    |
|                  |   |

- See Also EpcGetBusAttributes, EpcOpenSession.
- **Example** See EpcGetBusAttributes.

#### **EpcSetEpcLines**

**Description** Defines the interface control line state.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcSetEpcLines(unsigned long Session\_ID, unsigned long
Line\_Mask);

Session\_ID

Line\_Mask

Session\_ID specifies a session.

*Line\_Mask* specifies an interface control line state.

Visual Basic Synopsis

Declare Function EpcSetEpcLines% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Line\_Mask&)

**Remarks EpcSetEpcLines** defines the interface control line state as specified by *Line\_Mask*.

*Line\_Mask* is either zero or an OR'd bit mask of the following constants. A set bit indicates that the function should assert the corresponding interface control line. A clear bit indicates that the function should deassert the corresponding interface control line:

| <u>Constant</u> | Description |
|-----------------|-------------|
| EPC_SYSFAIL     | SYSFAIL.    |
| EPC_SYSRESET    | SYSRESET.   |

The function directly affects the interface control line state. Interface control line state reflects the state of bits in the interface's control line drive registers. Actual bus control line state is an OR'd combination of the states all devices on the bus. If the interface asserts a control line, the actual bus control line transitions from deasserted to asserted only if all other devices on the bus have previously deasserted the line. Likewise, if the interface deasserts a control line, the actual bus control line transitions from asserted to deasserted only if all devices on the bus have previously deasserted the line.

Whether asserting the SYSRESET actual bus control line resets the interface on an EPC-7 or EPC-8 depends on the value of the interface's EPC\_SYSRESET\_IN miscellaneous attribute bit (see EpcSetMiscAttributes).

Whether asserting or deasserting the SYSFAIL interface control line asserts or deasserts the SYSFAIL actual bus control line depends on the value of the interface's EPC\_SYSFAIL\_OUT miscellaneous attribute bit (see EpcSetMiscAttributes).

To assert or deassert SYSFAIL on an EPC-7, EPC-8, or VXLink interface, the function deasserts or asserts the interface's EPC PASS miscellaneous attribute bit (see EpcSetMiscAttributes). After asserting SYSFAIL, the interface's EPC PASS miscellaneous attribute remains deasserted. Likewise, after deasserting SYSFAIL. the interface's EPC PASS miscellaneous attribute remains asserted.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_MASK    | The parameter Line_Mask is invalid.                     |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.                    |
| EPC_INV_SW      | The BusManager device driver is not present.            |
| EPC_LOCKED      | Shared interface hardware is locked by another session. |
| EPC_SUCCESS     | The function completed successfully.                    |

See Also EpcGetBusLines, EpcGetEpcLines, EpcOpenSession, EpcPulseEpcLines, EpcSetMiscAttributes.

**Example** See EpcAssertInterrupt.

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#### **EpcSetEpcMODID**

**Description** Defines interface MODID line state.

C Synopsis

#include "busmgr.h"

short
EpcSetEpcMODID(unsigned long Session\_ID, unsigned long
MODID\_Mask);

| Session_ID | Session_ID specifi | es a session. |    |
|------------|--------------------|---------------|----|
| MODID_Mask | MODID_Mask         | specifies     | an |
|            | interface MODID    | line state.   |    |

Visual Basic Synopsis

Declare Function EpcSetEpcMODID% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal MODID\_Mask&)

**Remarks** EpcSetEpcMODID defines the interface MODID line state as specified by *MODID\_Mask*.

*MODID\_Mask* is either zero or an OR'd bit mask of the following constants. A set bit indicates that the function should assert the corresponding interface MODID line. A clear bit indicates that the function should deassert the corresponding interface MODID line:

| <u>Constant</u> | Description                            |
|-----------------|--|
| EPC_MODID0      | MODID line 0 (EPC-7 and VXLink only).  |
| •••             |  |
| EPC_MODID12     | MODID line 12 (EPC-7 and VXLink only). |

Only the VXIbus slot-0 controller device can assert or deassert the actual bus MODID lines. When an interface is the VXIbus slot-0 controller, defining the interface MODID line state also defines the actual bus MODID line state. When an interface is not the VXIbus slot-0 controller, defining the interface MODID line state has no effect on the actual bus MODID lines.

#### **Return Value** The function returns an EPConnect return value:

| EPC_INV_MASK    | The parameter <i>MODID_Mask</i> is invalid.             |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.                    |
| EPC_INV_SW      | The Bus Manager device driver is not present.           |
| EPC_LOCKED      | Shared interface hardware is locked by another session. |
| EPC_SUCCESS     | The function completed successfully.                    |

See Also EpcGetBusMODID, EpcOpenSession.

#### **EpcSetEpcTriggers**

**Description** Defines the interface trigger line state.

C Synopsis

#include "busmgr.h"

short
EpcSetEpcTriggers(unsigned long Session\_1D, unsigned long
Trigger\_Mask);

| Session_ID   | Session_ID specifies a session.                                    |
|--------------|--|
| Trigger_Mask | <i>Trigger_Mask</i> specifies an interface bus control line state. |

Visual Basic Synopsis

Declare Function

**EpcSetEpcTriggers**% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Trigger\_Mask&)

**Remarks** EpcSetEpcTriggers defines the interface trigger line state as specified by *Trigger\_Mask*.

*Trigger\_Mask* is either zero or an OR'd bit mask of the following constants. A set bit indicates that the function should assert the corresponding interface trigger line.

A clear bit indicates that the function should deassert the corresponding interface trigger line:

| <u>Constant</u> | Description                            |
|-----------------|--|
| EPC_ECL_TRIG0   | ECL trigger 0 (EPC-7 only).            |
| EPC_ECL_TRIG1   | ECL trigger 1 (EPC-7 only).            |
| EPC_TTL_TRIG0   | TTL trigger 0 (EPC-7 and VXLink only). |
|                 |  |
| EPC_TTL_TRIG7   | TTL trigger 7 (EPC-7 and VXLink only). |

The function directly affects the interface trigger line state. interface trigger line state reflects the state of bits in the interface's trigger line drive registers. Actual bus trigger line state is an OR'd combination of the states all devices on the bus. If the interface asserts a trigger line, the actual bus trigger line transitions from deasserted to asserted only if all other devices on the bus have previously deasserted the line. Likewise, if the interface deasserts a trigger line, the actual bus control line transitions from asserted to deasserted only if all devices on the bus have previously deasserted the line.

**Return Value** The function returns a EPConnect return value:

|          | EPC_INV_MASK                                   | The parameter Trigger_Mask is invalid.                  |
|----------|--|---|
|          | EPC_INV_SESSION                                | The specified Session_ID is invalid.                    |
|          | EPC_INV_SW                                     | The Bus Manager device driver is not present.           |
|          | EPC_LOCKED                                     | Shared interface hardware is locked by another session. |
|          | EPC_SUCCESS                                    | The function completed successfully.                    |
| See Also | EpcGetBusTriggers, Epo<br>EpcPulseEpcTriggers. | :GetEpcTriggers, EpcOpenSession,                        |

#### **EpcSetEventEnableMask**

**Description** Defines a session's enabled event mask attribute.

C Synopsis

#include "busmgr.h"

short
EpcSetEventEnableMask(unsigned long Session\_ID, unsigned
long Event\_Mask);

| Session_ID | Session_ID specifies a session. |
|------------|---------------------------------|
| Event_Mask | Event_Mask specifies a mask of  |
|            | enabled events.                 |

Visual Basic Synopsis

Declare Function EpcSetEventEnableMask% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Event\_Mask&)

**Remarks** EpcSetEventEnableMask sets the specified session's enabled event mask attribute to *Event\_Mask*.

The *Event\_Mask* parameter is a bit mask where each bit corresponds to an event. The *Event\_Mask* parameter should be either zero or an OR'd combination of the following constants:

### EpcSetEventEnableMask

| <u>Event</u>      | Description  |
|-------------------|--|
| EPC_MSG_INT       | Message interrupt (EPC-7 and EPC-8 only)               |
| EPC_VME1_INT      | VMEbus interrupt 1                                     |
|                   |  |
| EPC_VME7_INT      | VMEbus interrupt 7                                     |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                           |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0 interrupt<br>(EPC-7 only)         |
|                   |  |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7 interrupt<br>(EPC-7 only)         |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                  |
| EPC_ACFAIL_ERR    | VMEbus power failure error                             |
| EPC_BERR_ERR      | VMEbus access error                                    |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                   |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration error (EPC-7 and EPC-8 only) |
| EPC_EXT_TRIG0_INT | External trigger 0 interrupt (VXLink only)             |
| EPC_EXT_TRIG1_INT | External trigger 1 interrupt<br>(VXLink only)          |

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| Return Value | The function returns a Bus Management return value: |  |
|--------------|---|--|
|              | EPC_INV_MASK  | <i>Event_Mask</i> contains enabled events that are not valid for this interface. |
|              | EPC_INV_SESSION                                     | The specified Session_ID is invalid.   |
|              | EPC_INV_SW  | The BusManager device driver is not present.                                     |
|              | EPC_SUCCESS   | The function completed successfully.   |
| See Also     | EpcGetEventEnableMask, EpcOpenSession.              |  |
| Example      | See EpcGetEventEnab                                 | leMask.  |

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#### **EpcSetEventHandler**

**Description** Defines an entry in a session's event handler array.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcSetEventHandler( unsigned long Session\_ID, unsigned long Event\_Mask, void (FAR \* Event\_Handler) (unsigned long, unsigned long, unsigned long, unsigned long),

void FAR \*Stack\_Ptr);Session\_IDSession\_ID specifies a session.Event\_MaskEvent\_Mask specifies an event.Event\_HandlerEvent\_Handler specifies an event<br/>handler.

*Stack\_Ptr* specifies an event handler stack pointer.

Visual Basic Synopsis

Stack\_Ptr

Declare Function EpcSetEventHandler% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Event\_Mask&, Event\_Handler As Any, Stack\_Ptr As Any);

**Remarks** EpcSetEventHandler sets the specified session's specified event handler array entry to *Event\_Handler* and the event handler's stack pointer to *Stack\_Ptr.* 

The *Event\_Mask* parameter is a bit mask where each bit corresponds to an event. The *Event\_Mask* parameter should be one of the following constants:

| <u>Event</u>      | Description  |
|-------------------|--|
| EPC_MSG_INT       | Message interrupt (EPC-7 and EPC-8 only)               |
| EPC_VME1_INT      | VMEbus interrupt 1                                     |
|                   |  |
| EPC_VME7_INT      | VMEbus interrupt 7                                     |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                           |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0 interrupt<br>(EPC-7 only)         |
| •••               |  |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7 interrupt<br>(EPC-7 only)         |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                  |
| EPC_ACFAIL_ERR    | VMEbus power failure error                             |
| EPC_BERR_ERR      | VMEbus access error                                    |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                   |
| EPC_EXT_TRIG0_INT | External trigger 0 interrupt (VXLink only)             |
| EPC_EXT_TRIG1_INT | External trigger 1 interrupt<br>(VXLink only)          |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration error (EPC-7 and EPC-8 only) |

The *Event\_Handler* parameter is a pointer to an event handler function with the following call semantics:

#### void FAR

The event handler function should return to the caller using a normal RET instruction. It should not attempt to return using an IRET instruction.

The value passed in the event handler function's *Session\_ID* parameter specifies the session that received the event. The value passed in the event handler function's *Handler\_Mask* parameter specifies the event that caused execution of the event handler function.

Whether or not the event handler function receives a meaningful *Handler\_Data* parameter depends on the value of *Handler\_Mask*:

| <u>Handler_Mask</u>              | Handler_Data  |
|----------------------------------|---|
| EPC_MSG_INT                      | 0   |
| EPC_VME1_INT<br><br>EPC_VME7_INT | VMEbus interrupt status/id (zero-<br>extended to 32 bits) |
| EPC_SIGNAL_INT                   | VXIbus signal data (zero extended to 32 bits)             |
| EPC_TTL_TRIG0_INT                | 0   |
| <br>EPC_TTL_TRIG7_INT            |   |
| EPC_ACFAIL_ERR                   | 0   |
| EPC_BERR_ERR                     | 0   |
| EPC_SYSFAIL_ERR                  | 0   |
| EPC_WATCHDOG_ERR                 | 0   |
| EPC_SYSRESET_ERR                 | 0   |
| EPC_EXT_TRIG0_INT                | 0   |
| EPC_EXT_TRIG1_INT                | 0   |

The *Stack\_Ptr* parameter is a pointer to the bottom of a block of memory reserved for use as a stack.

Defining a NULL event handler and/or event handler stack pointer effectively removes any previously assigned event handler and event handler stack pointer.

Defining an event handler and an event handler stack pointer does not enable or disable reception of the corresponding event. A separate call to **EpcSetEventEnableMask** is required.

Bus Management for Windows calls an event handler exactly once for each occurrence of its corresponding event and disables virtual processor interrupts before an event handler is called. The table below describes the algorithm used by EPConnect/VXI in processing each event type:

. .

. . .

| Event                            | Algorithm   |  |
|----------------------------------|---|--|
| EPC_MSG_INT                      | For each session with the event<br>enabled and a handler installed, the<br>IRQ handler disables the event and<br>calls the installed event handler.   |  |
|                                  | To receive additional message<br>interrupt events, a session must<br>re-enable the event. To avoid<br>redundant message interrupt events,<br>a session should only re-enable the<br>event after receiving a word serial<br>command (using<br><b>EpcSrvReceiveWSCommand</b> ) or<br>sending a word serial command<br>response (using<br><b>EpcSrvSendWSResponse</b> ). |  |
| EPC_VME1_INT<br><br>EPC_VME7_INT | The IRQ handler acknowledges the<br>VMEbus interrupt and gets the<br>status/id data. For each session<br>with the event enabled and a<br>handler installed, the IRQ handler<br>calls the installed event handler.<br>Additional events occur whenever<br>additional VMEbus interrupts are<br>asserted on the bus.   |  |

| EPC_SIGNAL_INT                             | The IRQ handler gets the signal<br>data from the signal FIFO. For<br>each session with the event enabled<br>and a handler installed, the IRQ<br>handler calls the installed event<br>handler  |  |
|--|---|--|
|  | Additional events occur whenever a device writes to the interface's signal FIFO.  |  |
| EPC_TTL_TRIG0_INT<br><br>EPC_TTL_TRIG7_INT | For each session with the event<br>enabled and a handler installed, the<br>IRQ handler disables the event and<br>calls the installed event handler.   |  |
|  | To receive additional TTL trigger<br>interrupt events for a specific TTL<br>trigger, a session must re-enable the<br>event. To ensure that a previous<br>TTL trigger assertion does not<br>cause redundant events, a session<br>should wait for the deassertion of<br>the corresponding TTL trigger latch<br>bit (using <b>EpcGetMiscAttributes</b> )<br>before re-enabling a TTL trigger<br>interrupt event. |  |

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For each session with the event enabled and a handler installed, the IRQ handler disables the event and calls the installed event handler.

To receive additional ACFAIL error events, a session must re-enable the event. To ensure that the previous ACFAIL assertion does not cause redundant events, a session should wait for the deassertion of the ACFAIL bus control line (using **EpcGetBusLines**) before re-enabling the ACFAIL error event.

#### EPC\_BERR\_ERR

EPC\_ACFAIL ERR

The IRQ handler clears the BERR condition. For each session with the event enabled and a handler installed, the IRQ handler calls the installed event handler.

Additional BERR error events occur whenever the interface makes a bus access that terminates with a BERR condition.

| EPC_SYSFAIL_ERR  | For each session with the event<br>enabled and a handler installed, the<br>IRQ handler disables the event and<br>calls the installed event handler.   |  |  |
|------------------|---|--|--|
|                  | To receive additional SYSFAIL<br>error events, a session must<br>re-enable the event. To ensure that<br>the previous SYSFAIL assertion<br>does not cause a redundant event, a<br>session should wait for the<br>deassertion of the SYSFAIL bus<br>control line (using<br><b>EpcGetBusLines</b> ) before<br>re-enabling the SYSFAIL error<br>event.  |  |  |
| EPC_WATCHDOG_ERR | For each session with the event<br>enabled and a handler installed, the<br>IRQ handler disables the event and<br>calls the installed event handler.   |  |  |
|                  | To receive additional watchdog<br>timer error events, a session must<br>re-enable the watchdog timer error<br>event. To ensure that the previous<br>watchdog timer expiration does not<br>cause redundant events, a session<br>should reset the watchdog timer<br>(using EpcWatchdogTimer)<br>before re-enabling the watchdog<br>timer error event. |  |  |

EPC\_SYSRESET\_ERR The IRQ handler re-initializes the hardware interface. For each session with the event enabled and a handler installed, the IRQ handler disables the event and calls the installed event handler. To receive additional SYSRESET

error events, a session must re-enable the event. To ensure that the previous SYSRESET assertion does not cause redundant events, a session should wait for the deassertion of the SYSRESET bus control line (using EpcGetBusLines) before reenabling the SYSRESET error event.

#### EPC\_EXT\_TRIG0\_INT EPC\_EXT\_TRIG1\_INT

For each session with the event enabled and a handler installed, the IRQ handler disables the event and calls the installed event handler.

Additional events occur whenever additional external trigger events are detected.

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| Return Value | The function returns a Bus Management return value:   |   |  |
|--------------|---|---|--|
|              | EPC_INV_MASK  | <i>Event_Mask</i> contains more than one event or contains an event that is not valid for this EPC. |  |
|              | EPC_INV_SESSION   | The specified Session_ID is invalid.  |  |
|              | EPC_SUCCESS   | The function completed successfully.  |  |
| See Also     | EpcGetBusLines, EpcGetEventHandler,<br>EpcGetMiscAttributes, EpcOpenSession,<br>EpcSetEventEnableMask, EpcSrvReceiveWSCommand,<br>EpcSrvSendWSResponse, EpcWatchdogTimer. |   |  |
| Example      | See EpcGetEventEnableMask.  |   |  |

#### **EpcSetLockingTimeout**

**Description** Defines a session's locking timeout.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcSetLockingTimeout(unsigned long Session\_ID, unsigned long
Timeout);

| Session_ID | Session_ID specifies a session.      |
|------------|--------------------------------------|
| Timeout    | Timeout specifies a locking timeout. |

Visual Basic Synopsis

Declare Function EpcSetLockingTimeout% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Timeout&)

**Remarks** EpcSetLockingTimeout defines the specified session's locking timeout.

Timeout specifies the session's locking timeout, in milliseconds.

By default, a session has a locking timeout of zero milliseconds. When the session encounters a locking conflict, an **EPC\_LOCKED** error is returned immediately.

**Return Value** The function returns a Bus Management return value:

EPC\_INV\_SESSIONThe specified Session\_ID is invalid.EPC\_SUCCESSThe function completed successfully.

- See Also EpcGetLockingTimeout, EpcLockSession, EpcOpenSession.
- **Example** See EpcGetLockingTimeout.

#### **EpcSetMiscAttributes**

**Description** Defines the interface's miscellaneous configuration attributes.

C Synopsis

#include "busmgr.h"

#### short FAR PASCAL

**EpcSetMiscAttributes**(unsigned long *Session\_ID*, unsigned long *Misc\_Mask*);

| Session_ID | Session_ID specifies a session.  |
|------------|--|
| Misc_Mask  | <i>Misc_Mask</i> specifies miscellaneous interface configuration attributes. |

#### Visual Basic Synopsis

Declare Function EpcSetMiscAttributes% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Misc\_Mask&)

**Remarks EpcSetMiscAttributes** defines miscellaneous interface configuration attributes.

*Misc\_Mask* is either zero or an OR'd bit mask of the following constants, where a set bit indicates that the function should assert the corresponding miscellaneous interface attribute bit. A clear bit indicates that the function should deassert the corresponding miscellaneous interface attribute bit:

| <u>Constant</u> | Description  |
|-----------------|--|
| EPC_DIR         | Word serial byte transfer protocol<br>DIR bit. Asserting the bit indicates<br>that the interface is ready to receive<br>data from its commander device.<br>Supported on EPC-7 and EPC-8<br>only. |

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| 2 |  |
|---|--|
|   |  |

| EPC_DOR               | Word serial byte transfer protocol<br>DOR bit. Asserting the bit<br>indicates that the interface is ready<br>to send data to its commander<br>device. Supported on EPC-7 and<br>EPC-8 only.  |
|-----------------------|--|
| EPC_ERR               | Word serial protocol ERR* bit.<br>Asserting the bit indicates to the<br>commander device that the<br>interface has detected a word serial<br>protocol error. Supported on<br>EPC-7 and EPC-8 only.   |
| EPC_LOCK              | Message-based device Locked* bit.<br>Asserting the bit indicates that the<br>commander device has locked<br>access to the interface from other<br>local sources. Supported on EPC-7<br>and EPC-8 only.   |
| EPC_MULTIPLE_LOCK     | Word serial protocol extension   |
|                       | multiple commander lock bit.<br>When asserted, the first<br>commander to read the asserted bit<br>from the interface's Response<br>register can safely send a word<br>serial command. Supported on<br>EPC-7 and EPC-8 only.  |
| EPC_PASS              | When asserted, the first<br>commander to read the asserted bit<br>from the interface's Response<br>register can safely send a word<br>serial command. Supported on   |
| EPC_PASS<br>EPC_READY | When asserted, the first<br>commander to read the asserted bit<br>from the interface's Response<br>register can safely send a word<br>serial command. Supported on<br>EPC-7 and EPC-8 only.<br>Device initialization PASSED bit.<br>Asserting the bit indicates that the |

#### **EpcSetMiscAttributes**

| EPC_RRDY        | Word serial protocol Read Ready<br>bit. Asserting the bit indicates to a<br>commander device that the<br>interface has a word serial response<br>in its message register. Supported<br>on EPC-7 and EPC-8 only.       |
|-----------------|---|
| EPC_RSRC_MGR    | Interface resource manager<br>execution bit. Asserting the bit<br>indicates that resource manager<br>execution is complete.   |
| EPC_STICKY_BERR | "Sticky" bus error bit. When<br>asserted, the bit indicates that a bus<br>error has occurred since the bit was<br>last deasserted. This bit cannot be<br>asserted directly by software; it can<br>only be deasserted. |
| EPC_SYSFAIL_OUT | SYSFAIL output enable bit. When<br>asserted, the interface can assert<br>SYSFAIL. When deasserted, the<br>interface cannot assert SYSFAIL.  |
| EPC_SYSRESET_IN | SYSRESET input enable bit.<br>When asserted, asserting<br>SYSRESET resets the interface.<br>When deasserted, asserting<br>SYSRESET does not reset the<br>interface.   |
| EPC_WRDY        | Word serial protocol Write Ready<br>bit. Asserting the bit indicates to a<br>commander device that the<br>interface is ready to receive a word<br>serial command. Supported on<br>EPC-7 and EPC-8 only.               |

Deasserting **EPC\_PASS** while asserting **EPC\_SYSFAIL\_OUT** causes the interface to assert SYSFAIL on the bus.

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| Return Value | The function returns a Bus Management return value: |   |  |
|--------------|---|---|--|
|              | EPC_INV_MASK  | The parameter <i>Misc_Mask</i> is invalid.              |  |
|              | EPC_INV_SESSION                                     | The parameter Session_ID is invalid.                    |  |
|              | EPC_INV_SW  | The BusManager device driver is not present.            |  |
|              | EPC_INV_TIMEOUT                                     | An invalid timeout was encountered.                     |  |
|              | EPC_LOCKED  | Shared interface hardware is locked by another session. |  |
|              | EPC_SUCCESS   | The function completed successfully.                    |  |
| See Also     | EpcGetMiscAttributes, EpcOpenSession.               |   |  |
| Example      | See EpcGetBusAttributes.                            |   |  |

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### **EpcSetSessionData**

| Description       | Defines a session's application-specified data.  |                                |                  |           |  |
|-------------------|--|--------------------------------|------------------|-----------|--|
| C Synopsis        | #include ''busmgr.h''  |                                |                  |           |  |
|                   | <pre>short FAR PASCAL EpcSetSessionData( unsigned long Session_ID,</pre>   |                                |                  |           |  |
|                   | Session_ID   | Session_ID session.            | specifies an     | open      |  |
|                   | Session_Data   | Session_Data a application-spe |                  | session's |  |
| Visual Basic Sync | opsis  |                                |                  |           |  |
|                   | Declare Function<br>EpcSetSessionData% Lib "bmvxiw16.dll" (ByVal Session_ID&,<br>ByVal Session_Data&)  |                                |                  |           |  |
| Remarks           | <b>EpcSetSessionData</b> def application-specified data.   | ines the                       | specified        | session's |  |
|                   | The application-specified data is a 4-byte quantity.   |                                |                  |           |  |
|                   | Typically, an application uses <b>EpcSetSessionData</b> to store a pointer to one of its data structures. Later, the application use <b>EpcGetSessionData</b> to quickly retrieve the pointer durin performance-critical operations (like event handling). |                                |                  |           |  |
| Return Value      | • The function returns a Bus Management return value:  |                                |                  |           |  |
|                   | EPC_INV_SESSION  | The specified S                | Session_ID is in | nvalid.   |  |
|                   | EPC_SUCCESS  | The function co                | ompleted succe   | essfully. |  |
| See Also          | EpcGetSessionData, EpcO  | penSession.                    |                  |           |  |
| Example           | See EpcCloseSession.   |                                |                  |           |  |

#### **EpcSetSlaveMapping**

**Description** Defines the interface's slave memory mapping.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcSetSlaveMapping( unsigned long Session\_ID, unsigned short Address\_Space, unsigned long Base Address);

Session\_ID

Address\_Space

Base\_Address

memory address space. Base\_Address specifies a slave memory base address.

Session\_ID specifies a session.

Address\_Space specifies a slave

Visual Basic Synopsis

Declare Function EpcSetSlaveMapping% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Address\_Space%, ByVal Base\_Address&)

**Remarks** EpcSetSlaveMapping defines the mapping of the interface's slave memory to the bus.

Address\_Space specifies whether the interface's slave memory appears on the bus, and if so, in which address space. Base\_Address specifies the base address of the interface's slave memory in the given Address\_Space.

Valid combinations of *Address\_Space* and *Base\_Address* are dependent on the interface type:

| <u>Interface</u><br><u>Type</u> | <u>Address_Space</u> | <u>Base_Address</u>                     |
|---------------------------------|----------------------|---|
| EPC-7                           | EPC_DISABLED         | N/A                                     |
|                                 | EPC_A24              | 0x00000000, 0x00400000,<br>, 0x00C00000 |
|                                 | EPC_A32              | 0x00000000, 0x01000000,<br>, 0xFF000000 |
| EPC-8                           | EPC_DISABLED         | N/A                                     |
| VXLink                          | EPC_DISABLED         | N/A                                     |

A24 base addresses are aligned on a 4 Mbyte boundary, and only the first 4 Mbytes of the interface's slave memory is mapped to the bus. A32 base addresses are aligned on a 16 Mbyte boundary, and only the first 16 Mbytes of the interface's slave memory is mapped to the bus.

**Return Value** The function returns a Bus Management return value:

|          | EPC_INV_BASE<br>EPC_INV_SESSION     | The parameter <i>Base_Address</i> is invalid.<br>The parameter <i>Session_ID</i> is invalid. |
|----------|-------------------------------------|--|
|          | EPC_INV_SPACE                       | The parameter <i>Address_Space</i> is invalid.   |
|          | EPC_INV_SW                          | The BusManager device driver is not present.   |
|          | EPC_INV_TIMEOUT                     | An invalid timeout was encountered.  |
|          | EPC_LOCKED                          | Shared interface hardware is locked by another session.                                      |
|          | EPC_SUCCESS                         | The function completed successfully.   |
| See Also | EpcGetSlaveMapping, EpcOpenSession. |  |

**Example** See EpcGetBusAttributes.

#### EpcSetULA

**Description** Defines the interface's unique logical address.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcSetULA(unsigned long Session\_ID, unsigned short ULA);

| Session_ID | Session_ID specifies a session.                       |  |
|------------|---|--|
| ULA        | ULA specifies the interface's unique logical address. |  |
|            | unique logical address.                               |  |

Visual Basic Synopsis

Declare Function EpcSetULA% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal ULA%)

**Remarks** EpcSetULA defines the interface's unique logical address.

Valid unique logical address values are 0x00 through 0xFF.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_SESSION | The parameter <i>Session_1D</i> is invalid.             |
|-----------------|---|
| EPC_INV_TIMEOUT | An invalid timeout was encountered.                     |
| EPC_INV_ULA     | The parameter ULA is invalid.                           |
| EPC_INV_SW      | The BusManager device driver is not present.            |
| EPC_LOCKED      | Shared interface hardware is locked by another session. |
| EPC_SUCCESS     | The function completed successfully.                    |

See Also EpcGetULA, EpcOpenSession.

**Example** See EpcGetBusAttributes.

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## **EpcSrvEnableWSCommand**

**Description** Enables word serial command reception.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcSrvEnableWSCommand

(unsigned long Session\_ID, unsigned short Enable\_Next\_Command);

Session\_ID

Session\_ID specifies a session.

Enable\_Next\_Command

*Enable\_Next\_Command* specifies the type of word serial command reception to enable.

Visual Basic Synopsis

Declare Function EpcSrvEnableWSCommand% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Enable\_Next\_Command%)

**Remarks** EpcSrvEnableWSCommand configures the interface hardware to receive a word serial command.

The following constants specify valid values for the *Enable\_Next\_Command* parameter:

## **EpcSrvEnableWSCommand**

| <u>Constant</u> | Description   |
|-----------------|---|
| EPC_DISABLE_ALL | Disable word serial command reception.  |
| EPC_ENABLE_WRDY | Enable word serial command<br>reception by asserting WRDY and<br>deasserting both DIR and DOR.  |
| EPC_ENABLE_DIR  | Enable word serial command<br>reception and data input by asserting<br>both WRDY and DIR and<br>deasserting DOR (EPC-7 and EPC-8<br>only).  |
| EPC_ENABLE_DOR  | Enable word serial command<br>reception and data output by<br>asserting both WRDY and DOR and<br>deasserting DIR (EPC-7 and EPC-8<br>only). |
| EPC_ENABLE_ALL  | Enable word serial command<br>reception, data input, and data output<br>by asserting WRDY, DIR, and DOR<br>(EPC-7 and EPC-8 only).          |

Disabling word serial command reception when it is already enabled and without receiving a word serial command can result in a word serial protocol violation by allowing the commander device to write an unexpected word serial command.

On an EPC-7, enabling word serial command reception when an outgoing word serial command response remains unread in the interface's message registers can result in a word serial protocol violation by allowing the commander device to write over the word serial command response. Enabling word serial command reception when it is already enabled can result in a word serial protocol violation. In particular, enabling word serial command reception with DIR deasserted when word serial command reception is already enabled with DIR asserted can generate a DIR violation. Likewise, enabling word serial command reception with DOR deasserted when word serial command reception is already enabled with DOR asserted can generate a DOR violation.

EPConnect does not support enabling word serial command reception on a VXLink interface. Attempting to use the function on a VXLink interface results in an EPC\_INV\_HW error.

**Return Value** The function returns a Bus Management return value:

|          | EPC_INV_ENABLE         | The parameter <i>Enable_Next_Command</i> is invalid.                   |
|----------|------------------------|--|
|          | EPC_INV_HW             | The interface does not support enabling word serial command reception. |
|          | EPC_INV_SESSION        | The specified Session_ID is invalid.                                   |
|          | EPC_INV_SW             | The BusManager device driver is not present.                           |
|          | EPC_LOCKED             | Shared interface hardware is locked by another session.                |
|          | EPC_SUCCESS            | The function completed successfully.                                   |
| See Also | EpcOpenSession, EpcSrv | vReceiveWSCommand.   |

## EpcSrvReceiveWSCommand

**Description** Receives a word serial command from a commander device.

C Synopsis

#include "busmgr.h"

| short FAR PASCAL<br>EpcSrvReceiveWSComm<br>(unsigned long<br>void FAR *<br>unsigned short FA<br>unsigned short<br>unsigned long | Session_ID,<br>Command_Ptr,  |
|---|--|
| Session_ID  | Session_ID specifies a session.  |
| Command_Ptr   | <i>Command_Ptr</i> specifies a location where the word serial command will be placed.  |
| Command_Size_Ptr  | <i>Command_Size_Ptr</i> specifies a location where the size of the word serial command will be placed.                             |
| Enable_Next_Command   | <i>Enable_Next_Command</i> specifies<br>whether to enable the interface<br>hardware to receive the another word<br>serial command. |
| Timeout   | <i>Timeout</i> specifies the number of milliseconds to wait for a word serial command.   |

#### Visual Basic Synopsis

Declare Function EpcSrvReceiveWSCommand% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Command\_Ptr As Any, Command\_Size\_Ptr%, ByVal Enable\_Next\_Command%, ByVal Timeout&) **Remarks** EpcSrvReceiveWSCommand receives a word serial command and places the command and its size in the locations pointed to by *Command\_Ptr* and *Command\_Size\_Ptr*, respectively. The function then configures the interface hardware for future word serial command reception.

*Command\_Size\_Ptr* points to a location where the function places the size of the received word serial command:

| *Command_Size_Ptr | Description  |
|-------------------|--|
| EPC_16_BIT        | Received a 16-bit word serial command.                   |
| EPC_32_BIT        | Received a 32-bit long word serial command (EPC-7 only). |

The following constants specify valid values for the *Enable\_Next\_Command* parameter:

## **EpcSrvReceiveWSCommand**

| Constant        | Description   |
|-----------------|---|
| EPC_DISABLE_ALL | Disable word serial command reception.  |
| EPC_ENABLE_WRDY | Enable word serial command<br>reception by asserting WRDY and<br>deasserting both DIR and DOR.                    |
| EPC_ENABLE_DIR  | Enable word serial command<br>reception and data input by asserting<br>both WRDY and DIR and<br>deasserting DOR.  |
| EPC_ENABLE_DOR  | Enable word serial command<br>reception and data output by<br>asserting both WRDY and DOR and<br>deasserting DIR. |
| EPC_ENABLE_ALL  | Enable word serial command<br>reception, data input, and data output<br>by asserting WRDY, DIR, and DOR.          |

Word serial command reception must be enabled <u>before</u> attempting to receive a word serial command. Otherwise, **EpcSrvReceiveWSCommand** returns invalid word serial command data. Use **EpcSrvEnableWSCommand** to enable initial word serial command reception.

Occasionally, it's useful to receive a word serial command without destroying the contents of the interface's message registers. To receive a word serial command without destroying the contents of the interface's message registers, use EpcSrvReceiveWSCommand with an Enable\_Next\_Command parameter value of EPC DISABLE ALL. This allows a subsequent call to EpcSrvReceiveWSCommand to receive the same word serial command. Note that either enabling word serial command reception or (on an EPC-7) sending a word serial command response overwrites the contents of the interface's message registers, destroying any data preserved there.

On an EPC-7 or EPC-8, the function returns EPC\_DIR\_ERR when a Byte Available or Trigger word serial command is received and the interface is not enabled for data input (e.g., interface's DIR bit is clear). Likewise, on an EPC-7 or EPC-8, the function returns EPC\_DOR\_ERR when a Byte Request word serial command is received and the interface is not enabled for data output (e.g., the interface's DOR bit is clear). Use EpcSrvSendWSProtocolError to send protocol errors to the commander device.

EPConnect does not support receiving a word serial command on a VXLink interface. Attempting to use the function on a VXLink interface results in an EPC\_INV\_HW error.

**Return Value** The function returns a Bus Management return value:

| EPC_DIR_ERR      | A word serial command protocol DIR violation error occurred.                 |
|------------------|--|
| EPC_DOR_ERR      | A word serial command protocol DOR violation error occurred.                 |
| EPC_INV_ENABLE   | The parameter<br>Enable_Next_Command is invalid.                             |
| EPC_INV_HW       | The interface does not support<br>enabling word serial command<br>reception. |
| EPC_INV_PTR      | One or more of parameters<br>Command_Ptr and<br>Command_Size_Ptr is invalid. |
| EPC_INV_SESSION  | The specified Session_ID is invalid.   |
| EPC_INV_SW       | The BusManager device driver is not present.                                 |
| EPC_LOCKED       | Shared interface hardware is locked by another session.                      |
| EPC_RECV_TIMEOUT | A timeout occurred waiting for a word serial command.                        |
| EPC_SUCCESS      | The function completed successfully.   |

See Also EpcOpenSession, EpcSrvEnableWSCommand, EpcSrvSendWSResponse.

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## **EpcSrvSendProtocolEvent**

**Description** Sends a protocol event to the commander device.

C Synopsis

| #include ''busmgr.h''                   |                  |  |   |
|---|------------------|--|---|
| short FAR PASCAL<br>EpcSrvSendProtocolE | Event(           | unsigned long<br>unsigned short<br>unsigned long<br>unsigned short | Session_ID,<br>ULA,<br>Method_Mask,<br>Protocol_Event); |
| Session_ID                              | Sess             | ion_ID specifies a   | session.  |
| ULA                                     |                  | specifies the unic<br>ess of the comma                             |   |
| Method_Mask                             |                  | hod_Mask specifie<br>for sending the pro                           |   |
| Protocol_Event                          | <i>Prot</i> even | - •  | ifies a protocol  |

ULA is unused when Method\_Mask specifies a VMEbus interrupt.

Visual Basic Synopsis

EpcSrvSendProtocolEvent% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal ULA%, ByVal Method\_Mask&, ByVal Protocol\_Event%)

**Remarks** EpcSrvSendProtocolEvent sends the VMEbus protocol event *Protocol\_Event* to the commander device at unique logical address *ULA*. *Method\_Mask* specifies the method to use for sending the protocol event. Valid values are:

| <u>Method_Mask</u> | Description                                      |
|--------------------|--|
| EPC_VME1_INT       | VMEbus interrupt 1 (EPC-7 only).                 |
| •••                |  |
| EPC_VME7_INT       | VMEbus interrupt 7 (EPC-7 only).                 |
| EPC_SIGNAL_REG     | Write to the commander device's signal register. |

The function always overwrites the lower eight bits of the specified *Protocol\_Event* parameter with the unique logical address of the interface.

On an EPC-7, using a VMEbus interrupt to send a protocol event requires the use of the EPC-7's message high register. Receiving 32bit long word serial commands also requires the use of the EPC-7's message high register. Therefore, using a VMEbus interrupt to send a protocol event while simultaneously receiving 32-bit long word serial commands can have unpredictable results. Note, however, that no conflict occurs when using a VMEbus interrupt to send a protocol event while simultaneously receiving 16-bit word serial commands.

|   | Return Value | The function returns a Bus Management return value: |  |
|---|--------------|---|--|
| 2 |              | EPC_BERR  | A bus error occurred writing the protocol event into the commander device's signal register. |
|   |              | EPC_INV_ASSERT                                      | The interface is already asserting a VMEbus interrupt.                                       |
|   |              | EPC_INV_EVENT                                       | The parameter <i>Protocol_Event</i> is invalid.  |
|   |              | EPC_INV_METHOD                                      | The parameter Method_Mask is invalid.  |
|   |              | EPC_INV_SESSION                                     | The specified Session_ID is invalid.   |
|   |              | EPC_INV_SW  | The BusManager device driver is not present.   |
|   |              | EPC_INV_ULA   | The parameter ULA is invalid.  |
|   |              | EPC_LOCKED  | Shared interface hardware is locked by another session.                                      |
|   |              | EPC_SUCCESS   | The function completed successfully.   |
|   | See Also     | EpcOpenSession.                                     |  |

## EpcSrvSendWSProtocolError

**Description** Sends a word serial protocol error to the commander device.

C Synopsis

## #include "busmgr.h"

#### short EncSrySendWSProtocolError

| EpcSrvSendWSProtocolE | rror  |
|-----------------------|---|
|                       | (unsigned long Session_ID,<br>unsigned short Protocol_Error,<br>unsigned short Enable_Next_Command,<br>unsigned long Timeout);  |
| Session_ID            | Session_ID specifies a session.   |
| Protocol_Error        | <i>Protocol_Error</i> specifies a <i>Read</i><br><i>Protocol Error</i> word serial command<br>response.   |
| Enable_Next_Command   | <i>Enable_Next_Command</i> specifies<br>whether to enable the interface<br>hardware to receive the another word<br>serial command.  |
| Timeout               | <i>Timeout</i> specifies the number of milliseconds to wait for a word serial command. <i>Timeout</i> also specifies the number of milliseconds to wait for a commander to read a word serial command response. |

Visual Basic Synopsis

Declare Function EpcSrvSendWSProtocolError% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Protocol\_Error%, ByVal Enable\_Next\_Command%, ByVal Timeout&)

- **Remarks** EpcSrvSendWSProtocolError notifies a commander device that a word serial protocol error has occurred by asserting the interface's response register Write Ready and Err\* bits. The function then either:
  - receives a READ PROTOCOL ERROR word serial command, deasserts the interface's response register Err\* bit, sends the specified *Protocol\_Error* response, and optionally enables reception of the next word serial command, or
  - receives an ABORT NORMAL OPERATION word serial command, deasserts the interface's response register Err\* bit, and returns EPC\_RECV\_ANO.
  - receives a CLEAR word serial command, deasserts the interface's response register Err\* bit, and returns EPC\_RECV\_CLEAR.
  - receives an END NORMAL OPERATION word serial command, deasserts the interface's response register Err\* bit, and returns EPC\_RECV\_ENO.

All word serial commands received while the interface is waiting for either a READ PROTOCOL ERROR word serial command, an ABORT NORMAL OPERATION, a CLEAR, or an END NORMAL OPERATION are discarded.

## EpcSrvSendWSProtocolError

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| The  | following   | constants   | specify | valid | values | for | the |
|------|-------------|-------------|---------|-------|--------|-----|-----|
| Enab | le_Next_Con | nmand parar | neter:  |       |        |     |     |

| <u>Constant</u> | Description  |
|-----------------|--|
| EPC_DISABLE_ALL | Disable word serial command reception.   |
| EPC_ENABLE_WRDY | Enable word serial command reception<br>by asserting WRDY and deasserting<br>both DIR and DOR.                 |
| EPC_ENABLE_DIR  | Enable word serial command reception<br>and data input by asserting both<br>WRDY and DIR and deasserting DOR.  |
| EPC_ENABLE_DOR  | Enable word serial command reception<br>and data output by asserting both<br>WRDY and DOR and deasserting DIR. |
| EPC_ENABLE_ALL  | Enable word serial command<br>reception, data input, and data output<br>by asserting WRDY, DIR, and DOR.       |

On an EPC-7, any outgoing word serial command response must be read from the interface's message registers by the interface's commander device <u>before</u> attempting to notify a commander device that a word serial protocol error has occurred. Otherwise, additional word serial protocol violations can occur. Successful completion of **EpcSrvSendWSResponse** indicates that an outgoing word serial command response has been read from the interface's message registers.

EPConnect does not support sending a word serial protocol error on VXLink interfaces. Attempting to use the function on an unsupported interface results in an EPC\_INV\_HW error.

**Return Value** The function returns a EPConnect return value:

| EPC_INV_ENABLE | The parameter <i>Enable_Next_Command</i> is invalid. |
|----------------|--|
| EPC_INV_ERROR  | The parameter <i>Protocol_Error</i> is invalid.      |

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| EPC_INV_HW       | The interface hardware does not support sending a word serial protocol error. |
|------------------|---|
| EPC_INV_SESSION  | The specified Session_ID is invalid.  |
| EPC_INV_SW       | The Bus Manager device driver is not present.                                 |
| EPC_LOCKED       | Shared interface hardware is locked by another session.                       |
| EPC_RECV_ANO     | The interface received an ABORT<br>NORMAL OPERATION<br>command.               |
| EPC_RECV_CLEAR   | The interface received a CLEAR word serial command.                           |
| EPC_RECV_ENO     | The interface received an END<br>NORMAL OPERATION word<br>serial command.     |
| EPC_RECV_TIMEOUT | A timeout occurred receiving a word serial command.                           |
| EPC_SEND_TIMEOUT | A timeout occurred sending a word serial command response.                    |
| EPC_SUCCESS      | The function completed successfully.  |

See Also EpcOpenSession, EpcSrvSendWSResponse.

## EpcSrvSendWSResponse

**Description** Sends a word serial command response to the commander device.

C Synopsis

| #include "busmgr.h"   |  |
|---|--|
| short FAR PASCAL<br>EpcSrvSendWSResponse<br>(unsigned long<br>void FAR *<br>unsigned short<br>unsigned short<br>unsigned long | Session_ID,<br>Response_Ptr,<br>Response_Size,   |
| Session_ID  | Session_ID specifies a session.  |
| Response_Ptr  | <i>Response_Ptr</i> specifies the location of a word serial command response.  |
| Response_Size   | <i>Response_Size</i> specifies the size of the word serial command response.   |
| Enable_Next_Command   | <i>Enable_Next_Command</i> specifies<br>whether to enable the interface<br>hardware to receive the another word<br>serial command. |
| Timeout   | <i>Timeout</i> specifies the number of milliseconds to wait for a commander to read the word serial command response.              |

Visual Basic Synopsis

Declare Function EpcSrvSendWSResponse% Lib "bmvxiw16.dll" (ByVal Session\_ID&, Response\_Ptr As Any, ByVal Response\_Size%, ByVal Enable\_Next\_Command%, ByVal Timeout&) **Remarks** EpcSrvSendWSResponse optionally sends the word serial command response at the location pointer to by *Response\_Ptr* to a commander device. The function then configures the interface hardware for future word serial command reception.

*Response\_Size* specifies the size of the word serial command response:

| <u>Response_Size</u> | Description   |
|----------------------|---|
| EPC_16_BIT           | Send a 16-bit word serial command response.                   |
| EPC_32_BIT           | Send a 32-bit long word serial command response. (EPC-7 only) |

The following constants specify valid values for the *Enable\_Next\_Command* parameter:

| <u>Constant</u> | Description   |
|-----------------|---|
| EPC_DISABLE_ALL | Disable word serial command reception.  |
| EPC_ENABLE_WRDY | Enable word serial command<br>reception by asserting WRDY and<br>deasserting both DIR and DOR.                    |
| EPC_ENABLE_DIR  | Enable word serial command<br>reception and data input by asserting<br>both WRDY and DIR and<br>deasserting DOR.  |
| EPC_ENABLE_DOR  | Enable word serial command<br>reception and data output by<br>asserting both WRDY and DOR and<br>deasserting DIR. |
| EPC_ENABLE_ALL  | Enable word serial command<br>reception, data input, and data output<br>by asserting WRDY, DIR, and DOR.          |

On an EPC-7, sending a word serial command response while word serial command reception is enabled can result in a word serial protocol violation by allowing the commander device to write over the word serial command response.

Occasionally, it is useful to ensure that a word serial response has been read without destroying the contents of the interface's message registers. To ensure that a word serial response has been read without destroying the contents of the interface's message registers, use **EpcSrvSendWSResponse** with a *Response\_Ptr* parameter value of null and an *Enable\_Next\_Command* parameter value of **EPC\_DISABLE\_ALL**. Such a call tests that a word serial command response has been read from the interface's message registers without destroying the contents of the registers. Note that either enabling word serial command reception or sending a word serial command response overwrites the contents of the interface's message registers, destroying any data preserved there.

The function returns EPC\_MULTIPLE\_ERR if the *Response\_Ptr* parameter is not null and previously sent response data remains unread in the interface's message registers.

The function returns EPC\_SEND\_TIMEOUT if a commander device does not read the word serial command response within the specified timeout time. If this error occurs, the word serial command response remains in the interface message register.

EPConnect does not support sending a word serial command response on a VXLink interface. Attempting to use the function on a VXLink interface results in an EPC\_INV\_HW error.

| Return Value | The function returns a Bus Management return value: |  |
|--------------|---|--|
|              | EPC_INV_ENABLE                                      | The parameter <i>Enable_Next_Command</i> is invalid.                                 |
|              | EPC_INV_SESSION                                     | The specified <i>Session_ID</i> is invalid.  |
|              | EPC_INV_HW  | The interface does not support sending a word serial command response.               |
|              | EPC_INV_SIZE  | The parameter <i>Response_Size</i> is invalid.                                       |
|              | EPC_INV_SW  | The BusManager device driver is not present.   |
|              | EPC_LOCKED  | Shared interface hardware is locked by another session.                              |
|              | EPC_MULTIPLE_ERR                                    | A word serial protocol multiple queries error occurred.                              |
|              | EPC_SEND_TIMEOUT                                    | A timeout occurred waiting for a commander to read the word serial command response. |
|              | EPC_SUCCESS   | The function completed successfully.   |
| See Also     | EpcOpenSession, EpcSrvEnableWSCommand.              |  |

**Description** Byte-swaps a 16-bit value.

C Synopsis

2

#include "busmgr.h"

short FAR PASCAL
EpcSwap16(unsigned short FAR \*Value\_Ptr);

Value\_Ptr

Value\_Ptr specifies a location containing a 16-bit value to byte-swap.

Visual Basic Synopsis Declare Function EpcSwap16% Lib "bmvxiw16.dll" (Value\_Ptr%)

- **Remarks** EpcSwap16 byte-swaps the 16-bit value in the location pointed to by Value\_Ptr.
- **Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR | The paran         | neter Value_Pti  | r is invalid. |
|-------------|-------------------|------------------|---------------|
| EPC_SUCCESS | The<br>successful | function<br>lly. | completed     |

- See Also EpcSwapBuffer, EpcSwap32, EpcSwap48, EpcSwap64, EpcSwap80.
- **Example** See EpcSwapBuffer.

**Description** Byte-swaps a 32-bit value.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcSwap32(unsigned long FAR\*Value\_Ptr);

Value\_PtrValue\_Ptrspecifiesalocationcontaining a 32-bit value to byte-swap.

Visual Basic Synopsis Declare Function EpcSwap32% Lib "bmvxiw16.dll" (Value\_Ptr&)

| Remarks      | <b>EpcSwap32</b> byte-swaps the 32-bit value in the location pointed to by <i>Value_Ptr</i> . |                                      |
|--------------|---|--------------------------------------|
| Return Value | The function returns a Bus Management return value:   |                                      |
|              | EPC_INV_PTR   | The parameter Value_Ptr is invalid.  |
|              | EPC_SUCCESS   | The function completed successfully. |
| See Also     | EpcSwapBuffer, EpcSwap16, EpcSwap48, EpcSwap64,<br>EpcSwap80.                                 |                                      |
| Example      | See EpcSwapBuffer.  |                                      |

**Description** Byte-swaps a 48-bit value.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcSwap48(void FAR\*Value\_Ptr);

| Value_Ptr | Value_Ptr  | spe | cifies | a | locat | ion |
|-----------|------------|-----|--------|---|-------|-----|
|           | containing | а   | 48-bit | v | /alue | to  |
|           | byte-swap. |     |        |   |       |     |

Visual Basic Synopsis

Declare Function EpcSwap48% Lib "bmvxiw16.dll" (Value\_Ptr As Any)

| Remarks | EpcSwap48 byte-swaps the 48-bit value in the location pointed to |
|---------|--|
|         | by Value_Ptr.  |

#### **Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR | The par        | rameter Value_  | Ptr is invalid. |
|-------------|----------------|-----------------|-----------------|
| EPC_SUCCESS | The<br>success | function fully. | completed       |

- See Also EpcSwapBuffer, EpcSwap16, EpcSwap32, EpcSwap64, EpcSwap80.
- **Example** See EpcSwapBuffer.

**Description** Byte-swaps a 64-bit value.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcSwap64(void FAR\*Value\_Ptr);

Value\_Ptr Value\_Ptr specifies a location containing a 64-bit value to byte-swap.

Visual Basic Synopsis

Declare Function EpcSwap64% Lib "bmvxiw16.dll" (Value\_Ptr As Any)

| Remarks      | EpcSwap64 byte-s by Value_Ptr.                      | waps the 64-bit value in the location pointed to |
|--------------|---|--|
| Return Value | The function returns a Bus Management return value: |  |
|              | EPC_INV_PTR   | The parameter Value_Ptr is invalid.              |
|              | EPC_SUCCESS   | The function completed successfully.             |
| See Also     | EpcSwapBuffer, E<br>EpcSwap80.                      | CpcSwap16, EpcSwap32, EpcSwap48,                 |

**Example** See EpcSwapBuffer.

| Description | Byte-swaps an 80-bit value. |
|-------------|-----------------------------|
|-------------|-----------------------------|

C Synopsis

#include "busmgr.h"

short
EpcSwap80(void FAR\*Value\_Ptr);

Value\_Ptr Value\_Ptr specifies a location containing a 80-bit value to byte-swap.

Visual Basic Synopsis

Declare Function EpcSwap80% Lib "bmvxiw16.dll" (Value\_Ptr As Any)

| Remarks      | EpcSwap80 byte-swaps the 80-bit value in the location pointed to by Value_Ptr. |                                      |
|--------------|--|--------------------------------------|
| Return Value | The function returns a Bus Management return value:                            |                                      |
|              | EPC_INV_PTR  | The parameter Value_Ptr is invalid.  |
|              | EPC_SUCCESS  | The function completed successfully. |
| See Also     | EpcSwapBuffer, EpcSwap16, EpcSwap32, EpcSwap48,<br>EpcSwap64.                  |                                      |
| Example      | See EpcSwapBuffer.   |                                      |

2

## **EpcSwapBuffer**

**Description** Byte-swaps a buffer of data.

C Synopsis

#include "busmgr.h"

| Buffer_Ptr  | Buffer_Ptr specifies the location of a buffer of data elements to byte-swap.            |
|-------------|---|
| Buffer_Size | Buffer_Size specifies the size of the specified data buffer, in bytes.                  |
| Width       | Width specifies the width of the individual data elements in the specified data buffer. |

Visual Basic Synopsis

Declare Function EpcSwapBuffer% Lib "bmvxiw16.dll" (Buffer\_Ptr As Any, ByVal Buffer\_Size&, ByVal Width%)

**Remarks** EpcSwapBuffer byte-swaps the array of data elements at the location pointed to by *Buffer\_Ptr*.

*Width* specifies the width of the individual data elements in the specified data buffer, in bytes. Valid values are:

| Width      | Description  |
|------------|--|
| EPC_8_BIT  | The specified buffer contains <i>Buffer_Size</i> 8-bit data elements.      |
| EPC_16_BIT | The specified buffer contains <i>Buffer_Size</i> /2 16-bit data elements.  |
| EPC_32_BIT | The specified buffer contains <i>Buffer_Size</i> /4 32-bit data elements.  |
| EPC_48_BIT | The specified buffer contains <i>Buffer_Size</i> /6 48-bit data elements.  |
| EPC_64_BIT | The specified buffer contains <i>Buffer_Size</i> /8 64-bit data elements.  |
| EPC_80_BIT | The specified buffer contains <i>Buffer_Size</i> /10 80-bit data elements. |

The function assumes that all of the data elements in the specified buffer are the same size.

The function does not limit *Buffer\_Size* to less than 64 Kbytes, nor does it make any attempt to detect the end of the buffer segment. The function wraps around to the beginning of the buffer segment if *Buffer\_Size* is too large.

#### Return Value The function returns a Bus Management return value:

| The parameter <i>Buffer_Ptr</i> is invalid.                          |
|--|
| The parameter <i>Buffer_Size</i> is not a multiple of <i>Width</i> . |
| The parameter Width is invalid.                                      |
| The function completed successfully.                                 |
|  |

See Also EpcSwap16, EpcSwap32, EpcSwap48, EpcSwap64, EpcSwap80.

#### Example

```
• Copyright 1994 by RadiSys Corporation. All rights reserved.
 * byteord.c -- Bus Management Library byte order functions sample code.
#include "busmgr.h"
 * FUNCTION PROTOTYPES...
 */
short FAR
ByteOrdSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
/ **
 * GLOBAL DATA...
 **/
unsigned char Value16[] = ( 0x00, 0x11 );
unsigned char Value32[] = { 0x00, 0x11, 0x22, 0x33 };
unsigned char Value48[] = { 0x00, 0x11, 0x22, 0x33, 0x44, 0x55 };
unsigned char Value64[] = { 0x00, 0x11, 0x22, 0x33, 0x44, 0x55, 0x66, 0x77 };
unsigned char Value80[] = { 0x00, 0x11, 0x22, 0x33, 0x44,
                               0x55, 0x66, 0x77, 0x88, 0x99 };
/*
 * CODE...
 */
short FAR
ByteOrdSample(void)
{
      *• Byte-swap a 16-bit value, then byte-swap it back to its original order.
      * NOTES:
      **
         1. For the sake of example, the code uses both EpcSwap16() and
      *
                EpcSwapBuffer() to swap the data.
      +/
    EpcSwap16((unsigned short FAR *) Value16);
    EpcSwapBuffer((void FAR *) Value16, sizeof(Value16), EPC_16_BIT);
     / *•
      *• Byte-swap a 32-bit value, then byte-swap it back to its original order.
      *• NOTES:
      ٠
           1. For the sake of example, the code uses both EpcSwap32() and
      *
                EpcSwapBuffer() to swap the data.
      +/
    EpcSwap32((unsigned long FAR *) Value32);
    EpcSwapBuffer((void FAR *) Value32, sizeof(Value32), EPC_32_BIT);
     1 *
      *• Byte-swap a 48-bit value, then byte-swap it back to its original order.
```

# 2

• NOTES: \* For the sake of example, the code uses both EpcSwap48() and 1. • EpcSwapBuffer() to swap the data. • / EpcSwap48((void FAR \*) Value48); EpcSwapBuffer((void FAR \*) Value48, sizeof(Value48), EPC\_48\_BIT); /\* • Byte-swap a 64-bit value, then byte-swap it back to its original order. • NOTES: ٠ For the sake of example, the code uses both EpcSwap64() and 1. \* EpcSwapBuffer() to swap the data. • / EpcSwap64((void FAR \*) Value64); EpcSwapBuffer((void FAR \*) Value64, sizeof(Value64), EPC\_64\_BIT); /\* \* Byte-swap a 80-bit value, then byte-swap it back to its original order. \* NOTES: ٠ For the sake of example, the code uses both EpcSwap80() and 1. EpcSwapBuffer() to swap the data. \* / EpcSwap80((void FAR \*) Value80); EpcSwapBuffer((void FAR \*) Value80, sizeof(Value80), EPC\_80\_BIT); WinPrintf("SUCCESS: ByteOrdSample() complete.\n"); return (EPC\_SUCCESS);

}

## EpcUnlockSession

**Description** Unlocks shared interface hardware for a session.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcUnlockSession(unsigned long Session\_ID);

Session\_ID Session\_ID specifies a session.

Visual Basic Synopsis

Declare Function EpcUnlockSession% Lib "bmvxiw16.dll" (ByVal Session\_ID&)

- **Remarks** EpcUnlockSession unlocks shared interface hardware for the specified Session\_ID.
- Return Value The function returns a Bus Management return value:

|          | EPC_INV_SESSION        | The specified Session_ID is invalid.                              |
|----------|------------------------|---|
|          | EPC_INV_SW             | The BusManager device driver is not present.                      |
|          | EPC_NOT_LOCKED         | Shared interface hardware is not locked by the specified session. |
|          | EPC_SUCCESS            | The function completed successfully.                              |
| See Also | EpcLockSession, EpcOpe | enSession.  |

**Example** See EpcGetLockingTimeout.

## **EpcUnmapBusMemory**

**Description** Destroys a bus memory mapping.

C Synopsis

#include "busmgr.h"

| short FAR PASCAL   |                   |                 |
|--------------------|-------------------|-----------------|
| EpcUnmapBusMemory( | unsigned long     | Session_ID,     |
|                    | volatile void HUG | E *Mapped_Ptr); |

| Session_ID | Session_ID specifies a bus session.                  |
|------------|--|
| Mapped_Ptr | Mapped_Ptr specifies a pointer to mapped bus memory. |

Visual Basic Synopsis

See Also

-

Declare Function EpcUnmapBusMemory% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Mapped\_Ptr As Any)

**Remarks EpcUnmapBusMemory** destroys a bus memory mapping.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_MAP                      | The specified Mapped_Ptr is invalid.                                    |
|----------------------------------|---|
| EPC_INV_SESSION                  | The specified Session_ID is invalid.                                    |
| EPC_INV_USAGE                    | The mapping specified by <i>Mapped_Ptr</i> is in use by another thread. |
| EPC_OS_ERROR                     | An operating system error occurred.                                     |
| EPC_SUCCESS                      | The function completed successfully.                                    |
| EpcMapBusMemory, EpcOpenSession. |   |

**Example** See EpcCopyData.

## **EpcUnmapSharedMemory**

**Description** Destroys a shared memory mapping.

C Synopsis

#include "busmgr.h"

short FAR PASCAL EpcUnmapSharedMemory( unsigned long Session\_ID, volatile void HUGE \*Mapped\_Ptr);

| Session_ID | Session_ID specifies a bus session.                            |
|------------|--|
| Mapped_Ptr | <i>Mapped_Ptr</i> specifies a pointer to mapped shared memory. |

Visual Basic Synopsis

Declare Function EpcUnmapSharedMemory% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Mapped\_Ptr As Any)

**Remarks** EpcUnmapSharedMemory destroys a shared memory mapping.

**Return Value** The function returns a Bus Management return value:

|          | EPC_INV_MAP         | The specified <i>Mapped_Ptr</i> is invalid.                             |
|----------|---------------------|---|
|          | EPC_INV_SESSION     | The specified Session_ID is invalid.                                    |
|          | EPC_INV_SW          | The BusManager device driver is not present.                            |
|          | EPC_INV_USAGE       | The mapping specified by <i>Mapped_Ptr</i> is in use by another thread. |
|          | EPC_OS_ERROR        | An operating system error occurred.                                     |
|          | EPC_SUCCESS         | The function completed successfully.                                    |
| See Also | EpcMapSharedMemory, | EpcOpenSession.   |

Example See EpcCopyData.

## **EpcValidateBusMapping**

Validates a bus memory mapping that uses dynamically configured bus window hardware.

#### C Synopsis

#include "busmgr.h"

| short FAR PASCAL<br>EpcValidateBusMapping( | unsigned long Session_ID,<br>volatile void HUGE *Mapped_Ptr); |
|--|---|
| Session_ID                                 | Session_ID specifies a bus session.                           |
| Mapped_Ptr                                 | Mapped_Ptr specifies a pointer to mapped bus memory.          |

**Remarks EpcValidateBusMapping** configures an interface's dynamically configured bus window hardware with the attributes of the specified bus memory mapping.

The function supports direct bus access for bus memory mappings created using **EpcMapBusMemoryExt**. The function is a no-op for bus memory mappings created using **EpcMapBusMemory**.

Direct bus access using dynamically configured bus window hardware requires using EpcValidateBusMapping before a group direct bus accesses to ensure that the bus window references the desired bus memory.

On an operating system that supports multiple processes or threads (any non-DOS operating system), direct bus access using dynamically configured bus windows also requires a mechanism for protecting the bus window hardware from reconfiguration during the bus accesses. In a non-preemptive environment (like Windows), simply insuring that all direct bus accesses complete before giving up the processor is sufficient. In an environment with a preemptive scheduling algorithm (like LynxOS or OS/2), direct bus access using dynamically configured bus window hardware also requires using **EpcLockSession** and **EpcValidateBusMapping** before a group of direct bus accesses and **EpcUnlockSession** after the direct bus access is complete. Using **EpcLockSession** and **EpcUnlockSession** insures that another thread does not reconfigure the dynamically configured bus window hardware during the access.

#### **Return Value** The function returns a EPConnect return value:

| EPC_INV_MAP     | The specified Mapped_Ptr is invalid.                        |
|-----------------|---|
| EPC_INV_SESSION | The specified Session_ID is invalid.                        |
| EPC_INV_SW      | The BusManager device driver is not present.                |
| EPC_LOCKED      | Shared interface hardware is locked by another bus session. |
| EPC_OS_ERROR    | An operating system error occurred.                         |
| EPC_SUCCESS     | The function completed successfully.                        |
|                 |   |

See Also EpcMapBusMemory, EpcMapBusMemoryExt, EpcLockSession, EpcOpenSession, EpcUnlockSession.

## **EpcVerifyEnvironment**

**Description** Verifies and queries the EPConnect environment.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcVerifyEnvironment(struct EpcEnvironment FAR
\*Environment\_Ptr);

*Environment\_Ptr Environment\_Ptr* specifies a location where data describing the EPConnect environment will be placed.

Visual Basic Synopsis

Declare Function EpcVerifyEnvironment% Lib "bmvxiw16.dll" (Environment\_Ptr As Any)

**Remarks EpcVerifyEnvironment** verifies the EPConnect environment and places data describing the environment in the structure pointed to by *Environment\_Ptr.* 

The returned EPConnect environment structure contains a complete description of the underlying hardware and software. The structure also contains a complete description of the Bus Management Library features supported by the underlying software and hardware:

```
struct EpcEnvironment
   /* Hardware, firmware, and software revision attributes.
                                                                         • /
                                      /* Hardware revision number:
   unsigned char HWRevision;
                                                                        • /
                                      1.
                                            EPC_7
                                                                         * /
                                     /•
                                            EPC 8
                                                                         */
                                     1*
                                            VXLink_ISA
                                                                         • /
   unsigned char BIOSMajorRevision; /* BIOS major revision number.
                                                                         */
   unsigned char BIOSMinorRevision; /* BIOS minor revision number.
                                                                        +/
   unsigned char SWMajorRevision; /* Software major revision number.*/
   unsigned char SWMinorRevision;
                                     /* Software minor revision number.*/
```

|  | /*              | Memory mapping attributes.  | */       |
|--|-----------------|---|----------|
| unsigned char IsHWByte                           | Swap; /*        | Is the interface capable of hardware byte                             | */       |
|  | /*              |   | */       |
|  | /*<br>/*        | TRUE<br>FALSE   | */       |
| unsigned short Addre                             |                 | Valid address modifiers (OR'd   | */       |
| U U  | • •             | combination of):  | */       |
|  | /*              | EPC_A16S  | */       |
|  | /*<br>/*        | EPC_A24SD<br>EPC_A32SD  | •/       |
| unsigned short Byte                              |                 | Valid byte ordering values (OR'd                                      | */       |
|  |                 | combination of):  | *'/      |
|  | /*              | EPC_IBO   | •/       |
| ward and a branch mark of                        | /*              | EPC_MBO   | */       |
| unsigned short Data                              | Width; /*<br>/* | Valid data widths (OR'd combination of):<br>EPC_8_BIT                 | */<br>*/ |
|  | /*              | EPC_8_BIT_ODD   | */       |
|  | /*              | EPC_16_BIT  | •/       |
|  | /*              | EPC_32_BIT  | */       |
| undered shout Min111                             | /*              | EPC_FASTCOPY  | */       |
| unsigned short MinA164<br>unsigned short MaxA164 |                 | Minimum accessible A16 address.<br>Maximum accessible A16 address.    | •/<br>•/ |
| unsigned long MinA24Ad                           |                 | Minimum accessible A24 address.                                       | +1       |
| unsigned long MaxA24Ad                           | ldress; /*      | Maximum accessible A24 address.                                       | */       |
| unsigned long MinA32Ad                           |                 | Minimum accessible A32 address.                                       | */       |
| unsigned long MaxA32Ad                           | dress; /*       | Maximum accessible A32 address.                                       | */       |
| /* Event attributes. *                           | •/              |   |          |
| unsigned long EventMas                           | uk: /*          | Valid events (OR'd combination of):                                   | • /      |
| the forget of the second                         | /*              | EPC_MSG_INT */  | '        |
|  | /*              | EPC_VME1_INT */   |          |
|  | /*              | */  |          |
|  | /*<br>/*        | EPC_VME7_INT */<br>EPC_SIGNAL_INT */                                  |          |
|  | , /*            | EPC_TTL_TRIGO_INT */  |          |
|  | /*              | •/  |          |
|  | /*              | EPC_TTL_TRIG7_INT */  |          |
|  | /*              | EPC_ACFAIL_ERR */<br>EPC BERR ERR */                                  |          |
|  | /*              | EPC_BERR_ERR */<br>EPC SYSFAIL ERR */                                 |          |
|  |                 | EPC_WATCHDOG_ERR */   |          |
|  | /*              | EPC_SYSRESET_ERR */   |          |
| /* EPC bus configurat                            | ion attribut    | es. */  |          |
| unsigned short BusE                              | nable;          | /* Valid bus enable attributes (OR'd                                  | */       |
| -  | -               | <pre>/* combination of):</pre>  | */       |
|  |                 | /* EPC_DISABLE_BUS  | */       |
| ungigned short Bugh                              | whiteda.        | <pre>/* EPC_ENABLE_BUS /* Valid bus arbitration mode attributes</pre> | */       |
| unsigned short Bush                              | rbMođe;         | /* (OR'd combination of):   | */       |
|  |                 | /* EPC_PRIORITY   | */       |
|  |                 | /* EPC_ROUND_ROBIN  | •/       |
| unsigned short BusA                              | rbPriority;     | /* Valid bus arb priority attributes                                  | */       |
|  | /* (01          | <pre>C'd combination of):     EPC_PRIORITY0</pre>                     | •/       |
|  | , <b>*</b>      | EPC_PRIORITY1   | */       |
|  | /*              | EPC_PRIORITY2   | */       |
|  | /*              | EPC_PRIORITY3   | */<br>*/ |
| unsigned short BusR                              |                 | lid bus release attributes (OR'd<br>mbination of):                    | */       |
|  | , со            |   |          |

|                                    | /* EPC_ROR<br>/* EPC_RONR   | */<br>*/ |
|------------------------------------|---|----------|
| /* Miscellaneous configurat        | ion attributes. */  |          |
| unsigned char IsBERRAsserti        | <pre>pm; /* Is software capable of asserting the /* sticky BERR bit? */</pre> | */       |
|                                    | /* TRUE */<br>/* FALSE */   |          |
| unsigned long GetMiscMask;         | /* Miscellaneous attributes that can be                                       | •/       |
|                                    | /* queried (OR'd combination of):   | •/       |
|                                    | /* EPC_DIR •/<br>/* EPC_DOR */  |          |
|                                    | /* EPC_ERR */   |          |
|                                    | /* EPC_LOCK */<br>/* EPC_PASS */  |          |
|                                    | /* EPC_PASS */<br>/* EPC_PIPELINE_BUSY */                                     |          |
|                                    | /* EPC_READY */   |          |
|                                    | /* EPC_RRDY */<br>/* EPC_RSRC_MGR */  |          |
|                                    | /* EPC_STICKY_BERR */   |          |
|                                    | /* EPC_SYSFAIL_OUT */<br>/* EPC_SYSRESET IN */                                |          |
|                                    | /* EPC_SYSRESET_IN */<br>/* EPC_TTL_LATCH0 */                                 |          |
|                                    | /• */   |          |
|                                    | /* EPC_TTL_LATCH7 */<br>/* EPC_WATCHDOG */                                    |          |
|                                    | /* EPC_WRDY */  |          |
| unsigned long <b>SetMiscMask</b> ; | /* Miscellaneous attributes that can be                                       | •/<br>•/ |
|                                    | <pre>/* defined (OR'd combination of): /* EPC_DIR */</pre>                    | - /      |
|                                    | /* EPC_DOR */   |          |
|                                    | /* EPC_ERR */<br>/* EPC_LOCK */   |          |
|                                    | /* EPC_PASS */  |          |
|                                    | /* EPC_READY */<br>/* EPC RESET •/  |          |
|                                    | /* EPC_RESET •/<br>/* EPC_RRDY */   |          |
|                                    | /* EPC_RSRC_MGR •/  |          |
|                                    | /* EPC_STICKY_BERR •/<br>/• EPC_SYSFAIL_OUT •/                                |          |
|                                    | /* EPC_SYSRESET_IN */   |          |
|                                    | /• EPC_WRDY */  |          |
| /* Slave memory configurati        | on attributes. */   |          |
| unsigned char Igealflogage         | /* Is the I/F capable of slave memory */                                      |          |
| unsigned chai isseiinceess,        | /* self-accesses (via the VMEbus)? */   |          |
|                                    | /* TRUE */<br>/* FALSE */   |          |
| unsigned short <b>SlaveSpace</b> ; | /* FALSE */<br>/* Valid slave memory address spaces */                        |          |
|                                    | <pre>/* (OR'd combination of): •/</pre>                                       |          |
|                                    | /* EPC_DISABLED */<br>/* EPC_A24 */   |          |
|                                    | /* EPC_A32 */   |          |
| unsigned short <b>SlaveWidth</b> ; | <pre>/* Valid slave memory access widths (OR'd*/</pre>                        |          |
|                                    | /* combination of): */<br>/* EPC_8_BIT */                                     |          |
|                                    | /* EPC_16_BIT */  |          |
| unsigned long MinA24Slave;         | /* EPC_32_BIT */<br>/* Minimum A24 slave memory base •/                       |          |
| unsigned tong Miller 4016V6,       | /* address. */  |          |

```
unsigned long MaxA24Slave;
                               /* Maximum A24 slave memory base
                                                                +/
                               /* address.
                                                                */
unsigned long A24SlaveBaseInc;
                               /* A24 slave memory base increment*/
unsigned long MinA32Slave;
                               /* Minimum A32 slave memory base
                                                                */
                               /* address.
                                                                 •1
unsigned long MaxA32Slave;
                               /* Maximum A32 slave memory base
                                                                */
                               /* address.
                                                                */
unsigned long A32S1aveBaseInc;
                               /* A32 slave memory base increment */
/* unique logical address configuration attributes.
                                                                +/
unsigned char MinULA; /* Minimum valid ULA.
                                                                */
unsigned char MaxULA;
                               /* Maximum valid ULA.
                                                                */
/* Bus line attributes. */
unsigned long GetBusLineMask; /* Bus control lines that can be queried */
                             /* (OR'd combination of):
                                                                    */
                             /*
                                EPC_ACFAIL
                                                                    */
                             /•
                                 EPC_SYSFAIL
EPC_SYSRESET
                                                                    +1
                            /•
                                                                    +1
unsigned long GetBusMODIDMask; /* Bus MODID lines that can be queried
                                                                    +/
                             /* (OR'd combination of):
                                                                    */
                             1.
                                EPC_SLOT_MODID
                                                                    */
                             /*
                                  EPC_MODID0
                                                                    */
                             /*
                                                                    */
                                 ...
EPC_MODID12
                                                                    •/
                             /*
unsigned long GetBusTriggerMask; /* Bus trigger lines that can be queried */
                             /* (OR'd combination of):
                                                                    */
                                                                    */
                             1*
                                 EPC_ECL_TRIG0
                             1*
                                                                    */
                                  EPC_ECL_TRIG1
                             1*
                                  EPC_TTL_TRIG0
                                                                    */
                             /*
                                                                    */
                                   . . .
                             1*
                                  EPC_TTL_TRIG7
                                                                    • /
/* EPC line attributes. */
                            /* I/f control lines that can be gueried */
unsigned long GetEpcLineMask;
                                                                    */
                             /* (OR'd combination of):
                             /*
                                EPC_SYSFAIL
                                                                    */
                            /•
                                  EPC_SYSRESET
                                                                    */
unsigned long SetEpcLineMask; /* I/f control lines that can be defined */
                             /* (OR'd combination of):
                                                                    */
                                 EPC_SYSFAIL
                             /*
                                                                    -
                             1.
                                  EPC_SYSRESET
                                                                    */
unsigned long SetEpcMODIDMask; /* I/f MODID lines that can be defined
                                                                    * /
                             /* (OR'd combination of):
                                                                    */
                             /*
                                                                    **/
                                 EPC_MODID0
                             1*
                                  ...
EPC_MODID12
                                                                    * /
                                                                    */
                             1*
                               /* I/f trigger lines that can be queried */
unsigned long GetEpcTriggerMask;
                             /* (OR'd combination of):
                                                                    */
                             /*
                                                                    */
                                 EPC_ECL_TRIG0
                             /**
                                                                    */
                                  EPC_ECL_TRIG1
                                 EPC_TTL_TRIGO
                             /*
                                                                    */
                             1*
                                                                    */
                                   ...
                                  EPC_TTL_TRIG7
                             /*
                                                                    +/
unsigned long SetEpcTriggerMask; /* I/f trigger lines that can be queried */
                             /* (OR'd combination of):
                                                                    */
                             /*
                                                                    */
                                 EPC_ECL_TRIG0
                                  EPC_ECL_TRIG1
                             /*
                                                                    */
                                  EPC_TTL_TRIG0
                             /*
                                                                    * /
                                 ...
EPC_TTL_TRIG7
                             1*
                                                                    */
                             1 **
                                                                    */
                                                                    */
                             /* I/f trigger lines used as an
unsigned long InTriggerMask;
```

/\* input trigger in a trigger mapping \*/ \*/ /\* operation (OR'd combination of): /\* EPC\_TTL\_TRIG0 \*/ 1\* \*/ . . . 1\* EPC\_TTL\_TRIG7 \*/ unsigned long OutTriggerMask[EPC\_TRIGGER\_CNT]; /\* I/f trigger lines that can be used as \*/ /\* output triggers in a trigger mapping \*/ /\* operation (one array element for each \*/ /\* potential input trigger; each entry is an \*/ /\* OR'd combination of): \* / 1. EPC\_TTL\_TRIG0 \*/ /\* \*/ . . . /\* EPC\_TTL\_TRIG7 + / /\* Watchdog timer attributes. \*/ WatchdogCfg; /\* Valid watchdog timer configuration +/ unsigned short /\* constants (OR'd combination of): \*/ /• EPC\_WDT\_RESET +/ \*/ /\* EPC\_WDT\_FAST\_ERROR /\* +/ EPC\_WDT\_FAST\_RESET 1\* EPC WDT SLOW ERROR \*/ 1. EPC\_WDT\_SLOW\_RESET /\* Servant attributes. \*/ unsigned char IsProtocolError; /\* Is the EPC capable of signaling a \*/ /\* protocol error to its commander? \* / /• TRUE \*/ 1. \*/ FALSE unsigned short WSSize; /\* Valid word serial command/response \*/ \*/ /\* sizes (OR'd combination of): /• \*/ EPC\_16\_BIT 1\* EPC\_32\_BIT \*/ 1. EPC\_48\_BIT \*/ EnableNextCommand; /\* Valid word serial command enable unsigned short \*/ \*/ /\* constants (OR'd combination of): 1. EPC\_DISABLE\_ALL \*/ 1. EPC\_ENABLE\_WRDY \*/ 1. +/ EPC\_ENABLE\_DIR /\* \*/ EPC ENABLE DOR /\* EPC ENABLE ALL \*/ \*/ /\* Valid methods for sending protocol unsigned long MethodMask; \*/ /\* events (OR'd combination of): \*/ /\* EPC\_VME1\_INT /\* \*/ /\* \*/ EPC\_VME7\_INT /\* EPC\_SIGNAL\_REG \*/ unsigned short IRQ; /\* PC-AT IRO used. • / unsigned short /\* I/O base address. \*/ IOBase; unsigned long WindowBase; /\* Bus window base address. \*/ unsigned long WindowSize; /\* Bus window size, in bytes. \*/

```
/* Dynamic memory mapping attributes. */
   unsigned char IsHWByteSwapExt; /* Is the interface capable of hardware */
                                /* byte swapping? */
                                /* TRUE
                                                                 */
                                1.
                                       FALSE
                                                                 *
   unsigned short
                     AddressModExt;
                                      /* Valid address modifiers (OR'd
   •/
                                /* combination of):
                                                                 */
                                /*
                                                                 */
                                      EPC_A16N
                                1+
                                                                 */
                                       EPC_A16S
                                1*
                                       EPC_A24ND
                                                                 •1
                                /*
                                      EPC_A24NP
EPC_A24SD
                                                                 +/
                                /*
                                                                 */
                                     EPC_A24SP
                                1*
                                                                 */
                                /*
                                     EPC_A32ND
EPC_A32NP
EPC_A32SD
EPC_A32SP
                                                                 */
                                 /*
                                                                 * /
                                /*
                                                                 **/
                                /*
                                                                 +1
                     ByteOrderExt, /* Valid byte ordering values (OR'd
   unsigned short
                                                                                  **/
                                /* combination of):
                                                                 */
                                /* EPC_IBO
                                                                 +/
                                /*
                                       EPC_MBO
                                                                 +1
   unsigned short DataWidthExt; /* Valid data widths (OR'd combination of):
   * /
                                /*
                                       EPC_8_BIT
                                                                 */
                                                                 **/
                                       EPC_8_BIT_ODD
EPC_16_BIT
                                 /*
                                1*
                                                                 */
                                /*
                                       EPC_32_BIT
                                                                 */
                                                                 •/
                                / **
                                     EPC_FASTCOPY
   unsigned short MinAl6AddressExt;/* Minimum accessible Al6 address.*/
unsigned short MaxAl6AddressExt;/* Maximum accessible Al6 address.*/
   unsigned long MinA24AddressExt; /* Minimum accessible A24 address. */
   unsigned long MaxA24AddressExt; /* Maximum accessible A24 address.
                                                                            */
   unsigned long MinA32AddressExt; /* Minimum accessible A32 address.
                                                                            +/
   unsigned long MaxA32AddressExt; /* Maximum accessible A32 address.
                                                                            */
                                      /* Reserved area (for future expansion). */
   unsigned long Reserved[6];
};
```

```
2
```

| Return Value  | The function returns a Bus Management return value:   |   |
|---|---|---|
|   | EPC_INV_HW  | EPConnect does not support this revision of interface hardware. |
| EPC_INV_SW The Bush<br>or there i<br>Bus Man<br>BusMana | The parameter Environment_Ptr is invalid.   |   |
|   | The BusManager device driver is not present<br>or there is a revision mismatch between the<br>Bus Management Library and the<br>BusManager VxD. |   |
|   | EPC_SUCCESS   | The function completed successfully.                            |
| Example   | See EpcGetErrorString.  |   |

### **EpcWaitForEvent**

**Description** Wait for an event to occur.

C Synopsis

#include "busmgr.h"

| short FAR PASCAI | .a            |             |
|------------------|---------------|-------------|
| EpcWaitForEvent( | unsigned long | Session_ID, |
|                  | unsigned long | Timeout,    |
|                  | unsigned long | Wait_Mask,  |

|                | unsigned long FAR * Event_Data_Ptr);  |
|----------------|---|
| Session_ID     | Session_ID specifies a session.   |
| Timeout        | <i>Timeout</i> specifies the number of milliseconds to wait for an enabled event to occur.                        |
| Wait_Mask      | <i>Wait_Mask</i> specifies the events to await.   |
| Event_Mask_Ptr | <i>Event_Mask_Ptr</i> specifies a location where an event mask that specifies the occurring event will be placed. |
| Event_Data_Ptr | <i>Event_Data_Ptr</i> specifies a location where event data from the occurring event will be placed.              |

unsigned long FAR \* Event\_Mask\_Ptr,

Visual Basic Synopsis

Declare Function EpcWaitForEvent% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Timeout&, ByVal Wait\_Mask&, Event\_Mask\_Ptr&, Event\_Data\_Ptr&) **Remarks EpcWaitForEvent** waits at least *Timeout* milliseconds for one of the events specified by *Wait\_Mask* to occur, then places an event mask identifying the event and the event's data in the locations pointed to by *Event\_Mask\_Ptr* and *Event\_Data\_Ptr*, respectively.

The *Wait\_Mask* parameter is a bit mask where each bit corresponds to an event. The *Wait\_Mask* parameter should be an OR'd combination of the following constants:

| Event             | Description   |
|-------------------|---|
| EPC_MSG_INT       | Message (EPC-7 and EPC-8 only)                            |
| EPC_VME1_INT      | VMEbus interrupt 1  |
|                   |   |
| EPC_VME7_INT      | VMEbus interrupt 7  |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                              |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0<br>interrupt (EPC-7 only)            |
|                   |   |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7<br>interrupt (EPC-7 only)            |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                     |
| EPC_ACFAIL_ERR    | VMEbus power failure error                                |
| EPC_BERR_ERR      | VMEbus access error                                       |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                      |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration<br>error (EPC-7 and EPC-8 only) |
| EPC_EXT_TRIG0_INT | External trigger 0 interrupt<br>(VXLink only)             |
| EPC_EXT_TRIG1_INT | External trigger 1 interrupt<br>(VXLink only)             |

The value that **EpcWaitForEvent** places in the location pointed to by *Event\_Mask\_Ptr* can be one the following constants:

| *Event_Mask_Ptr   | Description   |
|-------------------|---|
| EPC_MSG_INT       | Message interrupt (EPC-7 and EPC-8 only)                  |
| EPC_VME1_INT      | VMEbus interrupt 1  |
| •••               |   |
| EPC_VME7_INT      | VMEbus interrupt 7  |
| EPC_SIGNAL_INT    | VXIbus signal FIFO interrupt                              |
| EPC_TTL_TRIG0_INT | VXIbus TTL Trigger 0<br>interrupt (EPC-7 only)            |
| •••               |   |
| EPC_TTL_TRIG7_INT | VXIbus TTL Trigger 7<br>interrupt (EPC-7 only)            |
| EPC_SYSRESET_ERR  | VMEbus SYSRESET error                                     |
| EPC_ACFAIL_ERR    | VMEbus power failure error                                |
| EPC_BERR_ERR      | VMEbus access error                                       |
| EPC_SYSFAIL_ERR   | VMEbus SYSFAIL error                                      |
| EPC_WATCHDOG_ERR  | Watchdog timer expiration<br>error (EPC-7 and EPC-8 only) |
| EPC_EXT_TRIG0_INT | External trigger 0 interrupt<br>(VXLink only)             |
| EPC_EXT_TRIG1_INT | External trigger 1 interrupt<br>(VXLink only)             |

Whether the value that **EpcWaitForEvent** places in the location pointed to by *Event\_Data\_Ptr* is meaningful depends on the event:

| *Event_Mask_Ptr                | *Event_Data_Ptr  |
|--------------------------------|--|
| EPC_MSG_INT                    | 0  |
| EPC_VME1_INT                   | VMEbus interrupt   |
|                                | status/id  |
| EPC_VME7_INT<br>EPC_SIGNAL_INT | (zero-extended to 32 bits)<br>VXIbus signal data<br>(zero-extended to 32 bits) |
| EPC_TTL_TRIG0_INT              | 0  |
|                                | 0  |
| EPC_TTL_TRIG7_INT              | 0  |
| EPC_SYSRESET_ERR               | 0  |
| EPC_ACFAIL_ERR                 | 0  |
| EPC_BERR_ERR                   | 0  |
| EPC_SYSFAIL_ERR                | 0  |
| EPC_WATCHDOG_ERR               | 0  |
| EPC_EXT_TRIG0_INT              | 0  |
| EPC_EXT_TRIG1_INT              | 0  |

When the specified timeout expires before an event occurs, the locations pointed to by *Event\_Mask\_Ptr* and *Event\_Data\_Ptr* contain undefined values.

If a session has an event handler and event handler stack pointer defined for an enabled event, an occurrence of the event satisfies the wait condition and invokes the event handler. The order of execution of the two threads is undefined.

Waiting for an event does not enable reception of the corresponding event. A separate, preceding call to EpcSetEventEnableMask is required.

|   | Return Value | The function returns a Bus Management return value: |   |
|---|--------------|---|---|
|   |              | EPC_INV_MASK  | <i>Wait_Mask</i> contains an event that is not valid for this interface.          |
|   |              | EPC_INV_PTR   | One or more of the parameters<br>Event_Mask_Ptr and Event_Data_Ptr<br>is invalid. |
|   |              | EPC_INV_SESSION                                     | The specified Session_ID is invalid.  |
|   |              | EPC_SUCCESS   | The function completed successfully.  |
|   |              | EPC_TIMEOUT   | The specified timeout period expired before an enabled event occurred.            |
|   | See Also     | EpcOpenSession, EpcSetE                             | EventEnableMask.  |
| <b>Example</b> See EpcGetEventEnableMask. |              | lask.   |   |

The formation D . . . 1

### EpcWatchdogTimer

**Description** Modifies the interface's watchdog timer configuration.

C Synopsis

#include "busmgr.h"

short FAR PASCAL
EpcWatchdogTimer(unsigned long Session\_ID, unsigned short
WatchdogCfg);

Session\_ID

WatchdogCfg

Session\_1D specifies a session.

*WatchdogCfg* specifies a watchdog timer configuration.

Visual Basic Synopsis

Declare Function EpcWatchdogTimer% Lib "bmvxiw16.dll" (ByVal Session\_ID&, ByVal Watchdog\_Cfg%)

**Remarks EpcWatchdogTimer** modifies the configuration of the interface's watchdog timer.

*WatchdogCfg* specifies the configuration of the interface's watchdog timer.

Valid values are:

| Constant           | Description  |
|--------------------|--|
| EPC_WDT_RESET      | Reset the watchdog timer<br>without modifying either the<br>watchdog timer period or the<br>operation that occurs upon<br>watchdog timer expiration<br>expiration (EPC-7 and EPC-8<br>only). |
| EPC_WDT_FAST_ERROR | Reset the watchdog timer, use<br>the short watchdog timer<br>period, and generate a<br>watchdog timer error event<br>upon watchdog timer<br>expiration expiration (EPC-7<br>and EPC-8 only). |
| EPC_WDT_FAST_RESET | Reset the watchdog timer, use<br>the short watchdog timer<br>period, and reset the EPC upon<br>watchdog timer expiration<br>expiration (EPC-7 and EPC-8<br>only).                            |
| EPC_WDT_SLOW_ERROF | Reset the watchdog timer, use<br>the long watchdog timer<br>period, and generate a<br>watchdog timer error event<br>upon watchdog timer<br>expiration expiration (EPC-7<br>and EPC-8 only).  |
| EPC_WDT_SLOW_RESET | Reset the watchdog timer, use<br>the long watchdog timer<br>period, and reset the EPC upon<br>watchdog timer expiration<br>expiration (EPC-7 and EPC-8<br>only).                             |

The actual length of an interface's watchdog timer period varies depending on the type of the interface:

| <u>Interface</u><br><u>Type</u> | <u>Short Watchdog</u><br><u>Timer Period</u> | <u>Long Watchdog</u><br><u>Timer Period</u> |
|---------------------------------|--|---|
| EPC-7                           | 210 milliseconds                             | 6.7 seconds                                 |
| EPC-8                           | 128 milliseconds                             | 8.2 seconds                                 |

If an interface's watchdog timer is not reset within the current watchdog timer period, a watchdog timer error event occurs. An application can enable the event (using EpcSetEventEnableMask) and install an event handler (using EpcSetEventHandler) to gracefully handle the error.

The interface's watchdog timer is typically reset in sections of code that execute frequently and/or execute at regular time intervals. Note that other bus operations may reset the watchdog timer as a side-effect of their execution.

By default, an interface's watchdog timer is configured to use the long watchdog timer period and generate a watchdog error event upon expiration.

EPC-5 and VXLink hardware do not include watchdog timer support. Attempting to use the function on an EPC-5 or VXLink interface results in an EPC\_INV\_CFG error.

**Return Value** The function returns a Bus Management return value:

|          | EPC_INV_CFG                                  | The parameter WatchdogCfg is invalid.   |
|----------|--|---|
|          | EPC_INV_SESSION                              | The specified Session_ID is invalid.  |
|          | EPC_INV_SW                                   | The BusManager device driver is not<br>present or there is a revision mismatch<br>between the Bus Management Library<br>and the BusManager VxD. |
|          | EPC_SUCCESS                                  | The function completed successfully.  |
| See Also | EpcOpenSession, EpcSe<br>EpcSetEventHandler. | tEventEnableMask,   |

#### Example

```
• Copyright 1994 by RadiSys Corporation. All rights reserved.
 • watchdog.c -- Bus Management Library EPC watchdog timer functions sample
                 code.
 */
#include "busmgr.h"
 • FUNCTION PROTOTYPES...
 */
short FAR
WatchdogSample(void);
int FAR
WinPrintf(char FAR *Format_Ptr, ...);
 • CODE...
 •/
short FAR
WatchdogSample(void)
£
    char
                          err_string[ERROR_STRING_SZ];
    short
                          err_num;
    unsigned long
                          event_data;
    unsigned long
                         event_mask;
    unsigned long
                         session_id;
    struct EpcEnvironment environment;
     • Verify the EPConnect environment.
     */
    if ((err_num = EpcVerifyEnvironment(&environment)) != EPC_SUCCESS)
    {
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcVerifyEnvironment() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
     • Open a session.
    if ((err_num = EpcOpenSession(&session_id)) != EPC_SUCCESS)
    {
        EpcGetErrorString(err_num, err_string);
        WinPrintf("FAILURE: EpcOpenSession() error == %s (%d).\n",
                  err_string,
                  err_num);
        return (err_num);
    }
    1.

    If watchdog timer functionality is supported on this EPC, configure the
```

```
• EPC to generate a watchdog timer error event using a short timeout, then
 • wait for up to one second (1000 ms) for the event to occur.
 */
if ((environment.WatchdogCfg & EPC_WDT_FAST_ERROR) != 0 &&
    (environment.WatchdogCfg & EPC_WDT_SLOW_ERROR) != 0 )
£
    EpcWatchdogTimer(session_id, EPC_WDT_FAST_ERROR);
    EpcWaitForEvent(session_id,
                    1000,
                    EPC_WATCHDOG_ERR,
                    &event_mask,
                    &event_data);
    if ((event_mask & EPC_WATCHDOG_ERR) == 0)
    {
        WinPrintf("Watchdog timer event DID NOT occurred.\n");
    }
    else
    {
        WinPrintf("Watchdog timer event occurred.\n");
    }
    EpcWatchdogTimer(session_id, EPC_WDT_SLOW_ERROR);
}
 • Close the session and return.
 • /
EpcCloseSession(session_id);
WinPrintf("SUCCESS: WatchdogSample() complete.\n");
return (EPC_SUCCESS);
```

}

## 3. On-Line Resource Manager

EPConnect provides an On-Line Resource Manager (OLRM) interface for querying configuration and state information about an instrument system and devices within that instrument system. Configuration information is typically static (i.e., established by a Start-Up Resource Manager (SURM) at system initialization time, and not changed thereafter). Device state information is typically dynamic (i.e., reflects the run-time state changes of a device as it is used by an executing application).

The following OLRM functions are available:

| Function        | Description   |
|-----------------|---|
| OlrmGetArbAttr  | Queries an arbitrary string attribute.  |
| OlrmGetBoolAttr | Queries a boolean attribute.  |
| OlrmGetNmByGPA  | Queries the device name corresponding to a GPIB address.                        |
| OlrmGetNmByNA   | Queries the device name corresponding to a network address.                     |
| OlrmGetNmByULA  | Queries the device name<br>corresponding to a VXIbus unique<br>logical address. |
| OlrmGetNumAttr  | Queries a numeric attribute.  |
| OlrmGetStrAttr  | Queries a string attribute.   |

To use OLRM to query a specific device's attributes, an application must first know the name of the device. An application can use OlrmGetNmByGPA, OlrmGetNmByNA, or OlrmGetNmByULA to query a device's name from its address. Once the application knows the device's name, it can use OlrmGetArbAttr, OlrmGetBoolAttr, OlrmGetNumAttr, and OlrmGetStrAttr to query individual device attributes.

### 3.1 Functions By Name

This section contains an alphabetical listing of the OLRM library functions. Each listing describes the function, gives its invocation sequence and arguments, discusses its operation, and lists its returned values. Where usage of the function may not be clear, an example with comments is given.

### OlrmGetArbAttr

**Description** Queries an arbitrary string attribute.

C Synopsis

#include ''busmgr.h'' #include ''olrm.h''

| .5 |
|----|
|    |

| Name_Ptr          | Name_Ptr specifies a device name.  |
|-------------------|--|
| Arb_Attribute_Ptr | Arb_Attribute_Ptr specifies an arbitrary string attribute.   |
| Arb_Result_Ptr    | Arb_Result_Ptr specifies the location of a buffer where the specified string attribute will be placed. |

Visual Basic Synopsis

Declare Function OlrmGetNmByGPA% Lib "olrmw16.lib"

(ByVal Primary%, ByVal Secondary% ByVal Name\_Ptr\$)

**Remarks** OlrmGetArbAttr queries an arbitrary string attribute of the specified device and places the result in the buffer pointed to by *Arb\_Result\_Ptr*. The function allows an application to obtain attribute information that is not accessible via the standard set of integer search keys, particularly attribute information about non-GPIB/non-VME devices.

*Name\_Ptr* is a null-terminated ASCII string specifying a device name.

Arb\_Attribute\_Ptr is a null-terminated ASCII string specifying the string attribute to query.

*Arb\_Result\_Ptr* specifies the location a buffer where the function places the result of the arbitrary string attribute query. The buffer must be at least **ATTRIBUTE\_SZ** bytes long.

The result of an arbitrary string attribute query is always a null-terminated ASCII character string. Depending on the specified string attribute, the result string may represent a decimal number, a hexadecimal number, a binary number, a bit mask, or a string of characters. It is the responsibility of the application to interpret the result string.

#### Return Value The function returns a Bus Management return value:

|          | EPC_INV_ATTR       | The parameter Arb_Attribute_Ptr is invalid.   |
|----------|--------------------|---|
|          | EPC_INV_NAME       | A device with the specified name does not exist.                                    |
|          | EPC_INV_PTR        | One or more of the parameters <i>Name_Ptr</i> and <i>Arb_Result_Ptr</i> is invalid. |
|          | EPC_OS_ERROR       | The resource manager database file could not be read.                               |
|          | EPC_SUCCESS        | The function completed successfully.  |
| See Also | OlrmGetBoolAttr, C | PirmGetNumAttr, OlrmGetStrAttr.   |

### OIrmGetBoolAttr

**Description** Queries a boolean attribute.

C Synopsis

#include "busmgr.h"
#include "olrm.h"

| <pre>short OlrmGetBoolAttr(char FAR * Name_Ptr,</pre> |   |  |
|---|---|--|
|   | <pre>short Bool_Attribute,<br/>unsigned short FAR * Bool_Result_Ptr);</pre>                       |  |
| Name_Ptr  | Name_Ptr specifies a device name.   |  |
| Bool_Attribute  | <i>Bool_Attribute</i> specifies a boolean attribute.  |  |
| Bool_Result_Ptr                                       | <i>Bool_Result_Ptr</i> specifies a location where the specified boolean attribute will be placed. |  |

Visual Basic Synopsis

Declare Function OlrmGetBoolAttr% Lib "olrmw16.dll" (ByVal Name\_Ptr\$\$, (ByVal Bool\_Attribute% Bool\_Result\_Ptr%)

**Remarks** OlrmGetBoolAttr queries a boolean attribute of the specified device and places the result in the location pointed to by *Bool\_Result\_Ptr*.

Name\_Ptr is a null-terminated ASCII string specifying a device name.

*Bool\_Attribute* specifies the boolean attribute to query. Valid values for VXIbus devices are:

| Bool_Attribute    | *Bool Result Ptr  |
|-------------------|---|
| VXI_ISMEMACT      | Memory active (accessible).   |
| VXI_ISMODID       | MODID line asserted.  |
| VXI_ISREADY       | Ready for normal operation.   |
| VXI_ISPASSED      | Passed self test.   |
| VXI_MEM_ISNONPRIV | Non-privileged access capability (memory devices only).               |
| VXI_MEM_ISBLKTR   | Block transfer capability (memory devices only).                      |
| VXI_MEM_ISNONVOL  | Non-volatile RAM memory (memory devices only).                        |
| VXI_MEM_ISELPROG  | EPROM memory (memory devices only).                                   |
| VXI_MEM_ISD32TR   | D32 transfer capability (memory devices only).                        |
| VXI_MPR_ISCMDR    | Commander capability<br>(message-based devices only).                 |
| VXI_MPR_ISSIG     | Signal register present<br>(message-based devices only).              |
| VXI_MPR_ISMSTR    | VXIbus master capability<br>(message-based devices only).             |
| VXI_MPR_ISINTR    | Interrupter capability<br>(message-based devices only).               |
| VXI_MPR_ISFHS     | FHS capability (message-based devices only).                          |
| VXI_MPR_ISSHMEM   | Shared memory protocol<br>capability (message-based<br>devices only). |
| VXI_MRP_ISRG      | Response generation capability (message-based devices only).          |

| VXI_MRP_ISEG     | Event generation capability (message-based devices only).                 |
|------------------|---|
| VXI_MRP_ISPI     | Programmable interrupter<br>capability (message-based<br>devices only).   |
| VXI_MRP_ISPH     | Programmable handler<br>capability (message-based<br>devices only).       |
| VXI_MRP_ISTRG    | Supports word serial <i>Trigger</i> command (message-based devices only). |
| VXI_MRP_ISI4     | Supports IEEE 488.2 instrument protocol (message-based devices only).     |
| VXI_MRP_ISINST   | Supports VXIbus instrument<br>protocol (message-based<br>devices only).   |
| VXI_MRP_ISELW    | Extended long word serial<br>capability (message-based<br>devices only).  |
| VXI_MRP_ISLW     | Long word serial capability<br>(message-based devices only).              |
| VXI_MRR_ISDOR    | DOR asserted (message-based devices only).                                |
| VXI_MRR_ISDIR    | DIR asserted (message-based devices only).                                |
| VXI_MRR_ISERR    | Word serial protocol error<br>detected (message-based devices<br>only).   |
| VXI_MRR_ISRRDY   | Read Ready asserted<br>(message-based devices only).                      |
| VXI_MRR_ISWRDY   | Write Ready asserted<br>(message-based devices only).                     |
| VXI_MRR_ISFHSACT | FHS protocol active<br>(message-based devices only).                      |

VXI\_MRR\_ISLOCKED

Local lockout active (message-based devices only).

No valid *Bool\_Attribute* values are defined for GPIB network devices.

If the requested boolean attribute is false, the function places a zero at the location pointed to by *Bool\_Result\_Ptr*. Otherwise, the function places a non-zero value at the location.

#### **Return Value** The function returns a Bus Management return value:

| EPC_INV_ATTR | The parameter <i>Bool_Attribute</i> is invalid.                                      |
|--------------|--|
| EPC_INV_NAME | A device with the specified name does not exist.                                     |
| EPC_INV_PTR  | One or more of the parameters <i>Name_Ptr</i> and <i>Bool_Result_Ptr</i> is invalid. |
| EPC_NO_DATA  | The resource management database does not contain the requested attribute.           |
| EPC_SUCCESS  | The function completed successfully.   |

#### See Also OlrmGetArbAttr, OlrmGetNumAttr, OlrmGetStrAttr.

### OlrmGetNmByGPA

**Description** Queries the device name corresponding to a GPIB address.

C Synopsis

#include "busmgr.h"
#include "olrm.h"

short
OlrmGetNmByGPA(short Primary, short Secondary, char FAR
\*Name\_Ptr);

| Primary   | Primary specifies a GPIB primary address.   |
|-----------|---|
| Secondary | <i>Secondary</i> specifies a GPIB secondary address.  |
| Name_Ptr  | <i>Name_Ptr</i> specifies the location of a buffer where the name of the device corresponding to the specified GPIB address will be placed. |

Visual Basic Synopsis

Declare Function OlrmGetNmByGPA% Lib "olrmw16.dll" (ByVal Primary%, ByVal Secondary%, ByVal Name\_Ptr\$)

**Remarks** OlrmGetNmByGPA queries the name of the device corresponding to the specified GPIB address and places the name in the buffer pointed to by *Name\_Ptr*.

*Primary* specifies a GPIB primary address. Valid values are 0 through 30, inclusive.

Secondary specifies a GPIB secondary address. Valid values are -1 and 0 through 30, inclusive. If Secondary is -1, the function searches for a GPIB device with the specified primary address and no secondary address. Otherwise, the function searches for a GPIB device with the specified primary address and the specified secondary address. *Name\_Ptr* specifies the location of a buffer where a device's name will be placed. The buffer must be at least **DEVNAME\_SZ** byte long.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_GPA   | One or more of the parameters <i>Primary</i> and <i>Secondary</i> is invalid. |
|---------------|---|
| EPC_INV_PTR   | The parameter Name_Ptr is invalid.  |
| EPC_NO_DEVICE | A device corresponding to the specified GPIB address does not exist.          |
| EPC_SUCCESS   | The function completed successfully.  |
|               |   |

See Also OlrmGetNmByNA, OlrmGetNmByULA.

### OIrmGetNmByNA

**Description** Queries the device name corresponding to a network address.

C Synopsis

#include "busmgr.h" #include "olrm.h"

short
OlrmGetNmByNA(char FAR \*Net\_Address\_Ptr, char FAR
\*Name\_Ptr);

| Net_Address_Ptr | <i>Net_Address_Ptr</i> specifies a network address string.   |
|-----------------|--|
| Name_Ptr        | <i>Name_Ptr</i> specifies the location of a buffer where the name of the device corresponding to the specified network address will be placed. |

Visual Basic Synopsis

Declare Function OlrmGetNmByNA% Lib "olrmw16.dll" (ByVal Net\_Address\_Ptr\$, ByVal Name\_Ptr\$)

**Remarks** OlrmGetNmByNA queries the name of the device corresponding to the specified network address and places the name in the buffer pointed to by *Name\_Ptr*.

*Net\_Address\_Ptr* specifies the location of a null-terminated ASCII string representing a network address.

*Name\_Ptr* specifies the location of a buffer where a device's name will be placed. The buffer must be at least **DEVNAME\_SZ** byte long.

Return Value The function returns a Bus Management return value:

|          | EPC_INV_PTR       | One or more of the parameters <i>Net_Address_Ptr</i> and <i>Name_Ptr</i> is invalid. |
|----------|-------------------|--|
|          | EPC_NO_DEVICE     | A device corresponding to the specified network address does not exist.              |
|          | EPC_SUCCESS       | The function completed successfully.   |
| See Also | OlrmGetNmByGPA, ( | DirmGetNmByULA.  |

### OIrmGetNmByULA

**Description** Queries the device name corresponding to a VXIbus unique logical address.

C Synopsis

#include ''busmgr.h'' #include ''olrm.h''

short
OlrmGetNmByULA(unsigned short ULA, char FAR \*Name\_Ptr);

ULA specifies a VXIbus unique logical address.

Name\_Ptr Name\_Ptr specifies the location of a buffer where the name of the device corresponding to the specified VXIbus unique logical address will be placed.

Visual Basic Synopsis

Declare Function OlrmGetNmByULA% Lib "olrmw16.dll" (ByVal ULA%, ByVal Name\_Ptr\$)

**Remarks** OlrmGetNmByULA queries the name of the device corresponding to the specified VXIbus unique logical address and places the name in the buffer pointed to by *Name\_Ptr*.

ULA specifies a VXIbus unique logical address. Valid values are 0 through 255, inclusive.

*Name\_Ptr* specifies the location of a buffer where a device's name will be placed. The buffer must be at least **DEVNAME\_SZ** byte long.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_PTR                    | The parameter Name_Ptr is invalid.  |
|--------------------------------|---|
| EPC_NO_DEVICE                  | A device corresponding to the specified<br>VMEbus unique logical address does not<br>exist. |
| EPC_SUCCESS                    | The function completed successfully.  |
| OlrmGetNmByGPA, OlrmGetNmByNA. |   |

Se

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See Also

### OlrmGetNumAttr

**Description** Queries a numeric attribute.

C Synopsis

#include "busmgr.h"
#include "olrm.h"

| short<br>OlrmGetNumAt | ttr(char FAR * Name_Ptr,                                     |
|-----------------------|--|
|                       | short Num_Attribute,<br>unsigned long FAR * Num_Result_Ptr); |
| Name Ptr              | Name Ptr specifies a device name                             |

| Name_Ptr       | <i>Name_Ptr</i> specifies a device name.   |
|----------------|--|
| Num_Attribute  | Num_Attribute specifies a numeric attribute.   |
| Num_Result_Ptr | <i>Num_Result_Ptr</i> specifies a location where the specified numeric attribute will be placed. |

Visual Basic Synopsis

Declare Function OlrmGetNumAttr% Lib "olrmw16.dll" (ByVal Name\_Ptr\$\$, ByVal Num\_Attribute%, ByVal Num\_Result\_Ptr&)

**Remarks** OlrmGetNumAttr queries a numeric attribute of the specified device and places the result in the location pointed to by *Num\_Result\_Ptr*.

Name\_Ptr is a null-terminated ASCII string specifying a device name.

*Num\_Attribute* specifies the numeric attribute to query. Valid values for VXIbus devices are:

| <u>Num_Attribute</u> | *Num Result Ptr  |
|----------------------|--|
| VXI_ULA              | Unique logical address.  |
| VXI_IDREG            | ID register.   |
| VXI_DTREG            | Device type register.  |
| VXI_STREG            | Status register.   |
| VXI_OFFREG           | Offset register.   |
| VXI_MNFID            | Manufacturer ID.   |
| VXI_MODCOD           | Model code.  |
| VXI_DEVCLASS         | Device class. 0=memory, 1=extended,<br>2=message-based, 3=register-based.                  |
| VXI_ADRSP            | Address space. 0=A16/A24,<br>1=A16/A32, 3=A16 only.  |
| VXI_A16BASE          | A16 memory base.   |
| VXI_A24BASE          | A24 memory base (A16/A24 devices only).  |
| VXI_A24SIZE          | A24 memory size, in bytes (A16/A24 devices only).  |
| VXI_A32BASE          | A32 memory base (A16/A32 devices only).  |
| VXI_A32SIZE          | A32 memory size, in bytes (A16/A32 devices only).  |
| VXI_STATDD           | Status register device dependent bits.<br>Defined and reserved bits are masked<br>to zero. |
| VXI_BUSNUM           | Bus mainframe number.  |
| VXI_SLOTNUM          | Slot number.   |
| VXI_CMDRLA           | Unique logical address of commander device.  |

| VXI_S0LA                         | Unique logical address of slot-0 device.  |
|----------------------------------|---|
| VXI_SVARSZ                       | Servant area size.  |
| VXI_HDLRMAP1<br><br>VXI_HDLRMAP7 | Interrupt line assigned to handlers 1<br>through 7. A zero result indicates the<br>handler is not assigned for the device.            |
| VXI_INTRMAP1<br><br>VXI_INTRMAP7 | Interrupt line assigned to interrupters 1<br>through 7. A zero result indicates the<br>interrupter is not assigned for the<br>device. |
| VXI_MEM_ATTREG                   | Attribute register (memory devices only).   |
| VXI_MEM_TYPE                     | Memory type (memory devices only).<br>1 = ROM, 2 = other, 3 = RAM.  |
| VXI_MEM_SPEED                    | Minimum memory access time, in nanoseconds (memory devices only).   |
| VXI_MEM_DD                       | Attribute register device dependent<br>bits (memory devices only). Defined<br>and reserved bits are masked to zero.                   |
| VXI_SBC_REG                      | Subclass register (extended devices only)   |
| VXI_SBC_RES                      | Reserved subclass ID (extended devices only).   |
| VXI_SBC_MNFID                    | Subclass manufacturer ID (extended devices only).   |
| VXI_SBC_MFSBC                    | Manufacturer subclass (extended devices only).  |
| VXI_MSG_PTOREG                   | Protocol register (message-based devices only).   |
| VXI_MSG_RSPREG                   | Response register (message-based devices only).   |
| VXI_MSG_DHIREG                   | Data high register (message-based<br>devices only). Warning: querying this<br>register may modify device state.                       |

| VXI_MSG_DLOREG | Data low register (message-based devices only). Warning: querying this register may modify device state.  |
|----------------|---|
| VXI_MSG_PRDD   | Protocol register device dependent bits<br>(message-based devices only).<br>Defined and reserved bits are masked<br>to zero.                                |
| VXI_MSG_RDPR   | Word serial <i>Read Protocol</i> command response (message-based devices only).   |
| VXI_MSG_RDPRDD | Word serial <i>Read Protocol</i> command<br>response device dependent bits<br>(message-based devices only) Defined<br>and reserved bits are masked to zero. |
| VXI_MSG_RRDD   | Response register device dependent<br>bits (message-based devices only)<br>Defined and reserved bits are masked<br>to zero.                                 |

No valid *Num\_Attribute* values are defined for GPIB network devices.

**Return Value** The function returns a Bus Management return value:

| EPC_INV_ATTR | The parameter <i>Num_Attribute</i> is invalid.   |  |
|--------------|--|--|
| EPC_INV_NAME | A device with the specified name does not exist.                                       |  |
| EPC_INV_PTR  | One or more of the parameters<br><i>Name_Ptr</i> and <i>Num_Result_Ptr</i> is invalid. |  |
| EPC_NO_DATA  | The resource management database does not contain the requested attribute.             |  |
| EPC_SUCCESS  | The function completed successfully.   |  |

See Also OlrmGetArbAttr, OlrmGetBoolAttr, OlrmGetStrAttr.

### OlrmGetStrAttr

**Description** Queries a string attribute.

C Synopsis

#include ''busmgr.h''
#include ''olrm.h''

| short                               |  |  |
|-------------------------------------|--|--|
| OlrmGetStrAttr(char FAR * Name_Ptr, |  |  |
| short Str_Attribute,                |  |  |
| char FAR * Str_Result_Ptr);         |  |  |

| Name_Ptr       | Name_Ptr specifies a device name.   |  |
|----------------|---|--|
| Str_Attribute  | Str_Attribute specifies a string attribute.   |  |
| Str_Result_Ptr | <i>Str_Result_Ptr</i> specifies the location of a buffer where the specified string attribute will be placed. |  |

Visual Basic Synopsis

Declare Function OlrmGetStrAttr% Lib "olrmw16.dll" (ByVal Name\_Ptr\$, ByVal Str\_Attribute%, ByVal Str\_Result\_Ptr\$)

**Remarks** OlrmGetStrAttr queries a string attribute of the specified device and places the result in the buffer pointed to by *Str\_Result\_Ptr*.

Name\_Ptr is a null-terminated ASCII string specifying a device name.

*Str\_Attribute* specifies the string attribute to query. Valid values for VXIbus devices are:

| <u>Str_Attribute</u> | *Str_Result_Ptr           |
|----------------------|---------------------------|
| VXI_CMDRNM           | Name of commander device. |
| VXI_MFNM             | Manufacturer name.        |
| VXI_MODNM            | Model name.               |
| VXI_S0NM             | Name of slot-0 device.    |

Valid *Str\_Attribute* values for network devices are:

| <u>Str_Attribute</u> | <u>*Str_Result_Ptr</u> |
|----------------------|------------------------|
| NET_ADDRESS          | Network address.       |

No valid Str\_Attribute values are defined for GPIB devices.

*Str\_Result\_Ptr* specifies the location a buffer where the function places the result of the string attribute query. The buffer must be at least **ATTRIBUTE\_SZ** bytes long.

Return Value The function returns a Bus Management return value:

|          | EPC_INV_ATTR                                     | The parameter Str_Attribute is invalid.   |
|----------|--|---|
|          | EPC_INV_NAME                                     | A device with the specified name does not exist.                                    |
|          | EPC_INV_PTR                                      | One or more of the parameters <i>Name_Ptr</i> and <i>Str_Result_Ptr</i> is invalid. |
|          | EPC_NO_DATA                                      | The resource management database does not contain the requested attribute.          |
|          | EPC_SUCCESS                                      | The function completed successfully.  |
| See Also | OlrmGetArbAttr, OlrmGetBoolAttr, OlrmGetNumAttr. |   |

## Bus Management for Windows Programmer's Reference

NOTES

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## 4. Advanced Topics

This chapter discusses topics of interest to advanced application programmers. Topics include:

- Byte Ordering and Data Representation
- Handler Operations
- Event Handler Execution Under Windows
- Event Handler Implementation
- TTL Trigger Interrupt Handling on an EPC-7
- Using the backward-compatibility library

### 4.1 Byte Ordering and Data Representation

Byte ordering adds complexity to the VXIbus interface. Many VXIbus devices use the data formats of Motorola microprocessors. Others, including RadiSys EPC controllers, use the data format of Intel microprocessors. Although the Motorola and Intel microprocessors use the same data types, the hardware representations of these data types differ.

Figure 4-1 shows how the same sequence of bytes in memory is interpreted by Intel and Motorola microprocessors. Memory value 11 is at the lowest address and memory value AA is at the highest address. The data widths shown correspond to the data operand sizes found on both microprocessors.

| Memory<br>Value | Intel<br>Order       | Data<br>Width | Motorola<br>Order  |  |
|-----------------|----------------------|---------------|--------------------|--|
| 11              | 11                   | 8 bits        | 11                 |  |
| 22              | 2211                 | 16 bits       | 1122               |  |
| 33              |                      |               |                    |  |
| 44              | 44332211             | 32 bits       | 11223344           |  |
| 55              |                      |               |                    |  |
| 66              | 665544332211         | 48 bits       | 112233445566       |  |
| 77              |                      |               |                    |  |
| 88              | 8877665544332211     | 64 bits       | 1122334455667788   |  |
| 99              |                      |               |                    |  |
| AA              | AA998877665544332211 | 80 bits       | 1122334455667788AA |  |

Figure 4-1. Byte Order Example.

#### 4.1.1 Byte Swapping Functions

The EpcSwap\* functions convert 16-bit, 32-bit, 48-bit, 64-bit and 80-bit data between Intel and Motorola byte orders (8-bit data does not require conversion).

#### 4.1.2 Correcting Data Structure Byte Ordering

Even if byte-swapping occurs during a transfer, byte ordering problems occur when data is copied between Motorola and Intel memory using a different data width than the width of the operand itself. This situation occurs when a data structure containing mixed-type fields is copied in a single operation.

The following code fragment illustrates how to use the **EpcSwap\*** functions to correct the byte order in the local copy of the data structure:

```
struct DataStructure
{
       char
                  Field8:
       char
                  Field16[2];
       char
                  Field32[4];
       char
                 Field48[6];
       char
                 Field64[8];
       char
                 Field80[10];
} data;
/* Copy the data structure to EPC memory from the VXIbus. */
(void) EpcCopyData(Session_ID,
                    (void HUGE *) Mapped_Ptr,
                    (void HUGE *) &data,
                   sizeof(struct DataStructure),
                   EPC_8_BIT,
                   &Actual Size);
/* Byte-swap the individual structure fields (data.Field8 */
/* is an 8-bit field, so it is already correct).
                                                          • /
(void) EpcSwap16((unsigned short FAR *) data.Field16);
(void) EpcSwap32((unsigned long FAR *) data.Field32);
(void) EpcSwap48((void FAR *) data.Field48);
(void) EpcSwap64((void FAR *) data.Field64);
(void) EpcSwap80((void FAR *) data.Field80);
```

In the above example, the data structure was copied from VXIbus memory one byte at a time. To copy data from EPC memory to Motorola-ordered VXIbus memory, byte-swap the fields of the structure in local memory (using the above byte swapping functions) and copy the data using the **EpcCopyData** function.

It is sometimes more efficient to copy blocks of data using data transfer width greater than the expected data width. If you use a greater data transfer width to copy data structures containing mixed-type fields to/from Motorola-order memory, do not use the EPC's hardware byte-swapping feature. Swap the data structure fields individually.

## 4.2 Event Handler Execution

These conditions must be true before an application's event handlers can execute:

- The application must use EpcSetEventHandler to install an error handler.
- The application must call EpcSetEventEnableMask to enable event reception.
- An event must occur.

The Bus Management API discards all events that occur before the application installs an event handler.

When an application installs an event handler and enables event reception, the event handler processes events as soon as they are received. The installed event handler executes as part of an interrupt thread, with virtual processor interrupts disabled, and using the installed event handler stack.

# 4.3 Event Handler Operations Under Windows

Event handlers can execute as part of an interrupt thread under Windows. This feature implies that an event handler can only call fully reentrant Bus Management, "C" library, Windows, DOS, and BIOS support functions.



Bus Management Library and ORLM library functions are fully reentrant and may be called from an event handler or any application code that executes as part of an interrupt thread. Note, however, that when called reentrantly, EPConnect Bus Management Library functions cannot yield the processor to other tasks while in a timing loop.

The following "C" library functions are reentrant under Microsoft "C" Version 6.0, and may be called from an event handler or any application code that executes as part of an interrupt thread (it is likely that this list is different for other releases of the Microsoft "C" compiler and for compilers from other vendors):

| abs<br>atoi<br>atol<br>bsearch<br>chdir<br>getpid<br>halloc<br>hfree<br>itoa | memccpy<br>memchr<br>memcmp<br>memcpy<br>memicmp<br>memmove<br>memset<br>mkdir<br>movedata | strct<br>strchr<br>strcmp<br>strcmpi<br>strcpy<br>stricmp<br>strien<br>strlen<br>strlwr<br>strncat | strnset<br>strrchr<br>strrev<br>strset<br>strstr<br>strupr<br>swab<br>tolower<br>toupper |
|--|--|--|--|
| hfree  | mkdir  | strlwr   | tolower  |

No Windows functions are fully reentrant. As such, none of the Windows functions should be called from an event handler.

Not all DOS and BIOS functions are fully reentrant. However, mechanisms exist (the "InDos" and "CriticalError" flags) for avoiding DOS reentrancy by delaying background processing until DOS is not in use.

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## 4.4 Event Handler Implementation

An event handler is called as part of an interrupt thread with its own stack. Care must be taken during implementation to avoid several pitfalls.

Since an event handler function is called as part of an interrupt thread, the event handler function must reload the data segment register (DS) with its data segment upon entry. Any of the following three methods will correctly load the data segment register for an event handler function:

- 1. Explicitly declare the event handler function to be an exported function in the EXPORTS section of the application's module definition (.DEF) file.
- 2. Explicitly declare the event handler function to be an exported function using the "C" language "\_export" function declaration.
- 3. Explicitly declare that the event handler function reloads the data segment register upon entry using the "C" language "\_loadds" function declaration.

Since an event handler function is called using the installed event handler stack, an event handler function written in "C" must be compiled with the assumption that the data segment is not equivalent to the stack segment (DS != SS). Otherwise, a catastrophic failure can occur when the event handler function is called. For Microsoft compilers, use the "/Alfw" memory model parameter. For Borland compilers, use the "-ml" memory model parameter.

Since an event handler function is called using the installed event handler stack, an event handler function written in "C" must be compiled with automatic stack checking disabled. Otherwise, a catastrophic failure will occur when the event handler is called. For Microsoft compilers, use the "/Gs" parameter. For Borland compilers, avoid using the "-N" parameter.

Since an event handler function is generally performance-critical, its code and data segments should be carefully defined for maximum performance. For maximum performance, define the event handler function's code and data segments to be PRELOAD, FIXED, and NONDISCARDABLE in the application's module definition (.DEF) file.



## 4.5 TTL Trigger Interrupts on an EPC-7

Receiving and processing TL trigger interrupts on an EPC-7 requires software intervention. EPC-7 hardware generates a TTL trigger interrupt when all of the following conditions are true:

- A bit in the TTL trigger interrupt enable register is set. The Bus Management Library function EpcSetEventEnableMask sets and/or clears the register's bits.
- The corresponding bit in the TTL trigger latch register is clear.
- The corresponding TTL trigger line is asserted for at least 30 nanoseconds.

The main complication in this scenario is that a bit in the TTL trigger latch register cannot be cleared until the corresponding TTL trigger line is deasserted. In order to clear a bit in the register, the register must be read while the corresponding TTL trigger line is deasserted. TTL trigger line assertion is not necessarily under EPC control.

The operation of the EPC-7 TTL trigger latch register has three potential side effects for software:

- If a TTL trigger interrupt remains enabled after receiving the initial interrupt and clearing the TTL trigger latch register, the CPU can be monopolized by redundant TTL trigger interrupts.
- If a TTL trigger latch register bit is not cleared before enabling the corresponding TTL trigger interrupt, it is possible to receive an interrupt for a TTL trigger that was asserted, latched, and deasserted long before the TTL trigger interrupt was enabled.
- If a TTL trigger latch register bit is not cleared after receiving the corresponding TTL trigger interrupt, the EPC will not latch subsequent TTL trigger line assertions and, therefore, will miss subsequent TTL trigger interrupts.

To avoid the first side effect, the Bus Management for Windows implementation globally disables a TTL trigger interrupt upon reception. In addition, the Bus Manager Library implementation provides sufficient functionality to avoid the other two side effects.

To avoid the side effect of receiving extraneous TTL trigger interrupts, execute **EpcGetMiscAttributes** before calling **EpcGetEventEnableMask** and **EpcSetEventEnableMask** to enable TTL trigger interrupts for a session.

For example:

```
void FAR PASCAL
EnableTTLTriggerInterrupts (unsigned long Session ID, unsigned
long Event_Mask)
{
       unsigned long mask1;
       unsigned long mask2;
        /*
         * Wait for corresponding TTL trigger latch register
         * bits to clear, then enable the TTL trigger
         * interrupts.
         */
       mask1 = Event_Mask << 4;</pre>
       for (;;)
       {
              EpcGetMiscAttributes(Session ID, &mask2);
              if ((mask1 \& mask2) == 0)
              {
                     break;
              }
       }
       EpcGetEventEnableMask(Session_ID, &mask1);
       EpcSetEventEnableMask(Session_ID, mask1 | Event_Mask);
}
```

To avoid the side effect of missing multiple TTL trigger interrupts from the same TTL trigger, re-enable the interrupt immediately after receiving a TTL trigger interrupt, preferably as part of the event handler function itself. For example:



```
*/
EnableTTLTriggerInterrupts(Session_ID,Event_Mask;
/*
 * Execute other event handler tasks...
*/
```

### 4.6 Backward-Compatibility Library

}

The Backward Compatibility Library (EPCDICW.DLL) and its corresponding import library (EPCDICW.LIB) provide a level of compatibility between the Windows and DOS programming interfaces. Most of the functions available in the DOS Bus Management Library are available in the Windows Backward-Compatibility Library with identical calling conventions. However, the functionality the two libraries provide is not strictly identical. Differences between the Windows Backward-Compatibility Library and the DOS Bus Management Library include the following:

- No Message Delivery System (MDS) functionality is available in the Windows Backward-Compatibility Library. If using MDS support under Windows, the application should be ported to SICL. If MDS support under Windows is a **requirement**, users should not upgrade beyond EPConnect/VXI for DOS version 3.11.
- The Windows Backward Compatibility Library supports A16S, A24SD, A24S, A32SD, and A32S VMEbus address modifiers only. Attempting to use other VMEbus address modifiers (A16N, A16U, A24ND, A24NP, A24NP, A24N, A24SP, A32ND, A32NP, A32N, A32U and A32SP) under Windows results in an ERR\_FAIL error and/or a null mapped pointer.
- The Windows Backward Compatibility Library can access the first gigabyte of A32 space only (addresses 0x00000000 through 0x3FFFFFFF). Attempting to map higher A32 space addresses under Windows results in an ERR\_FAIL error and/or a null mapped pointer.
- The Windows Backward Compatibility Library automatically disables persistent interrupt and error events when they are received. Automatic disabling of persistent events prevents the generation of multiple,

redundant events. Persistent events include EPC\_MSG\_INTR, EPC\_TTL\_TRIG\*\_INTR, EPC\_SYSFAIL\_ERR, EPC\_ACFAIL\_ERR, and EPC\_WATCHDOG\_ERR. Under Windows, when one of these persistent events occurs, it must be re-enabled by the application before it will be received again.

• The Windows Backward Compatibility Library supports standard servant word serial communications only. The RadiSys-specific protocol for multiple-commander word serial communications is not supported. Under Windows, the multiple-commander arming codes (EPC\_WSRCV\_DISARM, EPC\_WSRCV\_ARM, and EPC\_WSRCV\_ARMandENABLE) and the single-commander arming codes (EPC\_WSRCV\_FDISARM, EPC\_WSRCV\_FARM, and EPC\_WSRCV\_FARMandENABLE) are equivalent.



# 5. Support and Service

## 5.1 In North America

#### 5.1.1 Technical Support

RadiSys maintains a technical support phone line at (503) 646-1800 that is staffed weekdays (except holidays) between 8 AM and 5 PM Pacific time. If you have a problem outside these hours, you can leave a message on voice-mail using the same phone number. You can also request help via electronic mail or by FAX addressed to RadiSys Technical Support. The RadiSys FAX number is (503) 646-1850. The RadiSys E-mail address on the Internet is *support@radisys.com*. If you are sending E-mail or a FAX, please include information on both the hardware and software being used and a detailed description of the problem, specifically how the problem can be reproduced. We will respond by E-mail, phone or FAX by the next business day.

Technical Support Services are designed for customers who have purchased their products from RadiSys or a sales representative. If your RadiSys product is part of a piece of OEM equipment, or was integrated by someone else as part of a system, support will be better provided by the OEM or system vendor that did the integration and understands the final product and environment.

#### 6.1.2 Bulletin Board

RadiSys operates an electronic bulletin board (BBS) 24 hours per day to provide access to the latest drivers, software updates and other information. The bulletin board is not monitored regularly, so if you need a fast response please use the telephone or FAX numbers listed above.

The BBS operates at up to 14400 baud. Connect using standard settings of eight data bits, no parity, and one stop bit (8, N, 1). The telephone number is (503) 646-8290.

## **5.2 Other Countries**

Contact the sales organization from which you purchased your RadiSys product for service and support.

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