

# Recent IBM patents

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*The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.*

## France

433,520	A. Pauporte, C. Poiraud, and J. M. Munier	Elastic Configurable Buffer for Buffering Asynchronous Data
515,760	D. Chevalier, G. Platel, J. Gallian, M. Geneste, and P. Massi	Communication Controller Product Having Line Adapters Loaded with an Application Program During an Initialization Phase Prior to the Data Communication Phase and Method for Performing the Loading of the Storage of a Line Adapter with an Application Program
539,642	D. Pinchon	Method for Determining the Composition of a Compound Color Which is a Mixture of Basic Component Colors

## Germany

315,161	H. Rager	Device for Placing Components in Particular on Circuit Boards
4,209,280.9	M. Zoeppritz	Computer System and Method for the Automated Analysis of Texts
4,213,533.8	L. Schoenherr	System for the Automated Analysis of Compound Words
4,411,192.4	F. Notter, G. Klein, and J. Haumann	Receiving Device for Components Directly Connected with a Board
4,413,173.9	F. Notter and H. G. Bernhardt	Apparatus for Uninterruptable Cooling of a Device
69,115,898.3	H. Schulze-Schoelling, J. J. Ruedinger, and P. Rudolph	Out of Phase, Synchronous Interface

## Japan

948,163	K. Nakada and T. Takahashi	Industrial Design of CRT Display
948,164	T. Imai	Thermal Printer
949,289	K. Nakada	Industrial Design of Desktop PC
949,291	T. Takahashi	Industrial Design of Yahagi
949,293	K. Takado, M. Aoyagi, S. Iwata, and Y. Sawada	19mm Height CD-ROM Drive with 3.5" Form Factor Drive Unit
949,293-1	T. Ikegami and Y. Horiuchi	Movable Computer System
949,294	K. Nakada	Industrial Design of External FDD
949,297	H. Shima and K. Yamazaki	Industrial Design for Docking Station
949,312	S. S. Moffatt and T. Imai	Personal Computer
949,859	K. Yamazaki	Port Repuricator
949,863	K. Nakada, K. Yamazaki, and T. A. Merino	Industrial Design of Camera Unit
1,997,210	K. Sekiya and Y. Shiraishi	A Quick Method of Top-Margin Setting on PDP-Display
1,998,357	Y. Kobayashi	High Speed Image Processor for Mask Operation
2,001,016	H. Takahashi and S. Katoh	Method for Segmenting Character Components
2,003,597	K. Inoue, M. Shinomura, and N. Harada	Dynamic Configuration Selection for Micro Channel/EISA Systems

2,003,887	H. Yanagisawa and S. Asada	Processor Synchronous Image Scanner Controlling Method
2,006,851	T. Furuhashi and T. O. Hirano	Array-Type Microactuator Using the Difference Between Maximum Static Friction and Moving Friction
2,008,210	K. Sato, K. Satoh, R. Sapper, S. T. Chambers, and T. Sawa	Bill Recyclable Dispense/Deposit Machine
2,010,541	K. Sekiya and Y. Shiraishi	A Mapping Method M Grays out of K to N Fixed Grays
2,010,703	H. Nomura, T. Ogawa, and Y. Uchiyama	Panel Latch Mecha for One Hand
2,012,605	H. Kaneko	A Method of Learning Phrase Boundary in Kana-to-Kanji Conversion
2,014,827	F. Hayashiguchi and T. Tokuda	Improved LCD Data Transfer Sequence
<b>Switzerland</b>		
471,130	E. A. Zurfluh and R. Cideciyan	Coding Method and Apparatus for Pipelined and Parallel Processing
482,271	B. Michel and U. T. Duerig	Current-to-Voltage Converter with Low Noise, Wide Bandwidth and High Dynamic Range
<b>United Kingdom</b>		
414,694	P. Baker and V. P. Thomas	Estimator Positioning System and Method
432,333	G. H. Speirs and V. G. Golding	Computer System Security Device
2,051,406	J. Tierney, M. Oates, and P. Flowers	Personal Computer System
2,051,407	M. Oates	Visual Display Unit
2,269,080	A. J. Morrish	Deflection Apparatus for Raster Scanned CRT Displays
2,270,446	C. J. Holloway	Initial Symmetric Key Distribution Without Trusted Couriers and Without Public Key Algorithms
<b>United States</b>		
5,448,558	K. J. Gildea, P. H. Hochschild, and Y.-P. Huang	Method and Apparatus for Managing Packet FIFOs
5,450,158	R. F. Jesson, C. A. Mertens, N. J. Olsen, and R. J. Telfer	Optical Sensor for a Jam-Free Continuous-Forms Printer
5,456,081	G. M. Chrysler, R. C. Chu, R. E. Simons, and D. T. Vader	Thermoelectric Cooling Assembly with Optimized FIN Structure for Improved Thermal Performance and Manufacturability
5,456,788	S. G. Barbee, M. Datta, T. F. Heinz, L. Li, E. H. Ratzlaff, and R. V. Shenoy	Method and Apparatus for Contactless Real-Time In-Situ Monitoring of a Chemical Etching Process
5,457,005	E. D. Babich, J. D. Gelorme, R. W. Nunes, S. L. Nunes, J. R. Paraszczak, and R. J. Serino	Dry Developable Photoresist and Use Thereof
5,457,071	E. J. Dombroski	Stackable Vertical Thin Package/Plastic Molded Lead-on-Chip Memory Cube
5,457,299	C. Blais and P. A. Chalco	Semiconductor Chip Packaging Method Which Heat Cures an Encapsulant Deposited on a Chip Using a Laser Beam to Heat the Back Side on the Chip
5,457,345	H. C. Cook, P. A. Farrar, Sr., R. M. Geffken, W. T. Motsiff, and A. E. Wirsing	Metallization Composite Having Nickel Intermediate/Interface
5,457,415	E. S. Schlig	Charge Metering Sampling Circuit and Use Thereof in TFT/LCD
5,457,658	H. Nijima, H. Asano, Y. Sakaue, T. Toyooka	Nonvolatile Memory with Cluster-Erase Flash Capability and Solid State File Apparatus Using the Same
5,457,680	D. A. Kamm, S. T. Parker, M. A. Trimmer, Jr., and S. L. Trimmer	Data Gateway for Mobile Data Radio Terminals in a Data Communication Network
5,457,718	C. J. Anderson, A. X. Widmer, and K. R. Wrenner	Compact Phase Recover Scheme Using Digital Circuits
5,457,775	L. E. Johnson, Jr., D. J. Kokoszka, S. P. Larky, and P. Sidoli	High Performance Triangle Interpolator
5,457,787	H. Asano and M. Murakami	Interface Circuit for Controlling Data Transfers
5,457,789	W. C. Dietrich, Jr., M. A. Lavin, H. Li, and M.-C. Sheng	Method and Apparatus for Performing Memory Protection Operations in a Single Instruction Multiple Data System
5,457,793	D. A. Elko, J. A. Frey, B. B. Moore, J. M. Nick, K. F. Smith, and M. D. Swanson	Software Cache Management of a Shared Electronic Store in a Supplex
5,457,882	N. B. Clarke, L. G. Cook, R. G. Doyle, and M. Renner	Method of Making Conductive Molded Tip Head Assembly
5,458,281	A. J. Downing, D. C. Foster, and K. J. Puttlitz	Method for Removing Melttable Material from a Substrate
5,458,520	T. A. DeMercurio, K. H. Wong, and R. Yu	Method for Producing Planar Field Emission Structure
5,458,756	E. Bassous, J.-M. Halbout, S. S. Iyer, and V. P. Kesan	Apparatus for Producing Porous Silicon on a Substrate

5,458,908	M. T. Krounbi, K. T. Kung, and C. H. Tsang	Method of Fabricating a Magneto-resistive Read Transducer
5,459,001	S. A. Estes, T. B. Faure, and S. C. Nash	Low Stress Electrodeposition of Gold for X-ray Mask Fabrication
5,459,309	N. Kagami and M. Takekoshi	Focus Acquiring Method and Optical Disk Drive
5,459,624	K. J. Erickson and A. H. Sacks	Activator Control Method and Apparatus for Positioning a Transducer Using a Phase Plane Trajectory Through Function for a Direct Access Storage Device with Estimated Velocity and Position States
5,459,628	P. E. Brooks	Disk Drive with Profiled Attachment of a Spindle and Hub
5,459,725	R. A. Bodner, C.-S. Chow, I. Cidon, J. G. Dudley, A. K. Edwards, I. S. Gopal, C. P. Immanuel, M. A. Kaplan, S. Kutten, and T. E. Tedijanto	Reliable Multicasting Over Spanning Trees in Packet Communications Networks
5,459,740	R. J. Glaise	Method and Apparatus for Implementing a Triple Error Detection and Double Error Correction Code
5,459,821	S. Kuyiyama, H. Minakata, N. Urano, and K. Shimizu	Method and Apparatus for Generating a Free-Form Surface
5,459,831	E. A. Brewer and M. B. Pinson	Method for Selecting Graphical Objects in Quadrants with a Cursor
5,459,838	A. S. Coscarella and J. L. Temple	I/O Access Method for Using Flags to Selectively Control Data Operation Between Control Unit and I/O Channel to Allow Them to Proceed Independently and Concurrently
5,459,839	J. S. Swarts and G. L. Rouse	System and Method for Managing Queue Read and Write Pointers
5,459,842	R. M. Begun, P. W. Browne, M. R. Faucher, G. L. Frank, and C. M. Herring	System for Combining Data from Multiple CPU Write Requests Via Buffers and Using Read-Modify-Write Operation to Write the Combined Data to the Memory
5,459,843	G. T. Davis, S. T. Ventrone, J. J. Reilly, R. D. Mandalia, M. G. Holung, and W. R. Robinson, Jr.	RISC-Type Pipeline Processor Having N Slower Execution Units Operating in Parallel Interleaved and Phase Offset Manner with a Faster Fetch Unit and a Faster Decoder
5,459,844	R. J. Eickemeyer, S. Vassiliadis, and B. Blaner	Predecode Instruction Compounding
5,459,849	J. J. Bergkvist, Jr., M. J. Peters, and D. M. Wronski	Method and Apparatus for Compressing Cacheable Data
5,459,853	J. S. Best, S. R. Hetzler, R. F. Hoyt, J. M. Menon, and M. F. Mitoma	Efficient Variable-Block Data Storage System Employing a Staggered Fixed-Block-Architecture Array
5,459,858	L. E. Lyon and R. L. Teed	Method for File Transfer
5,459,860	C. J. Burnett and G. L. Haas	Computerized System and Process for Managing a Distributed Database System
5,459,864	G. A. Brent, T. J. Dewkett, C. R. Panner, and C. A. Scalzi	Load Balancing, Error Recovery, and Reconfiguration Control in a Data Movement Subsystem with Cooperating Plural Queue Processors
5,459,870	H. Iwasa, M. Murakami, and H. Saitoh	Interface Circuit for Controlling Data Transfers
5,460,476	J. M. Gazza	Dual Gripper Picker Assembly for an Automated Storage System
5,460,858	S. Yamada and M. Shirai	Method for Forming a Smooth-Surfaced Insulating Layer
5,460,907	S. P. Ducharme, W. E. Moerner, J. C. Scott, and R. J. Twieg	Photorefractive Materials
5,460,921	D. A. Cywar, C. R. Davis, T. P. Duffy, F. D. Egitto, P. J. Hart, G. W. Jones, and E. McLeskey	High Density Pattern Template: Materials and Processes for the Application of Conductive Pastes
5,461,203	K. J. Blackwell, P. C. Chen, F. D. Egitto, A. R. Knoll, G. J. Matarese, and L. J. Matienzo	Electronic Package Including Lower Water Content Polyimide Film
5,461,243	B. A. Ek, S. S. Iyer, P. M. Pitner, A. R. Powell, and M. J. Tejwani	Substrate for Tensilely Strained Semiconductor
5,461,250	J. N. Burghartz, B. S. Meyerson, and Y.-C. Sun	SiGe Thin Film or SOI MOSFET and Method for Making the Same
5,461,324	J. E. Boyette, C. M. Fleck, J. C. Mahlbacher, and M. Servedio	Split-Fixture Configuration and Method for Testing Circuit Traces on a Flexible Substrate
5,461,331	E. B. Schorn	Dynamic to Static Logic Translator or Pulse Catcher
5,461,399	B. J. Cragun	Method and System for Enabling Visually Impaired Computer Users to Graphically Select Displayed Objects
5,461,415	J. L. Wolf and P. S. Yu	Look-Ahead Scheduling to Support Video-on-Demand Applications
5,461,455	P. W. Coteus and D. S. Goodman	Optical System for the Projection of Patterned Light onto the Surface of Three Dimensional Objects
5,461,536	D. B. Beach, A. Grill, and C. J. Smart	Storage Capacitors Using High Dielectric Constant Materials
5,461,600	W. D. Pohl	High-Density Optical Data Storage Unit and Method for Writing and Reading Information
5,461,611	J. E. Drake, Jr., E. A. Hervatic, J. W. Pace, R. J. Potok, and D. E. Taber	Quality of Service Management for Source Routing Multimedia Packet Networks
5,461,631	M. M. Blaum, J. Bruck, and C. M. Melas	Method for Bit Resynchronization of Code-Constrained Sequences

5,461,638	R. A. Hutchins, C. M. Melas, and P. Sutardja	Discrete Time Control Loop Method for Clocking Data in an Asynchronous Channel
5,461,641	J.-C. Abbiate, A. Blanc, P. Jeannot, and G. Richter	Decimation Filter for a Sigma-Delta Converter and A/D Converter Using the Same
5,461,642	R. A. Hutchins, C. M. Melas, and P. Sutardja	Discrete Time Control Loop Method and Apparatus for Clocking Data in an Asynchronous Channel
5,461,699	M. Arbabi and S. M. Fischthal	Forecasting Using a Neural Network and a Statistical Forecast
5,461,710	M. A. Bloomfield and S. A. Klinger	Method for Providing a Readily Distinguishable Template and Means of Duplication Thereof in a Computer System Graphical User Interface
5,461,712	I. C. Chelstowski and P. R. Brown	Quadrant-Based Two-Dimensional Memory Manager
5,461,716	S. T. Eagen, H. G. Kiel, N. A. Martel, Jr., W. C. Rapp, and S. M. Shao	Method and Apparatus for Assisting in the Presentation and Removal of Windows
5,461,720	B. C. Beardsley and A. G. Yanes	System for Increasing the Efficiency of Communications Between Controller and Plurality of Host Computers by Prohibiting Retransmission the Same Message for Predetermined Period of Time
5,461,721	R. L. Cormier, R. J. Dugan, K. J. Fredericks, P. H. Gum, M. J. Kim, A. H. Preston, R. J. Schmalz, C. F. Webb, and L. W. Wyman	System for Transferring Data Between I/O Devices and Main or Expanded Storage Under Dynamic Control of Independent Indirect Address Words (IDAWS)
5,462,628	A. Viehbeck, M. Goldberg, S. L. Tisdale, S. L. Buchwalter, K. R. Grebe, C. A. Kovac, and L. C. Matthew	Method for Bonding Two Surfaces Together
5,462,636	G. Y. Chen and E. H. P. Lee	Method for Chemically Scribing Wafers
5,462,637	M. Thiele	Process for Fabrication of Thin Film Magnetic Heads
5,462,638	M. Datta and R. V. Shenoy	Selective Etching of TiW for C4 Fabrication
5,462,784	A. Grill and V. V. Patel	Fluorinated Diamond-Like Carbon Protective Coating for Magnetic Recording Media Devices
5,462,883	R. H. Dennard, B. S. Meyerson, and R. Rosenberg	Method of Fabricating Defect-Free Silicon on an Insulating Substrate
5,462,897	T. H. Baum, S. P. Mukherjee, T. R. O'Toole, A. F. Tai, and A. Viehbeck	Method for Forming a Thin Film Layer
5,463,254	S. S. Iyer, R. D. Thompson, and K.-N. Tu	Formation of 3-Dimensional Silicon Silicide Structures
5,463,269	S. M. Zimmerman	Process and Structure of an Integrated Vacuum Microelectronic Device
5,463,335	S. Divakaruni, J. H. Dreibelbis, W. F. Ellis, A. Furman, and H. L. Kalter	Power Up Detection Circuits
5,463,339	S. R. Riggio, Jr.	Amorphous, Thin Film Transistor Driver/Receiver Circuit with Hysteresis
5,463,525	M. S. Barnes, J. H. Keller, J. S. Logan, R. E. Tompkins, and R. P. Westerfield, Jr.	Guard Ring Electrostatic Chuck
5,463,574	B. Desrosiers, D. Louis, D. Pinchon, and A. Steimle	Apparatus for Argument Reduction in Exponential Computations of IEEE Standard Floating-Point Numbers
5,463,578	R. J. Gambino and R. R. Ruf	Magneto-Optic Memory Allowing Direct Overwrite of Data
5,463,625	M. Yasrebi	High Performance Machine for Switched Communications in a Heterogeneous Data Processing Network Gateway
5,463,634	J. L. Smith, B. S. Trubey, and A. D. Walker	Dual Ring Fault Isolation
5,463,701	R. F. Kantner, Jr., V. S. Iverson, K. Morse, M. A. Pietras, and A. A. Rodriguez	System and Method for Pattern-Matching with Error Control for Image and Video Compression
5,463,705	R. Clauberg, C. Harder, C. Heusch, and H. Jaeckel	Optical Waveguide Isolation
5,463,723	A. Saha	Method and Apparatus for Filling Polygons
5,463,725	J. D. Henckel and P. A. Hospers	Data Processing System Graphical User Interface Which Emulates Printed Material
5,463,726	R. T. Price	Method and Apparatus for Graphic Accessing of Multiple Software Applications
5,463,733	I. R. Forman and H. H. Madduri	Failure Recovery Apparatus and Method for Distributed Processing Shared Resource Control
5,463,736	D. A. Elko, J. A. Frey, A. A. Helffrich, J. F. Isenberg, Jr., B. B. Moore, J. M. Nick, M. D. Swanson, and J. A. Williams	Coupling Facility for Receiving Commands from Plurality of Hosts for Activating Selected Connection Paths to I/O Devices and Maintaining Status Thereof
5,463,739	V. A. Albaugh, J. S. Muhich, E. J. Silha, and M. T. Vanover	Apparatus for Vetoing Reallocation Requests During a Data Transfer Based on Data Bus Latency and the Number of Received Reallocation Requests Below a Threshold
5,463,741	S. B. Levenstein	Duplicated Logic and Interconnection System for Arbitration Among Multiple Information Processors

5,463,746	T. B. Brodnax, B. K. Bullis, S. A. King, P. M. Kogge, and D. A. Rickard	Data Processing System Having Prediction by Using an Embedded Guess Bit of Remapped and Compressed Opcodes
5,463,752	M. T. Benhase, B. C. Beardsley, W. G. Sherman II, and A. G. Yanes	Method and System for Enhancing the Efficiency of Communication Between Multiple Direct Access Storage Devices and a Storage System Controller
5,463,754	W. F. Beausoleil, C. R. Berghorn, J. A. Hupcey, and S. J. Schlosser	Shared Direct Access Storage Device for Fixed Block Architecture Devices
5,463,755	D. P. Dumarot and A. Garcia	High-Performance, Multi-Bank Global Memory Card for Multiprocessor Systems
5,463,758	H. H. Ottesen	System and Method for Reducing Seek Time for Read Operations in Mirrored DASD Files
5,463,764	T. R. Mueller	Method and System for System Debugging Through a Keyboard Device Driver
5,463,769	B. A. Tate, A. C. Chow, and S. B. Gest	Method and Apparatus Using Dictionary of Methods and States for High Performance Context Switching Between Build and Run Modes in a Computer Application Builder Program
5,463,770	S. J. P. Todd	System and Method for Controlling Data Storage in a Data Processing System in Which Objects Generated Within a Lower Level Language Program Are Associated with Memory Slots Allocated to Another Program
5,463,775	J. E. DeWitt, T. M. Holck, J. H. Summers, and S. L. Emrick	System and Method for Performing Monitoring of Resources in a Data Processing System in Real Time
5,463,872	D. T. Vader and V. C. Vasile	High Performance Thermal Interface for Low Temperature Electronic Modules
5,464,500	T. Tsujimura and H. Kitahara	Method for Taper Etching Metal
5,464,682	E. D. Perfecto, C. Prasad, K. Prasad, G. J. Robbins, M. Swaminathan, and G. E. White	Minimal Capture Pads Applied to Ceramic Vias in Ceramic Substrates
5,464,726	J. D. Gelorme, E. R. Skarvinko, and D. W. Wang	Photosensitive Composition and Use Thereof
5,464,927	M. Angelopoulos, J. D. Gelorme, J. W. Labadie, D. A. Lewis, S. A. Swanson, N. C. Labianca	Polyamic Acid and Polyimide from Fluorinated Reactant
5,465,039	I. Narita, K. Suzuki, T. Mito, and N. Tsurukawa	Power Supply for Electronic Device, and Electronic Device System
5,465,060	A. R. Pelella	Fast Edge Triggered Self-Resetting CMOS Receiver with Parallel L1/L2 (Master/Slave) Latch
5,465,118	S. M. Hancock and M. A. Pietras	Luminance Transition Coding Method for Software Motion Video Compression/Decompression
5,465,121	J. L. Blalock and W. R. Sterrett	Method and System for Compensating for Image Distortion Caused by Off-Axis Image Projection
5,465,155	A. D. Edgar	Duplex Film Scanning
5,465,185	D. E. Heim and S. S. P. Parkin	Magnetoresistive Spin Valve Sensor with Improved Pinned Ferromagnetic Layer and Magnetic Recording System Using the Sensor
5,465,186	C. H. Bajorek, A. D. Erpelding, G. A. Garfunkel, S. Pattanaik, N. L. Robertson, and A. J. Wallash	Shorted Magnetoresistive Head Leads for Electrical Overstress and Electrostatic Discharge Protection During Manufacture of a Magnetic Storage System
5,465,191	H. Nomura, Y. Uchiyama, and T. Ogawa	Single Hand Operable Latch Mechanism for Hinged Container
5,465,230	F. A. Montegari	Read/Write/Restore Circuit for Memory Arrays
5,465,251	I. D. Judd and R. Beer	Network Addressing
5,465,262	T. J. Dell and J. G. Foster	Method and Structure for Providing Error Correction Code and Automatic Parity Sensing
5,465,309	W. J. Johnson	Method of and Apparatus for Character Recognition Through Related Spelling Heuristics
5,465,317	E. A. Epstein	Speech Recognition System with Improved Rejection of Words and Sounds Not in the System Vocabulary
5,465,327	D. S. Wang and M. L. Williams	Method and System for Document Administration Utilizing Iconic Representations Within a Data Processing System
5,465,328	R. Dievendorf and C. Mohan	Fault-Tolerant Transaction-Oriented Data Processing
5,465,331	M. S. Yang and J.-S. Yih	Apparatus Having Three Separated and Decentralized Processors for Concurrently and Independently Processing Packets in a Communication Network
5,465,332	M. J. Deloye, D. P. Fuoco, and D. L. Moeller	Selectable 8/16 Bit DMA Channels for "ISA" Bus
5,465,333	H. T. Olnowich	Apparatus for Programming the Speed at Which an Expansion Card Generates Ready Signals to Insure Compatibility with the Speed of an Attached Bus
5,465,336	B. T. Imai, H. Q. Le, and D. Q. Nguyen	Fetch and Store Buffer that Enables Out-of-Order Execution of Memory Instructions in a Data Processing System
5,465,342	S. J. Walsh	Dynamically Adaptive Set Associativity for Cache Memories

5,465,346	A. Blanc and L. Ceresiani	Parallel Processing of Received and Transmitted Bit Stream in Telecommunications Equipment Including a DSP and Supporting HDLC/SDLC Protocols
5,465,347	H. H. Chao, J. H. Chang, and F.-H. W. Shih	System for Reducing Phase Difference Between Clock Signals of Integrated Circuit Chips by Comparing Clock Signal from One Chip to Clock Signal from Another Chip
5,465,355	T. E. Cook, M. J. Fantacone, R. E. Galbraith, A. S. Meritt, K. J. Oakes, and H. M. Yudenfriend	Establishing and Restoring Paths in a Data Processing I/O System
5,465,357	R. Bealkowski, D. R. Geisler, and M. R. Turner	Method and Apparatus for an Automated Dynamic Load of an BIOS Device Support Layer in a Computer System
5,465,358	J. A. Blades and H. G. Kiel	System for Enhancing User Efficiency in Initiating Sequence of Data Processing System User Inputs Using Calculated Probability of User Executing Selected Sequences of User Inputs
5,465,359	R. A. Allen, L. M. Goetze, J. M. Nick, K. B. Pushong, D. H. Surman, and M. D. Swanson	Method and System for Managing Data and Users of Data in a Data Processing System
5,465,364	F. L. Lathrop and K. A. Rowland	Method and System for Providing Device Driver Support Which is Independent of Changeable Characteristics of Devices and Operating Systems
5,465,373	J. A. Kahle, C.-C. Kau, D. S. Levitan, A. D. Ogden, A. A. Poursepanj, P. K.-G. Tu, and D. E. Waldecker	Method and System for Single Cycle Dispatch of Multiple Instructions in a Superscalar Processor System
5,465,374	R. M. Dinkjian, L. C. Heller, S. R. Kordus, K. A. Lauricella, T. W. Seigendall, R. A. Skaggs, and N. S. Xu	Processor for Processing Data String by Byte-by-Byte
5,465,377	B. Blaner and S. Vassiliadis	Compounding Preprocessor for Cache for Identifying Multiple Instructions Which May be Executed in Parallel
5,465,382	K. F. Day III, W. D. Lamear, Jr., and E. R. Morse	System and Method for Mapping Directly Accessible Magnetic DASD Storage to Fixed Block Optical Storage
5,465,859	J. D. Chapple-Sokol, L. L.-C. Hsu, P. J.-M. Tsang, and C.-M. Yuan	Dual Phase and Hybrid Phase Shifting Mask Fabrication Using a Surface Etch Monitoring Technique
5,466,539	S. Takayama	High Permeability Iron-Based Alloy
5,466,625	C.-M. Hsieh, L. L. C. Hsu, and S. Ogura	Method of Making a High-Density DRAM Structure on SOI
5,466,626	M. Armacost, A. R. Baker, Jr., W. S. Berry, D. A. Carl, D. M. Kenney, and T. J. Licata	Micro Mask Comprising Agglomerated Material
5,466,634	K. E. Beilstein, Jr., C. L. Bertin, J. E. Cronin, W. J. Howell, J. M. Leas, and R. B. Phillips	Electronic Modules with Interconnected Surface Metallization Layers and Fabrication Methods Therefore
5,466,636	J. E. Cronin, C. W. Kaanta, D. M. Kenney, H. S. Landis, B. J. Machesney, P. Parries, R. A. Previti-Kelly, and J. F. Rembetski	Method of Forming Borderless Contacts Using a Removable Mandrel
5,466,904	H. C. Pfeiffer and W. Stickel	Electron Beam Lithography System
5,466,980	M. D. Potter	Charged Rotor Pole Micromachine Motor
5,466,993	D. Leaver	Deflection Apparatus for Raster Scanned CRT Displays
5,467,020	J. E. Boyette, Jr. and J. C. Mahlbacher	Testing Fixture and Method for Circuit Traces on a Flexible Substrate
5,467,037	M. Kumar, G. M. Lattimore, and J. M. Poplawski, Jr.	Reset Generation Circuit to Reset Self Resetting CMOS Circuits
5,467,249	M. S. Barnes, J. H. Keller, J. S. Logan, R. E. Tompkins, and R. P. Westerfield, Jr.	Electrostatic Chuck with Reference Electrode
5,467,285	D. R. Flinn, J. F. Laszcz, and D. H. Withers	Method and System for Optimizing Material Movement Within a Computer Based Manufacturing System
5,467,305	C. L. Bertin, D. J. DiMaria, M. Miyakawa, and Y. Sakaue	Three-Dimensional Direct-Write EEPROM Arrays and Fabrication Methods
5,467,311	S. K. Wiedmann and D. F. G. Wendel	Circuit for Increasing Data-Valid Time Which Incorporates a Parallel Latch
5,467,350	S. Price, R. Jepson, B. J. Worley, S. P. Wood, and A. J. Keogh	Conversion of ISDN LAPB and ISDN LAPD Frames
5,467,352	I. Cidon, L. Georgiadis, R. A. Guerin, Y. Y. Shavitt, and A. E. Slater	Method and Apparatus for Improved Throughput in a Multi-Node Communication System with a Shared Resource
5,467,359	P. J. Huon, P. Jachimczyk, G. Barucchi, J. Calvignac, and F. Verplanken	Apparatus for Generating and Checking the Error Correction Codes of Messages in a Message Switching System
5,467,361	L. R. Shipman, Jr.	Method and System for Separate Data and Media Maintenance Within Direct Access Storage Devices
5,467,425	R. Lau, R. Rosenfeld, and S. Roukos	Building Scalable N-Gram Language Models Using Maximum Likelihood Maximum Entropy N-Gram Models