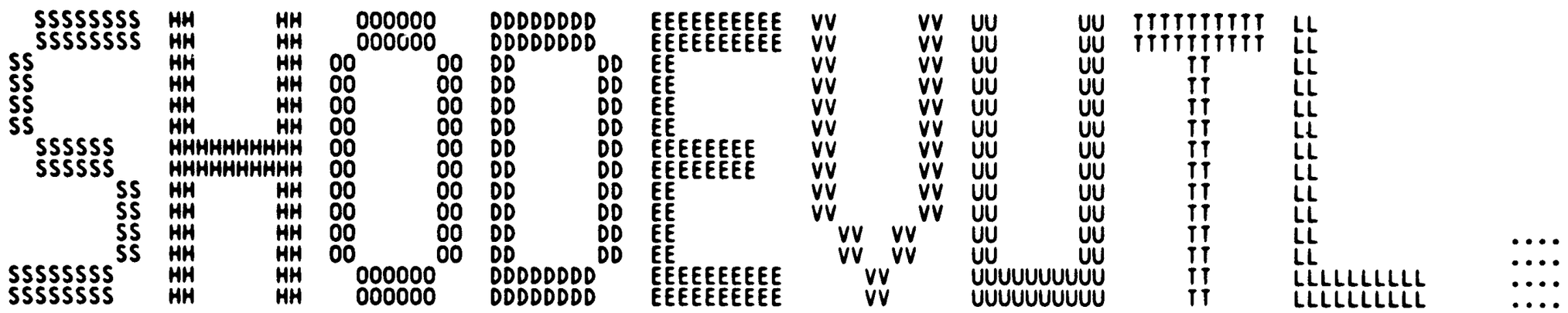
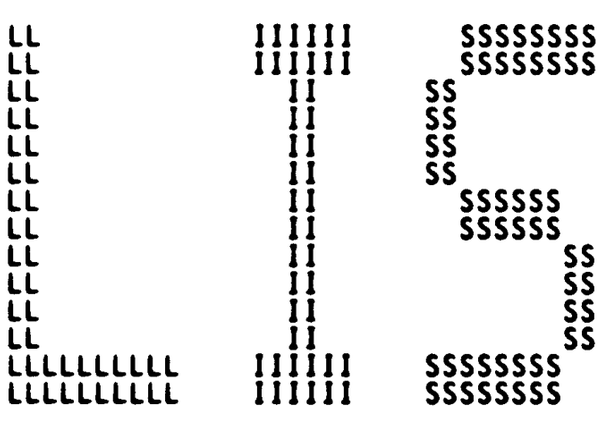
1 3

```
   CCCCCCCCCCCC  LLL                  IIIIIIIII     UUU         UUU  TTTTTTTTTTTTTTT   LLL
   CCCCCCCCCCCC  LLL                  IIIIIIIII     UUU         UUU  TTTTTTTTTTTTTTT   LLL
   CCCCCCCCCCCC  LLL                  IIIIIIIII     UUU         UUU  TTTTTTTTTTTTTTT   LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
CCC              LLL                     III        UUU         UUU        TTT         LLL
   CCCCCCCCCCCC  LLLLLLLLLLLLLLL      IIIIIIIII     UUUUUUUUUUUUUUU        TTT         LLLLLLLLLLLLLLL
   CCCCCCCCCCCC  LLLLLLLLLLLLLLL      IIIIIIIII     UUUUUUUUUUUUUUU        TTT         LLLLLLLLLLLLLLL
   CCCCCCCCCCCC  LLLLLLLLLLLLLLL      IIIIIIIII     UUUUUUUUUUUUUUU        TTT         LLLLLLLLLLLLLLL
```

FILEID**SHODEVUTL





```
;     1          0001  0 MODULE shodevutl(IDENT = 'V04-000',
;     2          0002  0                    ADDRESSING_MODE (EXTERNAL = GENERAL)) =
;     3          0003  0
;     4          0004  1 BEGIN
;     5          0005  1
;     6          0006  1 !*****************************************************************************
;     7          0007  1 !*                                                                           *
;     8          0008  1 !*  COPYRIGHT (c) 1978, 1980, 1982, 1984 BY                                  *
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;    25          0025  1 !*                                                                           *
;    26          0026  1 !*                                                                           *
;    27          0027  1 !*****************************************************************************
;    28          0028  1
;    29          0029  1 !++
;    30          0030  1 !
;    31          0031  1 ! FACILITY:  SHOW utility
;    32          0032  1 !
;    33          0033  1 ! ABSTRACT:
;    34          0034  1 !       This module contains the routines for the SHOW DEVICES command.
;    35          0035  1 !
;    36          0036  1 ! ENVIRONMENT:
;    37          0037  1 !       VAX native, user and kernel mode
;    38          0038  1 !
;    39          0039  1 ! AUTHOR:  Gerry Smith           CREATION DATE:  28-Jul-1982
;    40          0040  1 !
;    41          0041  1 ! MODIFIED BY:
;    42          0042  1 !
;    43          0043  1 !       V03-018 CWH3018                 CW Hobbs                  24-Jul-1984
;    44          0044  1 !               Add orb flags, max block, and ACP extent info to items
;    45          0045  1 !               which are collected.
;    46          0046  1 !
;    47          0047  1 !       V03-017 LMP0221         L. Mark Pilant,         12-Apr-1984  15:01
;    48          0048  1 !               Change UCB$L_OWNUIC to ORB$L_OWNER and UCB$W_VPROT to
;    49          0049  1 !               ORB$W_PROT.
;    50          0050  1 !
;    51          0051  1 !       V03-016 CWH3016                 CW Hobbs                  12-Apr-1984
;    52          0052  1 !               Move test for /MOUNT and /ALLOC to SHODEVPRT, make the routine
;    53          0053  1 !               suspicious of the PID in the UCB.
;    54          0054  1 !
;    55          0055  1 !       V03-015 CWH3015                 CW Hobbs                  3-Mar-1984
;    56          0056  1 !               Fix dual-path logic so that when getting data the "ddb"
;    57          0057  1 !               parameter is always the primary ddb.  Also support allocation
```

```
;   58          0058  1 !               class device names for file-oriented devices and sorted
;   59          0059  1 !               device displays.
;   60          0060  1 !
;   61          0061  1 !       V03-014 CWH3014                 CW Hobbs                29-Feb-1984
;   62          0062  1 !               Remove reference to D_L_VOLLKID, used during trial builds but
;   63          0063  1 !               not needed after EXE$DVI_FREEBLOCKS is built into the system.
;   64          0064  1 !
;   65          0065  1 !       V03-013 CWH3013                 CW Hobbs                27-Feb-1984
;   66          0066  1 !               Collect more information for remote and dual-path devices.
;   67          0067  1 !               Fix linkages for calls to the exec, and add a handler to
;   68          0068  1 !               trap and dismiss kernel mode access violations.
;   69          0069  1 !
;   70          0070  1 !       V03-012 TCM0001                 Trudy C. Matthews       10-Oct-1983
;   71          0071  1 !               If there are two paths to the same device, find the name of
;   72          0072  1 !               the alternate path (i.e. the device's alias).
;   73          0073  1 !
;   74          0074  1 !       V03-011 GAS0178                 Gerry Smith             7-Sep-1983
;   75          0075  1 !               Fix quota caching for ODS2 disks.  The quota cache size was
;   76          0076  1 !               being taken from the wrong cell.
;   77          0077  1 !
;   78          0078  1 !       V03-010 GAS0167                 Gerry Smith             22-Aug-1983
;   79          0079  1 !               Fix the journal device name: get rid of the underscore that
;   80          0080  1 !               ioc$cvt_devnam returns, and make the device name into an
;   81          0081  1 !               ASCIC string.
;   82          0082  1 !
;   83          0083  1 !       V03-009 GAS0160                 Gerry Smith             27-Jul-1983
;   84          0084  1 !               Show template devices by default.
;   85          0085  1 !
;   86          0086  1 !       V03-008 GAS0149                 Gerry Smith             28-Jun-1983
;   87          0087  1 !               Use IOC$CVT_DEVNAM to obtain the device name.
;   88          0088  1 !
;   89          0089  1 !       V03-007 GAS0133                 Gerry Smith             14-May-1983
;   90          0090  1 !               Add retention period, default extend quantity, default file
;   91          0091  1 !               protection.
;   92          0092  1 !
;   93          0093  1 !       V03-006 GAS0114                 Gerry Smith             1-Apr-1983
;   94          0094  1 !               Modify the cluster_device logic so that less checking and
;   95          0095  1 !               testing is done in kernel mode.
;   96          0096  1 !
;   97          0097  1 !       V03-005 GAS0110                 Gerry Smith             28-Feb-1983
;   98          0098  1 !               Add support for cluster devices.
;   99          0099  1 !
;  100          0100  1 !       V03-004 GAS0107                 Gerry Smith             3-Feb-1983
;  101          0101  1 !               Add support for journals.
;  102          0102  1 !
;  103          0103  1 !       V03-003 GAS0106                 Gerry Smith             24-Jan-1983
;  104          0104  1 !               In the case of multivolume sets, check to make sure that
;  105          0105  1 !               the volume is mounted.  Also tighten up the bounds checking.
;  106          0106  1 !
;  107          0107  1 !       V03-002 GAS00104                Gerry Smith             17-Jan-1983
;  108          0108  1 !               Fix the logic path for /ALLOCATED and /MOUNTED
;  109          0109  1 !
;  110          0110  1 !       V03-001 GAS00101                Gerry Smith             13-Jan-1983
;  111          0111  1 !               Only check for an RVN if the device is file-oriented.
;  112          0112  1 !
;  113          0113  1 !--
```

```
;  115          0114  1
;  116          0115  1 !
;  117          0116  1 ! Include files
;  118          0117  1 !
;  119          0118  1
;  120          0119  1 LIBRARY  'SYS$LIBRARY:LIB';                ! VAX/VMS system definitions
;  121          0120  1 REQUIRE 'SRC$:SHOWDEF';                    ! SHOW common definitions
;  122          0219  1 REQUIRE 'SRC$:SHODEVDEF';                  ! SHOW DEVICES common definitions
;  123          0510  1 REQUIRE 'SHRLIB$:JNLDEFINT';               ! Journal definitions
;  124          1525  1
;  125          1526  1 !
;  126          1527  1 ! Define the linkage for the routines to lock and unlock the I/O database,
;  127          1528  1 ! scan the I/O database, and obtain the device name.
;  128          1529  1 !
;  129          1530  1 LINKAGE
;  130          1531  1     IOLOCK = JSB (REGISTER = 4)
;  131          1532  1                : NOPRESERVE(0,1,2,3,4,5) PRESERVE(6,7,8,9,10,11),
;  132          1533  1     CVTDEV = JSB (REGISTER = 0,                    ! Length of output buffer,
;  133          1534  1                   REGISTER = 1,                    ! Address of output buffer
;  134          1535  1                   REGISTER = 4,                    ! Format of device name
;  135          1536  1                   REGISTER = 5;                    ! Address of UCB
;  136          1537  1                   REGISTER = 1)                    ! Length of final name
;  137          1538  1                : PRESERVE(0,2,3,4,5,6,7,8,9,10,11),
;  138          1539  1     IOSCAN = JSB (REGISTER = 11,                   ! Call with DDB,
;  139          1540  1                   REGISTER = 10;                   ! UCB,
;  140          1541  1                   REGISTER = 11,                   ! Return with DDB,
;  141          1542  1                   REGISTER = 10)                   ! UCB
;  142          1543  1                : NOPRESERVE(0,10,11) PRESERVE(1,2,3,4,5,6,7,8,9);
;  143          1544  1
;  144          1545  1 !
;  145          1546  1 ! The following macro makes it easier to copy stuff to the scratch area.
;  146          1547  1 !
;  147        M 1548  1 MACRO copy_data (source, dest) [item] =
;  148          1549  1     dest[%NAME('d_', item)] = .source[%NAME(source, '$', item)]%;
;  149          1550  1
```

```
;  151          1551  1 FORWARD ROUTINE
;  152          1552  1         kernel_handler;         ! Turn kernel mode signals to returns
;  153          1553  1
;  154          1554  1 FORWARD ROUTINE
;  155          1555  1     io_scan,
;  156          1556  1     utl_get_data;
;  157          1557  1
;  158          1558  1 EXTERNAL ROUTINE
;  159          1559  1     show$write_line : NOVALUE,
;  160          1560  1     exe$dvi_freeblocks,
;  161          1561  1     sch$iolockr : IOLOCK,
;  162          1562  1     sch$iounlock : IOLOCK,
;  163          1563  1     ioc$cvt_devnam : CVTDEV,
;  164          1564  1     ioc$scan_iodb_2p : IOSCAN;
;  165          1565  1
;  166          1566  1 EXTERNAL
;  167          1567  1     scs$gq_config,
;  168          1568  1     scs$ga_localsb,
;  169          1569  1     sch$gl_maxpix,
;  170          1570  1     sch$gl_pcbvec : REF VECTOR,
;  171          1571  1     sch$gl_curpcb,
;  172          1572  1     ioc$gl_devlist;
;  173          1573  1
;  174          1574  1 GLOBAL
;  175          1575  1     kernel_accvio : VECTOR [4, LONG] ADDRESSING_MODE (GENERAL);
;  176          1576  1
```

```
;  178          1577  1 GLOBAL ROUTINE kernel_handler (sig : REF BLOCK[,BYTE], mech : REF BLOCK[,BYTE]) =
;  179          1578  2 BEGIN
;  180          1579  2 !++
;  181          1580  2 !
;  182          1581  2 ! FUNCTIONAL DESCRIPTION:
;  183          1582  2 !
;  184          1583  2 !       This routine intercepts kernel mode signals.
;  185          1584  2 !
;  186          1585  2 !  INPUTS:
;  187          1586  2 !
;  188          1587  2 !       sig  - signal argument list
;  189          1588  2 !       mech - mechanism argument list
;  190          1589  2 !
;  191          1590  2 ! SIDE EFFECTS:
;  192          1591  2 !
;  193          1592  2 !       A return is made to user mode code.
;  194          1593  2 !--
;  195          1594  2
;  196          1595  2 EXTERNAL ROUTINE
;  197          1596  2     LIB$SIG_TO_RET : ADDRESSING_MODE (GENERAL);
;  198          1597  2
;  199          1598  2 ! If the signal name is an accvio, then clean up
;  200          1599  2
;  201          1600  2 IF .sig [chf$l_sig_name] EQL ss$_accvio          ! Is it an accvio?
;  202          1601  2 THEN
;  203          1602  3     BEGIN
;  204          1603  3     SCH$IOUNLOCK(.sch$gl_curpcb);                ! Unlock I/O database
;  205          1604  3     SET_IPL(0);                                  ! Lower IPL
;  206          1605  3     CH$MOVE (4*4, sig[chf$l_sig_arg1], kernel_accvio[0]);
;  207          1606  3     RETURN LIB$SIG_TO_RET (.sig, .mech);         ! Convert signal to return
;  208          1607  2     END;
;  209          1608  2
;  210          1609  2 RETURN ss$_resignal;
;  211          1610  1 END;


                                        .TITLE  SHODEVUTL
                                        .IDENT  \V04-000\

                                        .PSECT  $GLOBAL$,NOEXE,2

                            00000 KERNEL_ACCVIO::
                                        .BLKB   16

                                        .EXTRN  SHOW$WRITE_LINE
                                        .EXTRN  EXE$DVI_FREEBLOCKS
                                        .EXTRN  SCH$IOLOCKR, SCH$IOUNLOCK
                                        .EXTRN  IOC$CVT_DEVNAM, IOC$SCAN_IODB_2P
                                        .EXTRN  SCS$GQ_CONFIG, SCS$GA_LOCALSB
                                        .EXTRN  SCH$GL_MAXPIX, SCH$GL_PCBVEC
                                        .EXTRN  SCH$GL_CURPCB, IOC$GL_DEVLIST
                                        .EXTRN  LIB$SIG_TO_RET

                                        .PSECT  $CODE$,NOWRT,2

                    0FFC 00000          .ENTRY  KERNEL_HANDLER, Save R2,R3,R4,R5,R6,R7,R8,- ; 1577
                                                R9,R10,R11                                  :
```

```
                                 56        04     AC  D0 00002          MOVL    SIG, R6                                 ; 1600
                                 0C        04     A6  D1 00006          CMPL    4(R6), #12                              ;
                                                  26  12 0000A          BNEQ    1$                                      ;
                                 54 00000000G      00  D0 0000C          MOVL    SCH$GL_CURPCB, R4                       ; 1603
                                    00000000G      00  16 00013          JSB     SCH$IODNLOCK                            ;
                                 12                00  DA 00019          MTPR    #0, #18                                 ; 1604
                00000000'  00        08     A6                10  28 0001C  MOVC3   #16, 8(R6), KERNEL_ACCVIO              ; 1605
                                                       08     AC  DD 00025  PUSHL   MECH                                    ; 1606
                                                              56  DD 00028  PUSHL   R6                                     ;
                           00000000G  00                      02  FB 0002A  CALLS   #2, LIB$SIG_TO_RET                    ;
                                                              04  00031    RET                                            ;
                                 50      0918     8F          3C 00032 1$:  MOVZWL  #2328, R0                             ; 1609
                                                              04  00037    RET                                            ; 1610

; Routine Size:  56 bytes,    Routine Base:  $CODE$ + 0000
```

```
;  213          1611  1 GLOBAL ROUTINE io_scan (node, device, unit, flags, data) =
;  214          1612  2 BEGIN
;  215          1613  2
;  216          1614  2 !---
;  217          1615  2 !
;  218          1616  2 ! This routine is called in KERNEL mode to scan the device data base and
;  219          1617  2 ! determine which devices to collect information about.  Once a likely
;  220          1618  2 ! candidate for data collection is determined, control is passed to
;  221          1619  2 ! another routine, UTL_GET_DATA, where, based on the type of device and
;  222          1620  2 ! the qualifiers selected, device-specific data is stuffed into the scratch
;  223          1621  2 ! area.  This continues until either the end of the device database is
;  224          1622  2 ! reached, or an error status (STATUS low bit clear) is obtained.  Typical
;  225          1623  2 ! reasons for an error status are running out of scratch area, or having
;  226          1624  2 ! obtained all the data that is required of the caller.
;  227          1625  2 !
;  228          1626  2 ! Inputs
;  229          1627  2 !       NODE      - address of ASCIC of node part of device name, or allocation
;  230          1628  2 !                   class if FLAGS[DEVI$V_ALLOCLS]
;  231          1629  2 !       DEVICE    - address of ASCIC of device part of device name
;  232          1630  2 !       UNIT      - address of unit number. (-1 => no unit number)
;  233          1631  2 !       FLAGS     - address of options longword
;  234          1632  2 !       DATA      - address of scratch area.
;  235          1633  2 !
;  236          1634  2 ! Outputs
;  237          1635  2 !       DATA    - is full of all sorts of useful data about devices
;  238          1636  2 !
;  239          1637  2 !---
;  240          1638  2
;  241          1639  2 MAP
;  242          1640  2     data : REF VECTOR,
;  243          1641  2     node : REF VECTOR[,BYTE],
;  244          1642  2     device : REF VECTOR[,BYTE],
;  245          1643  2     flags : REF $BBLOCK;
;  246          1644  2
;  247          1645  2 LOCAL
;  248          1646  2     status,
;  249          1647  2     limit,
;  250          1648  2     ptr : REF VECTOR[,BYTE],                    ! Data area pointer
;  251          1649  2     scratch : REF $BBLOCK,                      ! Scratch pointer
;  252          1650  2     ucb : REF $BBLOCK,                          ! UCB pointer
;  253          1651  2     ddb : REF $BBLOCK,                          ! DDB pointer
;  254          1652  2     sb : REF $BBLOCK;                           ! System block pointer
;  255          1653  2
;  256          1654  2 !
;  257          1655  2 ! Trap anything weird, and turn it into a return
;  258          1656  2 !
;  259          1657  2 ENABLE
;  260          1658  2     kernel_handler;
;  261          1659  2
;  262          1660  2 !
;  263          1661  2 ! Set up the scratch area so that is can be addressed easily.  Also, calculate
;  264          1662  2 ! a limit toward the end of the scratch area, so that we don't write beyond the
;  265          1663  2 ! area.
;  266          1664  2 !
;  267          1665  2 scratch = data[1];                             ! Point to beginning of scratch area
;  268          1666  2 limit = .data[0] + data[0] - d_k_length;       ! Set the limit
;  269          1667  2
```

```
;  270          1668  2 !
;  271          1669  2 ! Lock the I/O data base.  Upon return from the call to SCH$IOLOCKR, the
;  272          1670  2 ! IPL will be 2, so that pagefaults are still allowed.
;  273          1671  2 !
;  274          1672  2 SCH$IOLOCKR(.sch$gl_curpcb);                    ! Lock the I/O database
;  275          1673  2
;  276          1674  2 !
;  277          1675  2 ! Start at the beginning of the I/O database and initiate the I/O scan.
;  278          1676  2 !
;  279          1677  2 status = IOC$SCAN_IODB_2P(0, 0; ddb, ucb);
;  280          1678  2
;  281          1679  2 !
;  282          1680  2 ! For each UCB in the I/O database, determine if it might contain devices of
;  283          1681  2 ! interest.  If so, then call the data-gathering dispatch routine.  Upon
;  284          1682  2 ! return from the data-gathering, STATUS must be checked, to see if any
;  285          1683  2 ! further scan is necessary.  If not, then exit the DDB/UCB loops.
;  286          1684  2 !
;  287          1685  2 WHILE .status DO                                 ! As long as the scan returns
;  288          1686  3     BEGIN                                       ! a success, stay in the loop.
;  289          1687  3     IF                                          ! For each device found, make
;  290          1688  4         BEGIN                                   ! some checks.
;  291          1689  4         IF .flags[devi$v_allocls]               ! If an allocation class is desired
;  292          1690  4         THEN
;  293          1691  5             BEGIN
;  294          1692  6             IF .ddb[ddb$l_allocls] EQL .(node[0])! If the allocation class matches
;  295          1693  5             THEN true                           ! then the device is ok, otherwise
;  296          1694  5             ELSE ucb = 0                        ! go to the next DDB.
;  297          1695  5             END
;  298          1696  4         ELSE
;  299          1697  5             BEGIN
;  300          1698  5             IF .node[0] EQL 0                   ! If no node specified, then
;  301          1699  5             THEN true                           ! continue.
;  302          1700  5             ELSE                                ! Otherwise check to see if
;  303          1701  6                 BEGIN                           ! this node is one we want.
;  304          1702  6                 IF (sb = .ddb[ddb$l_sb]) EQL 0  ! If no node, go to
;  305          1703  6                 THEN ucb = 0                    ! next DDB.
;  306          1704  6                 ELSE
;  307          1705  7                     BEGIN
;  308          1706  7                     IF CH$EQL(.node[0], node[1],
;  309          1707  7                               .(sb[sb$t_nodename])<0,8>, sb[sb$t_nodename] + 1)
;  310          1708  7                     THEN true                   ! If nodenames match, good
;  311          1709  7                     ELSE ucb = 0                ! Else get next DDB
;  312          1710  7                     END
;  313          1711  6                 END
;  314          1712  5             END
;  315          1713  4         END
;  316          1714  3     AND
;  317          1715  4         BEGIN
;  318          1716  4         IF .device[0] EQL 0                     ! If no device specified, then
;  319          1717  4         THEN
;  320          1718  5             BEGIN                                       ! Don't display mailbox
;  321          1719  5             IF .$BBLOCK[ucb[ucb$l_devchar], dev$v_mbx]  ! UCB's, and get to
;  322          1720  5             THEN ucb = 0                                ! next DDB
;  323          1721  5             ELSE true
;  324          1722  5             END
;  325          1723  4         ELSE                                            ! If a device was
;  326          1724  5             BEGIN                                       ! specified, check for
```

```
;  327     1725  5              IF CH$EQL(.device[0], device[1],                    ! a match.
;  328     1726  5                        .device[0], ddb[ddb$t_name] + 1)
;  329     1727  5              THEN true                                           ! If a match, good
;  330     1728  5              ELSE ucb = 0                                        ! Otherwise, go to
;  331     1729  5              END                                                 ! next DDB
;  332     1730  4          END
;  333     1731  3      AND
;  334     1732  4          BEGIN                                                   ! If a unit specified,
;  335     1733  4          IF .unit NEQ -1                                         ! check for a match
;  336     1734  5          THEN (.unit EQL .ucb[ucb$w_unit])
;  337     1735  4          ELSE true                                               ! If no unit, ok
;  338     1736  4          END
;  339     1737  3      THEN
;  340     1738  4          BEGIN
;  341     1739  4          IF .scratch GEQA .limit          ! Before getting data, check
;  342     1740  4          THEN                             ! that there is room.
;  343     1741  5              BEGIN                        ! If no room, set status to
;  344     1742  5              status = SS$_VASFULL;        ! appropriate error
;  345     1743  5              EXITLOOP                     ! and get out.
;  346     1744  4              END;
;  347     1745  4
;  348     1746  4  !
;  349     1747  4  ! Determine how much data to get.  If no complete device was specified,
;  350     1748  4  ! return just information about this device.  However, if a complete device
;  351     1749  4  ! was specified, check to see if this is perhaps a multi-volume set.  If so,
;  352     1750  4  ! then return data about the entire set.
;  353     1751  4  !
;  354     1752  4  ! So, if no explicit device was given, or if the device is not file-oriented,
;  355     1753  4  ! or there's no VCB, or there is no Relative Volume Table, then
;  356     1754  4  ! collect data on one device.  Otherwise, rip thru the UCB list associated
;  357     1755  4  ! with the RVT, and get data about each device in the set.
;  358     1756  4  !
;  359     1757  4          IF .unit EQL -1                                         ! If not explicit
;  360     1758  4          OR NOT .$BBLOCK[ucb[ucb$l_devchar], dev$v_fod]          ! or not Files-11
;  361     1759  4          OR
;  362     1760  5              BEGIN
;  363     1761  5              BIND vcb = ucb[ucb$l_vcb] : REF $BBLOCK;
;  364     1762  5              IF .vcb EQL 0                                       ! or no VCB
;  365     1763  5              THEN true
;  366     1764  5              ELSE
;  367     1765  6                  BEGIN
;  368     1766  6                  IF .vcb[vcb$w_rvn] EQL 0                        ! or not an RVN
;  369     1767  6                  THEN true                                       ! then do one
;  370     1768  6                  ELSE false
;  371     1769  6                  END
;  372     1770  5              END
;  373     1771  4          THEN
;  374     1772  5              BEGIN
;  375     1773  5              status = utl_get_data(.ucb, .ddb, .flags, .scratch, .data);
;  376     1774  5                                                                  ! Get device data
;  377     1775  5              IF .status                                          ! If we got data,
;  378     1776  5              THEN                                                ! update the pointer
;  379     1777  6                  BEGIN
;  380     1778  6                  IF .scratch[d_b_devclass] EQLU dc$_journal
;  381     1779  6                  THEN scratch = .scratch+d_k_length;             ! Skip an extra
;  382     1780  6                  scratch = .scratch + d_k_length;                ! block if journal
;  383     1781  6                  END
```

```
;  384          1782  5              ELSE status = 1;                                ! The only time FALSE
;  385          1783  5                                                              ! is returned is if
;  386          1784  5                                                              ! /MOUNTED or /ALLOCATED
;  387          1785  5                                                              ! was specified and
;  388          1786  5                                                              ! the device wasn't either
;  389          1787  5              IF .unit NEQ -1                                 ! If explicit device
;  390          1788  5              AND .status                                     ! (don't mask error)
;  391          1789  5              THEN ucb = 0;                                   ! then we're done with
;  392          1790  5              END                                             ! this DDB.
;  393          1791  4          ELSE
;  394          1792  5              BEGIN
;  395          1793  5              LOCAL
;  396          1794  5                  vcb : REF $BBLOCK,
;  397          1795  5                  rvt : REF $BBLOCK,
;  398          1796  5                  ucblist : REF VECTOR;
;  399          1797  5
;  400          1798  5              vcb = .ucb[ucb$l_vcb];
;  401          1799  5              rvt = .vcb[vcb$l_rvt];
;  402          1800  5              ucblist = rvt[rvt$l_ucblst];
;  403          1801  5
;  404          1802  5              INCR index FROM 0 TO .rvt[rvt$b_nvols] - 1 DO
;  405          1803  6                  BEGIN
;  406          1804  6                  IF .scratch GEQA .limit                     ! Check limit
;  407          1805  7                  THEN (status = SS$_VASFULL; EXITLOOP)
;  408          1806  6                  ELSE IF .ucblist[.index] NEQ 0              ! If volume mounted,
;  409          1807  6                  THEN                                        ! get data
;  410          1808  7                      BEGIN
;  411          1809  7                      status = utl_get_data(.ucblist[.index], .ddb, .flags, .scratch, .data);
;  412          1810  7                      IF .status
;  413          1811  7                      THEN
;  414          1812  8                          BEGIN
;  415          1813  8                          IF .scratch[d_b_devclass] EQLU dc$_journal
;  416          1814  8                          THEN scratch = .scratch + d_k_length;
;  417          1815  8                          scratch = .scratch + d_k_length;
;  418          1816  7                          END;
;  419          1817  6                      END;
;  420          1818  5                  END;
;  421          1819  5              status = 0;                                     ! To indicate finished with
;  422          1820  4              END;                                            ! this volume set
;  423          1821  4          IF NOT .status THEN EXITLOOP;                       ! Go away?
;  424          1822  3          END;
;  425          1823  3      status = IOC$SCAN_IODB_2P(.ddb, .ucb; ddb, ucb);
;  426          1824  2      END;
;  427          1825  2
;  428          1826  2 scratch[d_t_device] = 0;                                    ! To show end of list
;  429          1827  2
;  430          1828  2 !
;  431          1829  2 ! Now to clean up.  Unlock the I/O database, then lower the IPL
;  432          1830  2 ! to zero.
;  433          1831  2 !
;  434          1832  2 SCH$IOUNLOCK(.sch$gl_curpcb);                               ! Unlock I/O database
;  435          1833  2 SET_IPL(0);                                                  ! Lower IPL
;  436          1834  2
;  437          1835  2 IF .scratch EQLA data[1]                                    ! If no data,
;  438          1836  2 THEN status = SS$_NOSUCHDEV                                ! return an error
;  439          1837  2 ELSE status = true;
;  440          1838  2
```

```
;  441        1839  2 RETURN .status;                        ! Return with status
;  442        1840  1 END;



                                         0FFC 00000          .ENTRY   IO_SCAN, Save R2,R3,R4,R5,R6,R7,R8,R9,R10,- ; 1611
                                                                      R11                                         ;
                           5E            08  C2 00002          SUBL2    #8, SP                                    ;
                           6D     018A   CF  DE 00005          MOVAL    27$, (FP)                                 ; 1612
           04    AE    14  AC            04  C1 0000A          ADDL3    #4, DATA, 4(SP)                           ; 1665
                           57       04   AE  D0 00010          MOVL     4(SP), SCRATCH                            ;
                 56    14  BC       14   AC  C1 00014          ADDL3    DATA, @DATA, R6                           ; 1666
                           56     FEF9   C6  9E 0001A          MOVAB    -263(R6), LIMIT                           ;
                           54 00000000G  00  D0 0001F          MOVL     SCH$GL_CURPCB, R4                         ; 1672
                              00000000G  00  16 00026          JSB      SCH$IOLOCKR                               ;
                                         5A  7C 0002C          CLRQ     R10                                       ; 1677
                              00000000G  00  16 0002E          JSB      IOC$SCAN_IODB_2P                          ;
                           58            50  D0 00034          MOVL     R0, STATUS                                ;
                           6E       10   AC  D0 00037          MOVL     FLAGS, (SP)                               ; 1689
                           55       04   AC  D0 0003B          MOVL     NODE, R5                                  ; 1692
                           6C            58  E9 0003F 1$:      BLBC     STATUS, 10$                               ; 1685
                 59        6E            01  C1 00042          ADDL3    #1, (SP), R9                              ; 1689
                 08        69            04  E1 00046          BBC      #4, (R9), 3$                              ;
                           65       3C   AB  D1 0004A          CMPL     60(DDB), (R5)                             ; 1692
                                         1D  13 0004E 2$:      BEQL     4$                                        ;
                                         3A  11 00050          BRB      6$                                        ; 1694
                                         65  95 00052 3$:      TSTB     (R5)                                      ; 1698
                                         17  13 00054          BEQL     4$                                        ;
                           54       34   AB  D0 00056          MOVL     52(DDB), SB                               ; 1702
                                         30  13 0005A          BEQL     6$                                        ;
                           51            65  9A 0005C          MOVZBL   (R5), R1                                  ; 1706
                           50       44   A4  9A 0005F          MOVZBL   68(SB), R0                                ; 1707
     50          00    01  A5            51  2D 00063          CMPC5    R1, 1(R5), #0, R0, 69(SB)                 ; 1706
                                    45   A4     00069                                                             ;
                                         E1  11 0006B          BRB      2$                                        ;
                           50       08   AC  D0 0006D 4$:      MOVL     DEVICE, R0                                ; 1716
                                         60  95 00071          TSTB     (R0)                                      ;
                                         07  12 00073          BNEQ     5$                                        ;
                 17    3A  AA            04  E1 00075          BBC      #4, 58(UCB), 8$                           ; 1719
                                         10  11 0007A          BRB      6$                                        ; 1720
                           52            60  9A 0007C 5$:      MOVZBL   (R0), R2                                  ; 1725
                           51            60  9A 0007F          MOVZBL   (R0), R1                                  ; 1726
     51          00    01  A0            52  2D 00082          CMPC5    R2, 1(R0), #0, R1, 21(DDB)                ; 1725
                                    15   AB     00088                                                             ;
                                         05  13 0008A          BEQL     8$                                        ;
                                         5A  D4 0008C 6$:      CLRL     UCB                                       ; 1728
                                       00CF  31 0008E 7$:      BRW      23$                                       ;
                     FFFFFFFF   8F  0C   AC  D1 00091 8$:      CMPL     UNIT, #-1                                 ; 1733
                                         09  13 00099          BEQL     9$                                        ;
   0C    AC      54    AA       10       00  ED 0009B          CMPZV    #0, #16, 84(UCB), UNIT                    ; 1734
                                         EA  12 000A2          BNEQ     7$                                        ;
                           56            57  D1 000A4 9$:      CMPL     SCRATCH, LIMIT                            ; 1739
                                         08  1F 000A7          BLSSU    11$                                       ;
                           58     0244   8F  3C 000A9          MOVZWL   #580, STATUS                              ; 1742
                                       00BB  31 000AE 10$:     BRW      24$                                       ; 1741
```

```
                FFFFFFFF  8F      0C  AC  D1 000B1 11$:   CMPL    UNIT, #-1                       ; 1757
                                      10  13 000B9        BEQL    12$                             ;
          0B        39  AA              06  E1 000BB        BBC     #6, 57(UCB), 12$                ; 1758
                        50        34    AA  D0 000C0        MOVL    52(UCB), R0                     ; 1762
                                      05  13 000C4        BEQL    12$                             ;
                                  0E  A0  B5 000C6        TSTW    14(R0)                          ; 1766
                                      3D  12 000C9        BNEQ    16$                             ;
                                  14  AC  DD 000CB 12$:    PUSHL   DATA                            ; 1773
                                      57  DD 000CE        PUSHL   SCRATCH                         ;
                                  10  AC  DD 000D0        PUSHL   FLAGS                           ;
                            7E            5A  7D 000D3        MOVQ    UCB, -(SP)                      ;
                      0000V CF            05  FB 000D6        CALLS   #5, UTL_GET_DATA                ;
                            58            50  D0 000DB        MOVL    R0, STATUS                      ;
                            13            58  E9 000DE        BLBC    STATUS, 14$                     ; 1775
                          A1  8F      78  A7  91 000E1        CMPB    120(SCRATCH), #161               ; 1778
                                      05  12 000E6        BNEQ    13$                             ;
                            57    0107  C7  9E 000E8        MOVAB   263(R7), SCRATCH                ; 1779
                            57    0107  C7  9E 000ED 13$:    MOVAB   263(R7), SCRATCH                ; 1780
                                      03  11 000F2        BRB     15$                             ; 1775
                            58            01  D0 000F4 14$:    MOVL    #1, STATUS                      ; 1782
                FFFFFFFF  8F      0C  AC  D1 000F7 15$:    CMPL    UNIT, #-1                       ; 1787
                                      5C  13 000FF        BEQL    22$                             ;
                            68            58  E9 00101        BLBC    STATUS, 24$                     ; 1788
                                      5A  D4 00104        CLRL    UCB                             ; 1789
                                      55  11 00106        BRB     22$                             ; 1757
                            50        34  AA  D0 00108 16$:    MOVL    52(UCB), VCB                    ; 1798
                            50        20  A0  D0 0010C        MOVL    32(VCB), RVT                    ; 1799
                            52        44  A0  9E 00110        MOVAB   68(R0), UCBLIST                 ; 1800
                            59        0B  A0  9A 00114        MOVZBL  11(RVT), R9                     ; 1802
                            53            01  CE 00118        MNEGL   #1, INDEX                       ;
                                      3A  11 0011B        BRB     20$                             ;
                            56            57  D1 0011D 17$:    CMPL    SCRATCH, LIMIT                  ; 1804
                                      07  1F 00120        BLSSU   18$                             ;
                            58      0244  8F  3C 00122        MOVZWL  #580, STATUS                    ; 1805
                                      32  11 00127        BRB     21$                             ;
                                  6243  D5 00129 18$:    TSTL    (UCBLIST)[INDEX]                ; 1806
                                      29  13 0012C        BEQL    20$                             ;
                                  14  AC  DD 0012E        PUSHL   DATA                            ; 1809
                                      57  DD 00131        PUSHL   SCRATCH                         ;
                                  10  AC  DD 00133        PUSHL   FLAGS                           ;
                                      5B  DD 00136        PUSHL   DDB                             ;
                                  6243  DD 00138        PUSHL   (UCBLIST)[INDEX]                ;
                      0000V CF            05  FB 0013B        CALLS   #5, UTL_GET_DATA                ;
                            58            50  D0 00140        MOVL    R0, STATUS                      ;
                            11            58  E9 00143        BLBC    STATUS, 20$                     ; 1810
                          A1  8F      78  A7  91 00146        CMPB    120(SCRATCH), #161               ; 1813
                                      05  12 0014B        BNEQ    19$                             ;
                            57    0107  C7  9E 0014D        MOVAB   263(R7), SCRATCH                ; 1814
                            57    0107  C7  9E 00152 19$:    MOVAB   263(R7), SCRATCH                ; 1815
          C2                53            59  F2 00157 20$:    AOBLSS  R9, INDEX, 17$                  ; 1802
                                      58  D4 0015B 21$:    CLRL    STATUS                          ; 1819
                            0C            58  E9 0015D 22$:    BLBC    STATUS, 24$                     ; 1821
                          00000000G       00  16 00160 23$:    JSB     IOC$SCAN_IODB_2P                ; 1823
                            58            50  D0 00166        MOVL    R0, STATUS                      ;
                                      FED3  31 00169        BRW     1$                              ; 1685
                                  08  A7  94 0016C 24$:    CLRB    8(SCRATCH)                      ; 1826
                            54 0C0000000G   00  D0 0016F        MOVL    SCH$GL_CURPCB, R4               ; 1832
```

```
                    00000000G  00  16 00176          JSB     SCH$IOUNLOCK                                ;
                12             00  DA 0017C          MTPR    #0, #18                                     ; 1833
             04 AE             57  D1 0017F          CMPL    SCRATCH, 4(SP)                              ; 1835
                               07  12 00183          BNEQ    25$                                         ;
                58      0908   8F  3C 00185          MOVZWL  #2312, STATUS                               ; 1836
                               03  11 0018A          BRB     26$                                         ;
                58             01  D0 0018C 25$:     MOVL    #1, STATUS                                  ; 1837
                50             58  D0 0018F 26$:     MOVL    STATUS, R0                                  ; 1839
                               04 00192             RET                                                 ; 1840
                             0000 00193 27$:        .WORD   Save nothing                                ; 1612
                               7E  D4 00195          CLRL    -(SP)                                       ;
                               5E  DD 00197          PUSHL   SP                                          ;
                7E       04    AC  7D 00199          MOVQ    4(AP), -(SP)                                ;
             FE26 CF           03  FB 0019D          CALLS   #3, KERNEL_HANDLER                          ;
                               04 001A2             RET                                                 ;

; Routine Size:  419 bytes,    Routine Base:  $CODE$ + 0038
```

```
;  444          1841  1 GLOBAL ROUTINE utl_get_data (in_ucb, in_ddb, flags, scratch, data) =
;  445          1842  2 BEGIN
;  446          1843  2
;  447          1844  2 !---
;  448          1845  2 !
;  449          1846  2 ! This routine executes in KERNEL mode, and is called by IO_SCAN to dispatch
;  450          1847  2 ! to specific data-gathering routines, based on the qualifiers and the type of
;  451          1848  2 ! device.
;  452          1849  2 !
;  453          1850  2 ! Inputs
;  454          1851  2 !       IN_UCB        - address of the UCB of the device of interest
;  455          1852  2 !       IN_DDB        - address of the DDB whose UCB chain we are following
;  456          1853  2 !       FLAGS         - pointer to flags longword
;  457          1854  2 !       SCRATCH       - location of scratch area where data can be stored
;  458          1855  2 !       DATA          - pointer to start of scratch area
;  459          1856  2 !
;  460          1857  2 ! Outputs
;  461          1858  2 !       SCRATCH - has data possibly stored into it.  Also, the value of
;  462          1859  2 !                 SCRATCH will have changed, to show the next place where
;  463          1860  2 !                 data can be stored.
;  464          1861  2 !
;  465          1862  2 !---
;  466          1863  2
;  467          1864  2 MAP
;  468          1865  2     data : REF VECTOR,
;  469          1866  2     scratch : REF $BBLOCK,
;  470          1867  2     flags : REF $BBLOCK;
;  471          1868  2
;  472          1869  2 LOCAL
;  473          1870  2     status,
;  474          1871  2     aqb : REF $BBLOCK,
;  475          1872  2     ddb : REF $BBLOCK,
;  476          1873  2     scr : REF $BPLOCK,
;  477          1874  2     ucb : REF $BBLOCK,
;  478          1875  2     orb : REF $BBLOCK,
;  479          1876  2     vcb : REF $BBLOCK;
;  480          1877  2
;  481          1878  2 !
;  482          1879  2 ! Move the input parameters to the local pointers.  Check if the ucb is marked as the class driver
;  483          1880  2 ! copy, used for dual-pathed massbus disks.  If so, substitute the primary UCB and DDB for the
;  484          1881  2 ! input parameters.
;  485          1882  2 !
;  486          1883  2 ucb = .in_ucb;
;  487          1884  2 IF .$BBLOCK[ucb[ucb$l_devchar2], dev$v_cdp]     ! Is it the class driver path?
;  488          1885  2 THEN ucb = .ucb[ucb$l_2p_altucb];              ! Get the "real" ucb address
;  489          1886  2 orb = .ucb[ucb$l_orb];                        ! Save a pointer to the object's rights block
;  490          1887  2 ddb = .ucb[ucb$l_ddb];                        ! Always use the ddb hanging from the ucb we are actually us
;  491          1888  2 vcb = .ucb[ucb$l_vcb];                        ! Save a pointer to the volume control block
;  492          1889  2
;  493          1890  2 !
;  494          1891  2 ! Collect data about this device.  Initialize the SHOW DEVICE
;  495          1892  2 ! control areas in the scratch cell.
;  496          1893  2 !
;  497          1894  2 scratch[d_w_bits] = 0;                        ! Clear all the bits
;  498          1895  2 scratch[d_l_ucb]  = .ucb;                     ! Save the ucb address
;  499          1896  2
;  500          1897  2 !
```

```
;  501          1898  2 ! First, determine if an alternate path to the device exists.  If so,
;  502          1899  2 ! next check that the UCB for the device is not already in the scratch
;  503          1900  2 ! area.  If it is, return without saving this device.  If not, get the
;  504          1901  2 ! secondary host information
;  505          1902  2 !
;  506          1903  2 IF .$BBLOCK[ucb[ucb$l_devchar2], dev$v_2p]      ! If device is dual-pathed
;  507          1904  2 THEN
;  508          1905  3     BEGIN
;  509          1906  3     REGISTER
;  510          1907  3         l,
;  511          1908  3         scr : REF $BBLOCK;
;  512          1909  3     scr = data[1];                               ! Start at the front of the data
;  513          1910  3     WHILE .scr LSSA .scratch                     ! Look up to the current pointer
;  514          1911  3     DO
;  515          1912  4         BEGIN
;  516          1913  4         IF .scr[d_l_ucb] EQLA .ucb               ! The UCB is already there,
;  517          1914  4         THEN RETURN false;                       ! so we can simply exit now.
;  518          1915  4         IF .scr[d_b_devclass] EQLU dc$_journal    ! If the device is a journal
;  519          1916  4         THEN scr = .scr + d_k_length;            ! skip over the journal's device
;  520          1917  4         scr = .scr + d_k_length;                 ! Skip to the next device
;  521          1918  3         END;
;  522          1919  3     !
;  523          1920  3     ! First time we've seen this UCB, start stashing some info away.
;  524          1921  3     !
;  525          1922  3     scr = .ucb[ucb$l_2p_ddb];                    ! Get the ddb for the second path
;  526          1923  3     scr = .scr[ddb$l_sb];                        ! Get the sb for the second host
;  527          1924  3     !
;  528          1925  3     ! Copy the node name and length
;  529          1926  3     !
;  530          1927  3     CH$MOVE (sb$s_nodename, scr[sb$t_nodename], scratch[d_t_host2_name]);
;  531          1928  3     !
;  532          1929  3     ! Copy the node type, a blank-padded string sitting in a long-word
;  533          1930  3     !
;  534          1931  3     scratch[d_l_host2_type] = .scr[sb$t_hwtype];
;  535          1932  3     !
;  536          1933  3     ! Tell if the host is available, i.e. if an SCS connection exists
;  537          1934  3     !
;  538          1935  4     scratch[d_v_host2_avail] = (IF .$BBLOCK[ucb[ucb$l_devchar2], dev$v_mscp]
;  539          1936  4                                 THEN
;  540          1937  5                                     BEGIN
;  541          1938  5                                     scr = .ucb[ucb$l_2p_cddb];  ! Move the pointer to the CDDB for the devic
;  542          1939  6                                     (NOT .scr[cddb$v_noconn])
;  543          1940  5                                     END
;  544          1941  3                                 ELSE 0);
;  545          1942  2     END;                                         ! of code for dual-pathed devices
;  546          1943  2
;  547          1944  2 !
;  548          1945  2 ! Save host info for the primary host.  We don't need to save the nodename, since that will be
;  549          1946  2 ! part of the device name we return.
;  550          1947  2 !
;  551          1948  2 scr = .ddb[ddb$l_sb];                           ! Get the sb for the host
;  552          1949  2 scratch[d_v_remote_device] = (.scr NEQ scs$ga_localsb);
;  553          1950  2 CH$MOVE (sb$s_nodename, scr[sb$t_nodename], scratch[d_t_host_name]);
;  554          1951  2 scratch[d_l_host_type] = .scr[sb$t_hwtype];      ! Copy the node type, a blank-padded string
;  555          1952  2 scratch[d_v_host_avail] =  1;                   ! Assume that a connection exists (local node alway true)
;  556          1953  2
;  557          1954  2 !
```

```
;  558          1955  2 ! Check out some things only valid for MSCP devices
;  559          1956  2 !
;  560          1957  2 IF .$BBLOCK[ucb[ucb$l_devchar2], dev$v_mscp]
;  561          1958  2 THEN
;  562          1959  3     BEGIN
;  563          1960  3     scratch[d_v_shadow_master] = (.ucb[ucb$w_mscpunit] LSS 0);  ! Shadow masters have negative unit #s
;  564          1961  3     scr = .ucb[ucb$l_cddb];                                    ! Move the pointer to the CDDB for the devic
;  565          1962  3     scratch[d_v_host_avail] = (NOT .scr[cddb$v_noconn]);       ! Does a connection really exist?
;  566          1963  2     END;
;  567          1964  2
;  568          1965  2 !
;  569          1966  2 ! Now get the device name.
;  570          1967  2 !
;  571          1968  2 ioc$cvt_devnam(20,                                  ! Get device name, max this long
;  572          1969  2                scratch[d_t_device],                 ! put it here,
;  573          1970  3                (IF .$BBLOCK[ucb[ucb$l_devchar], dev$v_fod] ! If file-oriented
;  574          1971  3                 THEN 0                             ! then try for "$n$ddcu" format
;  575          1972  2                 ELSE -1),                          ! else select "node$ddcu" display format
;  576          1973  2                .ucb;                               ! UCB is here
;  577          1974  2                scratch[d_b_devlen]);               ! final length here
;  578          1975  2
;  579          1976  2 !
;  580          1977  2 ! Copy standard cells from the UCB to the scratch area
;  581          1978  2 !
;  582        P 1979  2 copy_data (ucb, scratch, l_pid,                     ! Copy all the necessary
;  583        P 1980  2                          l_devchar,                 ! information from the UCB.
;  584        P 1981  2                          l_devchar2,
;  585        P 1982  2                          b_devclass,
;  586        P 1983  2                          b_devtype,
;  587        P 1984  2                          w_unit,
;  588        P 1985  2                          w_devbufsiz,
;  589        P 1986  2                          l_devdepend,
;  590        P 1987  2                          l_devdepnd2,
;  591        P 1988  2                          w_refc,
;  592        P 1989  2                          l_sts,
;  593        P 1990  2                          w_devsts,
;  594        P 1991  2                          l_opcnt,
;  595          1992  2                          w_errcnt);
;  596          1993  2
;  597          1994  2 !
;  598          1995  2 ! Copy ORB information to the scratch area
;  599          1996  2 !
;  600          1997  2 IF .orb[orb$v_prot_16]
;  601          1998  2 THEN scratch[d_w_vprot] = .orb[orb$w_prot]
;  602          1999  2 ELSE
;  603          2000  3     BEGIN
;  604          2001  3     (scratch[d_w_vprot])<0,4> = .(orb[orb$l_sys_prot])<0,4>;
;  605          2002  3     (scratch[d_w_vprot])<4,4> = .(orb[orb$l_own_prot])<0,4>;
;  606          2003  3     (scratch[d_w_vprot])<8,4> = .(orb[orb$l_grp_prot])<0,4>;
;  607          2004  3     (scratch[d_w_vprot])<12,4> = .(orb[orb$l_wor_prot])<0,4>;
;  608          2005  2     END;
;  609          2006  2 scratch[d_l_ownuic] = .orb[orb$l_owner];
;  610          2007  2 scratch[d_b_orb_flags] = .orb[orb$b_flags];
;  611          2008  2
;  612          2009  2 !
;  613          2010  2 ! Remember whether or not an ACL exists on the device
;  614          2011  2 !
```

```
;  615          2012  3 scratch[d_v_acl_present] = (IF .orb[orb$v_acl_queue]
;  616          2013  4                            THEN (.orb[orb$l_aclfl] NEQ orb[orb$l_aclfl])
;  617          2014  2                            ELSE 0);              ! Someday maybe (.orb[orb$l_acl_count] NEQ 0)
;  618          2015  2
;  619          2016  2 !
;  620          2017  2 ! Copy standard cells from the DDB to the scratch area
;  621          2018  2 !
;  622          2019  2 copy_data (ddb, scratch, l_allocls);
;  623          2020  2
;  624          2021  2 !
;  625          2022  2 ! If the device is owned, get the process name
;  626          2023  2 !
;  627          2024  2 IF .ucb[ucb$l_pid] NEQ 0
;  628          2025  2 THEN
;  629          2026  3     BEGIN
;  630          2027  3     LOCAL
;  631          2028  3         pix,
;  632          2029  3         pcb : REF $BBLOCK;
;  633          2030  3     pix = .(ucb[ucb$l_pid])<0,16>;
;  634          2031  3     IF .pix LEQU .sch$gl_maxpix
;  635          2032  3     THEN
;  636          2033  4         BEGIN
;  637          2034  4         pcb = .sch$gl_pcbvec[.pix];
;  638          2035  4         CH$MOVE(pcb$s_lname,
;  639          2036  4                 pcb[pcb$t_lname],
;  640          2037  4                 scratch[d_t_prcnam]);
;  641          2038  4         IF .pcb[pcb$l_pid] NEQ .ucb[ucb$l_pid]  ! Consistency check: do PIDs
;  642          2039  4         THEN scratch[d_t_prcnam] = 0;           ! Still match?  If no, don't
;  643          2040  3         END;                                    ! print the procname.
;  644          2041  2     END;
;  645          2042  2
;  646          2043  2 !
;  647          2044  2 ! For journals, get journal-specific information.
;  648          2045  2 !
;  649          2046  2 IF .ucb[ucb$b_devclass] EQLU dc$_journal
;  650          2047  2 THEN
;  651          2048  3     BEGIN
;  652        P 2049  3     copy_data (ucb, scratch, l_jnl_mask,
;  653        P 2050  3                              l_jnl_segno,
;  654        P 2051  3                              l_jnl_asid,
;  655        P 2052  3                              l_jnl_quot,
;  656        P 2053  3                              l_jnl_refc,
;  657        P 2054  3                              l_jnl_trefc,
;  658        P 2055  3                              w_jnl_id,
;  659        P 2056  3                              w_devsts,
;  660          2057  3                              b_amod);
;  661          2058  3     IF NOT .ucb[ucb$v_jnl_slv]              ! If not a slave UCB
;  662          2059  3     AND .vcb NEQ 0                          ! and there's a VCB
;  663          2060  3     THEN
;  664          2061  4         BEGIN
;  665          2062  4         LOCAL
;  666          2063  4             first_jmt,
;  667          2064  4             jmt : REF $BBLOCK;
;  668        P 2065  4         copy_data(vcb, scratch, l_jnl_char,
;  669          2066  4                                 w_jnl_cop);
;  670          2067  4         IF (first_jmt = jmt = .vcb[vcb$l_jnl_jmtfl]) NEQ 0
;  671          2068  4         THEN
```

```
;  672          2069  5          BEGIN
;  673          2070  5          LOCAL
;  674          2071  5              pointer : REF VECTOR[,BYTE],
;  675          2072  5              wcb : REF $BBLOCK,
;  676          2073  5              jnlucb : REF $BBLOCK,
;  677          2074  5              jnlddb : REF $BBLOCK;
;  678          2075  5          CH$MOVE(.(jmt[jmt$t_grpnam])<0,8> + 1,
;  679          2076  5                  jmt[jmt$t_grpnam],
;  680          2077  5                  scratch[d_t_grpnam]);
;  681          2078  5          scratch[d_l_fi_mxvbn] = .jmt[jmt$l_fil_mxvbn];
;  682          2079  5          scratch[d_b_jnl_spl] = .jmt[jmt$v_spooled];
;  683          2080  5          pointer = .scratch + d_k_length;
;  684          2081  5          scratch[d_b_jnl_avl] = 0;
;  685          2082  5          DO
;  686          2083  6              BEGIN
;  687          2084  6              IF .jmt[jmt$v_avl]
;  688          2085  6              THEN scratch[d_b_jnl_avl] = .scratch[d_b_jnl_avl] + 1;
;  689          2086  6              IF (wcb = .jmt[jmt$l_fil_wcb]) NEQ 0
;  690          2087  6              THEN
;  691          2088  7                  BEGIN
;  692          2089  7                  IF (jnlucb = .jmt[jmt$l_fil_ucb]) NEQ 0
;  693          2090  7                  THEN IF (jnlddb = .jnlucb[ucb$l_ddb]) NEQ 0
;  694          2091  7                  THEN
;  695          2092  8                      BEGIN
;  696          2093  8                      LOCAL
;  697          2094  8                          count;
;  698          2095  8                      ioc$cvt_devnam(20,
;  699          2096  8                                     pointer[0],
;  700          2097  8                                     -1,
;  701          2098  8                                     .jnlucb;
;  702          2099  8                                     count);
;  703          2100  8                              pointer[0] = .count - 1;
;  704          2101  8                              pointer = pointer[.count];
;  705          2102  7                              END;
;  706          2103  6                      END;
;  707          2104  6                  jmt = .jmt[jmt$l_forjnllnk];
;  708          2105  6                  END
;  709          2106  5              UNTIL (.jmt EQL .first_jmt) OR (.jmt EQL 0);
;  710          2107  4              END;
;  711          2108  3          END;
;  712          2109  2      END;
;  713          2110  2
;  714          2111  2  !
;  715          2112  2  ! If this is a disk, get the maxblock value
;  716          2113  2  !
;  717          2114  2  IF .ucb[ucb$b_devclass] EQLU dc$_disk
;  718          2115  2  THEN
;  719          2116  2      scratch[d_l_maxblock] = .ucb[ucb$l_maxblock];
;  720          2117  2
;  721          2118  2  !
;  722          2119  2  ! If this is a disk, tape, or journal, collect common information in the VCB.
;  723          2120  2  !
;  724          2121  2  IF .ucb[ucb$b_devclass] EQLU dc$_disk
;  725          2122  2  OR .ucb[ucb$b_devclass] EQLU dc$_tape
;  726          2123  2  OR .ucb[ucb$b_devclass] EQLU dc$_journal
;  727          2124  2  THEN
;  728          2125  3      BEGIN
```

```
;  729          2126  3     IF .vcb EQL 0
;  730          2127  3     THEN (scratch[d_b_cont] = 0; RETURN true);  ! If no VCB, go away.
;  731          2128  3     scratch[d_b_cont] = 1;                      ; Say there's more
;  732        P 2129  3     copy_data (vcb, scratch, b_status,          ! Copy VCB stuff
;  733        P 2130  3                              w_rvn,
;  734        P 2131  3                              w_mcount,
;  735          2132  3                              w_trans);
;  736          2133  3     IF .ucb[ucb$b_devclass] NEQ dc$_journal
;  737          2134  3     THEN CH$MOVE(vcb$s_volname,                 ! Get the volume label
;  738          2135  3                  vcb[vcb$t_volname],
;  739          2136  3                  scratch[d_t_volnam])
;  740          2137  3     ELSE CH$MOVE(ucb$s_jnl_nam,
;  741          2138  3                  ucb[ucb$b_jnl_nam],
;  742          2139  3                  scratch[d_t_volnam]);
;  743          2140  3
;  744          2141  3     scratch[d_b_aqbtype] = scratch[d_t_acpnam] = 0;      ! Assume no AQB, therefore no ACP name
;  745          2142  3     IF (aqb = .vcb[vcb$l_aqb]) EQL 0                    ! If no AQB, then no more
;  746          2143  3     THEN RETURN true;                                   ! Go away
;  747          2144  3
;  748          2145  3     scratch[d_b_aqbtype] = .aqb[aqb$b_acptype];         ! Stash the ACP type
;  749          2146  3     IF .aqb[aqb$l_acppid] NEQ 0                         ! If the pid checks pass, get the ACP process name
;  750          2147  3     THEN
;  751          2148  4         BEGIN
;  752          2149  4         LOCAL
;  753          2150  4             pcb : REF $BBLOCK;
;  754          2151  4         pcb = .sch$gl_pcbvec[.(aqb[aqb$l_acppid])<0,16>];
;  755          2152  4         IF .pcb[pcb$l_pid] EQL .aqb[aqb$l_acppid]
;  756          2153  4         THEN
;  757          2154  4             CH$MOVE(pcb$s_lname,
;  758          2155  4                     pcb[pcb$t_lname],
;  759          2156  4                     scratch[d_t_acpnam]);
;  760          2157  3         END;
;  761          2158  3
;  762          2159  3 !
;  763          2160  3 ! If a magtape, get magtape-specific data from the Magtape Volume List (MVL).
;  764          2161  3 ! This is rather involved, since there is no direct link between the MVL and
;  765          2162  3 ! the UCB in question.  Instead, the list of UCB's in the Relative Volume
;  766          2163  3 ! Table are scanned in index order, until this UCB is found.  The mounted tape
;  767          2164  3 ! in the MVL with the same index is then found.
;  768          2165  3 !
;  769          2166  3     IF .aqb[aqb$b_acptype] EQL aqb$k_mta
;  770          2167  3     THEN
;  771          2168  4         BEGIN
;  772          2169  4         BIND
;  773          2170  4             rvt = vcb[vcb$l_rvt] : REF $BBLOCK,
;  774          2171  4             ucblst = rvt[rvt$l_ucblst] : VECTOR;
;  775          2172  4         LOCAL
;  776          2173  4             index;
;  777          2174  4         index = -1;
;  778          2175  4         INCR i FROM 0 TO .rvt[rvt$b_nvols] -1 DO
;  779          2176  5         (IF .ucblst[.i] EQL .ucb
;  780          2177  4          THEN (index = .i; EXITLOOP));
;  781          2178  4         IF .index EQL -1
;  782          2179  4         THEN
;  783          2180  5             BEGIN
;  784          2181  5             scratch[d_t_volnam] = 0;
;  785          2182  5             scratch[d_w_rvn] = 0;
```

```
;  786          2183  5          END
;  787          2184  4      ELSE
;  788          2185  5          BEGIN
;  789          2186  5          LOCAL
;  790          2187  5              limit,
;  791          2188  5              mvl : REF $BBLOCK;
;  792          2189  5          mvl = .vcb[vcb$l_mvl] + mvl$k_fixlen;
;  793          2190  5          limit = .mvl[mvl$b_nvols] - 1;
;  794          2191  5          INCR mvli FROM 0 TO .limit DO                   ! Find an entry with
;  795          2192  6              BEGIN                                       ! the same RVN and
;  796          2193  6              IF .mvl[mvl$b_rvn] EQL .index               ! that is mounted
;  797          2194  6              AND .mvl[mvl$b_status]                      ! (low bit set -> mounted)
;  798          2195  6              THEN
;  799          2196  7                  BEGIN
;  800          2197  7                  scratch[d_w_rvn] = .mvli + 1;           ! Because RVN's start at 1
;  801          2198  7                  CH$MOVE(mvl$s_vollbl,                   ! Get the volume label
;  802          2199  7                          mvl[mvl$t_vollbl],
;  803          2200  7                          scratch[d_t_volnam]);
;  804          2201  7                  EXITLOOP                                ! Skip the rest of the MVL
;  805          2202  7                  END
;  806          2203  6              ELSE mvl = .mvl + mvl$k_length;             ! Otherwise, go to next
;  807          2204  5              END;                                        ! MVL entry
;  808          2205  4          END;
;  809          2206  4      scratch[d_w_recordsz] = .vcb[vcb$w_recordsz];       ! Get record size
;  810          2207  4      RETURN true;                                        ! Go away.
;  811          2208  3      END;
;  812          2209  3  !
;  813          2210  3  ! If this is a disk, collect disk-specific information
;  814          2211  3  !
;  815          2212  3      IF .aqb[aqb$b_acptype] EQL aqb$k_f11v1
;  816          2213  3      OR .aqb[aqb$b_acptype] EQL aqb$k_f11v2
;  817          2214  3      THEN
;  818          2215  4          BEGIN
;  819        P 2216  4          copy_data (vcb, scratch, w_cluster,
;  820        P 2217  4                                   w_extend,
;  821        P 2218  4                                   l_free,
;  822        P 2219  4                                   l_maxfiles,
;  823        P 2220  4                                   b_window,
;  824          2221  4                                   b_lru_lim);
;  825          2222  3          END;
;  826          2223  3
;  827          2224  3  !
;  828          2225  3  ! For ODS-2 disks, there is more information to collect, namely the retention
;  829          2226  3  ! periods and caching parameters.
;  830          2227  3  !
;  831          2228  3      IF .aqb[aqb$b_acptype] EQL aqb$k_f11v2
;  832          2229  3      THEN
;  833          2230  4          BEGIN
;  834          2231  4          LOCAL vca : REF $BBLOCK;
;  835          2232  4          !
;  836          2233  4          ! For ODS-2 disks, get the correct free blocks from the value block associated with
;  837          2234  4          ! the volume lock.  We call an internal routine in GETDVI which will use $GETLKI to
;  838          2235  4          ! grab the value from the XQP's lock value block.  This routine expects to be called
;  839          2236  4          ! at IPL = IPL$_ASTDEL.
;  840          2237  4          !
;  841          2238  4          exe$dvi_freeblocks (.vcb[vcb$l_volkid], scratch[d_l_free]);
;  842          2239  4          copy_data (vcb, scratch, b_status2);
```

```
;  843          2240  4          CH$MOVE(vcb$s_retainmin + vcb$s_retainmax,
;  844          2241  4                  vcb[vcb$q_retainmin],
;  845          2242  4                  scratch[d_q_retainmin]);
;  846          2243  4          scratch[d_w_fidsize] = scratch[d_w_quosize]
;  847          2244  4                               = scratch[d_w_extsize]
;  848          2245  4                               = 0;
;  849          2246  4          IF (vca = .vcb[vcb$l_cache]) NEQ 0                ! If fid/ext cache
;  850          2247  4          THEN                                              ! present, get those
;  851          2248  5              BEGIN
;  852          2249  5              LOCAL cache : REF $BBLOCK;
;  853          2250  5              IF (cache = .vca[vca$l_fidcache]) NEQ 0
;  854          2251  5              THEN scratch[d_w_fidsize] = .cache[vca$w_fidsize];
;  855          2252  5              IF (cache = .vca[vca$l_extcache]) NEQ 0
;  856          2253  5              THEN
;  857          2254  6                  BEGIN
;  858          2255  6                  scratch[d_w_extsize] = .cache[vca$w_extsize];
;  859          2256  6                  scratch[d_w_extlimit] = .cache[vca$w_extlimit];
;  860          2257  6                  scratch[d_l_exttotal] = .cache[vca$l_exttotal];
;  861          2258  5                  END;
;  862          2259  4              END;
;  863          2260  4          IF (vca = .vcb[vcb$l_quocache]) NEQ 0             ! If quota cache,
;  864          2261  4          THEN scratch[d_w_quosize] = .vca[vca$w_quosize];! get quota size.
;  865          2262  4          $ASSUME (d_s_acpnam, GEQ, fllbc$s_cachename);   ! Make sure it is large enough
;  866          2263  5          IF  ((vca = .aqb[aqb$l_bufcache]) NEQ 0)        ! If buffer cache exists get the cache name
;  867          2264  4            AND
;  868          2265  5              (.aqb[aqb$l_acppid] EQL 0)                  ! if the acp didn't have a name
;  869          2266  4          THEN
;  870          2267  5              BEGIN
;  871          2268  5              scratch[d_v_cachename] = 1;                 ! Remember that it is cache name and not ACP name
;  872          2269  5              CH$MOVE (fllbc$s_cachename,
;  873          2270  5                       vca[fllbc$t_cachename],
;  874          2271  5                       scratch[d_t_acpnam]);
;  875          2272  5              scratch[d_w_bfrcnt] = .vca[fllbc$w_bfrcnt]; ! Number of buffer cache blocks
;  876          2273  4              END;
;  877          2274  3          END;
;  878          2275  2      END;
;  879          2276  2
;  880          2277  2  !
;  881          2278  2  ! In the event that that the device is spooled, the VCB field actually
;  882          2279  2  ! points to a block containing the name of the queue to which this device
;  883          2280  2  ! is spooled, and UCB$L_AMB contains the address of the UCB of the
;  884          2281  2  ! intermediate device.
;  885          2282  2  !
;  886          2283  2  IF .$BBLOCK[ucb[ucb$l_devchar], dev$v_spl]
;  887          2284  2  THEN
;  888          2285  3      BEGIN
;  889          2286  3      BIND
;  890          2287  3          int_ucb = ucb[ucb$l_amb] : REF $BBLOCK,
;  891          2288  3          int_ddb = int_ucb[ucb$l_ddb] : REF $BBLOCK;
;  892          2289  3      ioc$cvt_devnam(20,                                    ! Get device name, max this long
;  893          2290  3                     scratch[d_t_intdev],                   ! put it here,
;  894          2291  3                     -1,                                    ! in standard display format
;  895          2292  3                     .int_ucb;                              ! UCB is here
;  896          2293  3                     scratch[d_l_intlen]);                  ! final length here
;  897          2294  3      IF .vcb NEQ 0
;  898          2295  3      THEN CH$MOVE(.vcb[vcb$b_qnamecnt] + 1,
;  899          2296  3                   vcb[vcb$b_qnamecnt],
```

```
;  900          2297  3                        scratch[d_t_qname])
;  901          2298  3      ELSE scratch[d_t_qname] = 0;
;  902          2299  3      RETURN true;
;  903          2300  2      END;
;  904          2301  2
;  905          2302  2  RETURN true;
;  906          2303  1  END;
```

```
                                    0FFC 00000          .ENTRY  UTL_GET_DATA, Save R2,R3,R4,R5,R6,R7,R8,R9,-; 1841
                                                                R10,R11                                      ;
                                5E      20  C2 00002           SUBL2   #32, SP                              ;
                                58  04  AC  D0 00005           MOVL    IN_UCB, UCB                          ; 1883
                          05    3C  A8      03  E1 00009       BBC     #3, 60(UCB), 1$                      ; 1884
                                58  00A8  C8  D0 0000E         MOVL    168(UCB), UCB                        ; 1885
                                59  1C  A8  D0 00013 1$:       MOVL    28(UCB), ORB                         ; 1886
                                5B  28  A8  D0 00017           MOVL    40(UCB), DDB                         ; 1887
                                5A  34  A8  D0 0001B           MOVL    52(UCB), VCB                         ; 1888
                                57  10  AC  D0 0001F           MOVL    SCRATCH, R7                          ; 1894
                            14  AE  04  A7  9E 00023           MOVAB   4(R7), 20(SP)                        ;
                                14  BE  B4 00028               CLRW    @20(SP)                              ;
                                67      58  D0 0002B           MOVL    UCB, (R7)                            ; 1895
                          56    3C  A8      04  E1 0002E       BBC     #4, 60(UCB), 8$                      ; 1903
                          56    14  AC      04  C1 00033       ADDL3   #4, DATA, SCR                        ; 1909
                                57          56  D1 00038 2$:   CMPL    SCR, R7                              ; 1910
                                            1B  1E 0003B       BGEQU   5$                                   ;
                                58          66  D1 0003D       CMPL    (SCR), UCB                           ; 1913
                                            03  12 00040       BNEQ    3$                                   ;
                                          0422  31 00042       BRW     47$                                  ;
                            A1  8F      78      A6  91 00045 3$:   CMPB    120(SCR), #161                       ; 1915
                                            05  12 0004A       BNEQ    4$                                   ;
                                56  0107    C6  9E 0004C       MOVAB   263(R6), SCR                         ; 1916
                                56  0107    C6  9E 00051 4$:   MOVAB   263(R6), SCR                         ; 1917
                                            E0  11 00056       BRB     2$                                   ; 1910
                                56  00A0    C8  D0 00058 5$:   MOVL    160(UCB), SCR                        ; 1922
                                56    34    A6  D0 0005D       MOVL    52(SCR), SCR                         ; 1923
                      30    A7      44  A6      10  28 00061   MOVC3   #16, 68(SCR), 48(R7)                 ; 1927
                                    40  A7  34  A6  D0 00067   MOVL    52(SCR), 64(R7)                      ; 1931
                          10    3C  A8      05  E1 0006C       BBC     #5, 60(UCB), 6$                      ; 1935
                                56  00C0    C8  D0 00071       MOVL    192(UCB), SCR                        ; 1938
                    50    12  A6      01      07  EF 00076   EXTZV   #7, #1, 18(SCR), R0                   ; 1939
                                50          50  D2 0007C       MCOML   R0, R0                               ;
                                            02  11 0007F       BRB     7$                                   ;
                                            50  D4 00081 6$:   CLRL    R0                                   ; 1935
                14  BE      01      02      50  F0 00083 7$:   INSV    R0, #2, #1, @20(SP)                  ;
                                56    34    AB  D0 00089 8$:   MOVL    52(DDB), SCR                         ; 1948
                                51  00000000G  00  9E 0008D   MOVAB   SCS$GA_LOCALSB, R1                    ; 1949
                                            50  D4 00094       CLRL    R0                                   ;
                                51          56  D1 00096       CMPL    SCR, R1                              ;
                                            02  13 00099       BEQL    9$                                   ;
                                            50  D6 0009B       INCL    R0                                   ;
                14  BE      01      03      50  F0 0009D 9$:   INSV    R0, #3, #1, @20(SP)                  ;
                      1C    A7      44  A6      10  28 000A3   MOVC3   #16, 68(SCR), 28(R7)                 ; 1950
                                    2C  A7  34  A6  D0 000A9   MOVL    52(SCR), 44(R7)                      ; 1951
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                        14  BE              02  88 000AE          BISB2   #2, @20(SP)                                 ; 1952
                    18  3C  A8              05  E1 000B2          BBC     #5, 60(UCB), 10$                            ; 1957
                        14  BE              10  8A 000B7          BICB2   #16, @20(SP)                                ; 1960
                        56      00BC    C8  D0 000BB          MOVL    188(UCB), SCR                               ; 1961
        50          12  A6              01  07  EF 000C0          EXTZV   #7, #1, 18(SCR), R0                         ; 1962
                        50              50  D2 000C6          MCOML   R0, R0                                      ;
    14  BE              01              01  50  F0 000C9          INSV    R0, #1, #1, @20(SP)                         ;
                    04  39  A8              06  E1 000CF 10$:      BBC     #6, 57(UCB), 11$                            ; 1970
                                            54  D4 000D4          CLRL    R4                                          ;
                                            03  11 000D6          BRB     12$                                         ;
                        54                  01  CE 000D8 11$:      MNEGL   #1, R4                                      ; 1972
                        51          08      A7  9E 000DB 12$:      MOVAB   8(R7), R1                                   ; 1969
                        55                  58  D0 000DF          MOVL    UCB, R5                                     ; 1974
                        50                  14  D0 000E2          MOVL    #20, R0                                     ;
                              00000000G     00  16 000E5          JSB     IOC$CVT_DEVNAM                              ;
                    06  A7                  51  90 000EB          MOVB    R1, 6(R7)                                   ;
                    5C  A7          2C      A8  D0 000EF          MOVL    44(UCB), 92(R7)                             ; 1992
                    70  A7          38      A8  7D 000F4          MOVQ    56(UCB), 112(R7)                            ;
                        56          40      A8  9A 000F9          MOVZBL  64(UCB), R6                                 ;
                    78  A7                  56  90 000FD          MOVB    R6, 120(R7)                                 ;
                    79  A7          41      A8  90 00101          MOVB    65(UCB), 121(R7)                            ;
                    52  A7          54      A8  B0 00106          MOVW    84(UCB), 82(R7)                             ;
                    7A  A7          42      A8  B0 0010B          MOVW    66(UCB), 122(R7)                            ;
                    7C  A7          44      A8  7D 00110          MOVQ    68(UCB), 124(R7)                            ;
                0086    C7          5C      A8  B0 00115          MOVW    92(UCB), 134(R7)                            ;
                0088    C7          64      A8  D0 0011B          MOVL    100(UCB), 136(R7)                           ;
                0090    C7          68      A8  B0 00121          MOVW    104(UCB), 144(R7)                           ;
                008C    C7          70      A8  D0 00127          MOVL    112(UCB), 140(R7)                           ;
                0092    C7      0082    C8  B0 0012D          MOVW    130(UCB), 146(R7)                           ;
                        50      0084    C7  9E 00134          MOVAB   132(R7), R0                                 ; 1998
                        06          0B  A9  E9 00139          BLBC    11(ORB), 13$                                ; 1997
                        60          18  A9  B0 0013D          MOVW    24(ORB), (R0)                               ; 1998
                                            19  11 00141          BRB     14$                                         ;
        60              04              00      18  A9  F0 00143 13$:      INSV    24(ORB), #0, #4, (R0)               ; 2001
        60              04              04      1C  A9  F0 00149          INSV    28(ORB), #4, #4, (R0)               ; 2002
    01  A0              04              00      20  A9  F0 0014F          INSV    32(ORB), #0, #4, 1(R0)              ; 2003
        60              04              0C      24  A9  F0 00156          INSV    36(ORB), #12, #4, (R0)              ; 2004
                            58  A7              69  DC 0015C 14$:      MOVL    (ORB), 88(R7)                       ; 2006
                        0098    C7          0B  A9  90 00160          MOVB    11(ORB), 152(R7)                    ; 2007
                    10          0B  A9              01  E1 00166          BBC     #1, 11(ORB), 15$                    ; 2012
                                        51      28  A9  9E 0016B          MOVAB   40(ORB), R1                         ; 2013
                                            50  D4 0016F          CLRL    R0                                          ;
                                        51      28  A9  D1 00171          CMPL    40(ORB), R1                         ;
                                            06  13 00175          BEQL    16$                                         ;
                                            50  D6 00177          INCL    R0                                          ;
                                            02  11 00179          BRB     16$                                         ;
                                            50  D4 0017B 15$:      CLRL    R0                                          ; 2012
    14  BE              01              09      50  F0 0017D 16$:      INSV    R0, #9, #1, @20(SP)                         ;
                            54  A7          3C  AB  D0 00183          MOVL    60(DDB), 84(R7)                     ; 2019
                                        2C      A8  D5 00188          TSTL    44(UCB)                             ; 2024
                                            28  13 0018B          BEQL    17$                                         ;
                                        50      2C  A8  3C 0018D          MOVZWL  44(UCB), PIX                        ; 2030
                                00000000G   00      50  D1 00191          CMPL    PIX, SCH$GL_MAXPIX                  ; 2031
                                            1B  1A 00198          BGTRU   17$                                         ;
                                        51 00000000G    00  D0 0019A          MOVL    SCH$GL_PCBVEC, R1                   ; 2034
                                        59      6140    D0  D0 001A1          MOVL    (R1)[PIX], PCB                      ;
                    60  A7          70  A9              10  28 001A5          MOVC3   #16, 112(PCB), 96(R7)               ; 2037
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                2C   A8      60   A9  D1 001AB          CMPL    96(PCB), 44(UCB)                        ; 2038
                             03   13  001B0            BEQL    17$
                             60   A7  94 001B2          CLRB    96(R7)                                  ; 2039
                             1C   AE  D4 001B5 17$:     CLRL    28(SP)                                  ; 2046
                A1   8F           56  91 001B8          CMPB    R6, #161
                             03   13  001BC            BEQL    18$
                           00B7   31  001BE            BRW     22$
                             1C   AE  D6 001C1 18$:     INCL    28(SP)
          00E4  C7        00D4   C8  D0 001C4          MOVL    212(UCB), 228(R7)                       ; 2057
          00E8  C7          44   A8  D0 001CB          MOVL    68(UCB), 232(R7)
          00EC  C7        00D8   C8  D0 001D1          MOVL    216(UCB), 236(R7)
          00F0  C7        00CC   C8  D0 001D8          MOVL    204(UCB), 240(R7)
          00F4  C7        00DC   C8  7D 001DF          MOVQ    220(UCB), 244(R7)
          00FC  C7        00D0   C8  B0 001E6          MOVW    208(UCB), 252(R7)
          0090  C7          68   A8  B0 001ED          MOVW    104(UCB), 144(R7)
          0100  C7          5F   A8  90 001F3          MOVB    95(UCB), 256(R7)
                             68   A8  95 001F9          TSTB    104(UCB)                                ; 2058
                                  7A  19 001FC          BLSS    22$                                     ; 2059
                                  5A  D5 001FE          TSTL    VCB
                                  76  13 00200          BEQL    22$
          00E0  C7          24   AA  D0 00202          MOVL    36(VCB), 224(R7)                        ; 2066
          0101  C7          45   AA  B0 00208          MOVW    69(VCB), 257(R7)
                59          3C   AA  D0 0020E          MOVL    60(VCB), JMT                            ; 2067
                5B               59  D0 00212          MOVL    JMT, FIRST_JMT
                                 61  13 00215          BEQL    22$
                50          7A   A9  9A 00217          MOVZBL  122(JMT), R0                            ; 2075
                                 50  D6 0021B          INCL    R0
          00CE  C7          7A   A9  50 28 0021D       MOVC3   R0, 122(JMT), 206(R7)                   ; 2077
          00DC  C7          58   A9  D0 00224          MOVL    88(JMT), 220(R7)                        ; 2078
    50    2D  A?            01       03 EF 0022A       EXTZV   #3, #1, 45(JMT), R0                     ; 2079
                52        0107   C7  9E 00230          MOVAB   263(R7), POINTER                        ; 2080
            0103  C7                 50  9B 00235      MOVZBW  R0, 259(R7)                             ; 2079
                04          2E   A9      01 E1 0023A 19$:  BBC     #1, 46(JMT), 20$                    ; 2084
                          0104   C7  96 0023F          INCB    260(R7)                                 ; 2085
                            18   AE      50 A9 D0 00243 20$:  MOVL    80(JMT), WCB                    ; 2086
                                        22 13 00248      BEQL    21$
                55          54   A9  D0 0024A          MOVL    84(JMT), JNLUCB                         ; 2089
                                 1C  13 0024E          BEQL    21$
                53          28   A5  D0 00250          MOVL    40(JNLUCB), JNLDDB                      ; 2090
                                 16  13 00254          BEQL    21$
                54               01  CE 00256          MNEGL   #1, R4                                  ; 2096
                51               52  D0 00259          MOVL    POINTER, R1
                50               14  D0 0025C          MOVL    #20, R0
                      00000000G  00  16 0025F          JSB     IOC$CVT_DEVNAM
                62               51  01 83 00265       SUBB3   #1, COUNT, (POINTER)                    ; 2100
                52               51  C0 00269          ADDL2   COUNT, POINTER                          ; 2101
                59               69  D0 0026C 21$:     MOVL    (JMT), JMT                              ; 2104
                5B               59  D1 0026F          CMPL    JMT, FIRST_JMT                          ; 2106
                                 04  13 00272          BEQL    22$
                                 59  D5 00274          TSTL    JMT
                                 C2  12 00276          BNEQ    19$
                                 50  D4 00278 22$:     CLRL    R0                                      ; 2114
                01               56  91 0027A          CMPB    R6, #1
                                 09  12 0027D          BNEQ    23$
                                 50  D6 0027F          INCL    R0
          0094  C7        00B0   C8  D0 00281          MOVL    176(UCB), 148(R7)                       ; 2116
                0C               50  E8 00288 23$:     BLBS    R0, 24$                                 ; 2121
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                                02              56  91 0028B            CMPB    R6, #2                          ; 2122
                                                07  13 0028E            BEQL    24$                             ;
                                03          1C  AE  E8 00290            BLBS    28(SP), 24$                     ; 2123
                                          0196  31  00294               BRW     44$                             ;
                                            5A  D5  00297 24$:          TSTL    VCB                             ; 2126
                                                07  12 00299            BNEQ    26$                             ;
                                          0099  C7  94 0029B            CLRB    153(R7)                         ; 2127
                                          01C1  31  0029F 25$:          BRW     46$                             ;
                              0099      C7        01  90 002A2 26$:     MOVB    #1, 153(R7)                     ; 2128
                              009A      C7    0B  AA  90 002A7          MOVB    11(VCB), 154(R7)                ; 2132
                                0C      AE  00B6  C7  9E 002AD          MOVAB   182(R7), 12(SP)                 ;
                                0C      BE    0E  AA  B0 002B3          MOVW    14(VCB), @12(SP)                ;
                              00CC      C7    4C  AA  B0 002B8          MOVW    76(VCB), 204(R7)                ;
                              009B      C7    0C  AA  B0 002BE          MOVW    12(VCB), 155(R7)                ;
                                        6E  00B8  C7  9E 002C4          MOVAB   184(R7), (SP)                   ; 2136
                                A1      8F        56  91 002C9          CMPB    R6, #161                        ; 2133
                                                08  13 002CD            BEQL    27$                             ;
                00      BE        14      AA        0C  28 002CF          MOVC3   #12, 20(VCB), @0(SP)            ; 2136
                                                07  11 002D5            BRB     28$                             ;
                00      BE      00B9      C8        12  28 002D7 27$:     MOVC3   #18, 185(UCB), @0(SP)           ; 2139
                                  18      AE    9E  C7  9E 002DE 28$:     MOVAB   158(R7), 24(SP)                 ; 2141
                                          18  BE  94  94 002E4           CLRB    @24(SP)                         ;
                                        9D  C7  94  94 002E7            CLRB    157(R7)                         ;
                                    59          10  AA  D0 002EB          MOVL    16(VCB), AQB                    ; 2142
                                                AE  13 002EF            BEQL    25$                             ;
                                  08    AE        15  A9  9A 002F1          MOVZBL  21(AQB), 8(SP)                  ; 2145
                              009D      C7    08  AE  90 002F6          MOVB    8(SP), 157(R7)                  ;
                                  10    AE    0C  A9  D0 002FC          MOVL    12(AQB), 16(SP)                 ; 2146
                                                1C  13 00301            BEQL    29$                             ;
                                    51  00000000G   00  D0 00303          MOVL    SCH$GL_PCBVEC, R1               ; 2151
                                    50          0C  A9  3C 0030A          MOVZWL  12(AQB), R0                     ;
                                    50            6140  D0 0030E          MOVL    (R1)[R0], PCB                   ;
                                  10    AE        60  A0  D1 00312          CMPL    96(PCB), 16(SP)                 ; 2152
                                                06  12 00317            BNEQ    29$                             ;
                18      BE        70      A0        10  28 00319          MOVC3   #16, 112(PCB), @24(SP)          ; 2156
                                    03          08  AE  91 0031F 29$:     CMPB    8(SP), #3                       ; 2166
                                                73  12 00323            BNEQ    38$                             ;
                                    50          20  AA  D0 00325          MOVL    32(VCB), R0                     ; 2171
                                    52          44  A0  9E 00329          MOVAB   68(R0), R2                      ;
                                  04    AE            01  CE 0032D          MNEGL   #1, INDEX                       ; 2174
                                    51          0B  A0  9A 00331          MOVZBL  11(R0), R1                      ; 2175
                                    50              01  CE 00335          MNEGL   #1, I                           ;
                                                0C  11 00338            BRB     31$                             ;
                                    58            6240  D1 0033A 30$:     CMPL    (R2)[I], UCB                    ; 2176
                                                06  12 0033E            BNEQ    31$                             ;
                                  04    AE            50  D0 00340          MOVL    I, INDEX                        ; 2177
                                                04  11 00344            BRB     32$                             ;
                F0                              50            51  F2 00346 31$:     AOBLSS  R1, I, 30$              ; 2176
                    FFFFFFFF    8F          04  AE  D1 0034A 32$:     CMPL    INDEX, #-1                      ; 2178
                                                08  12 00352            BNEQ    33$                             ;
                                          00    BE  94 00354            CLRB    @0(SP)                          ; 2181
                                          0C    BE  B4 00357            CLRW    @12(SP)                         ; 2182
                                                33  11 0035A            BRB     37$                             ; 2178
                56          34      AA            24  C1 0035C 33$:     ADDL3   #36, 52(VCB), MVL               ; 2189
                              1C    AE        0B  A6  9A 00361          MOVZBL  11(MVL), LIMIT                  ; 2190
                                          1C    AE  D7 00366            DECL    LIMIT                           ;
                                    5B            01  CE 00369          MNEGL   #1, MVLI                        ; 2191
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                                          1C  11 0036C          BRB     36$
  04  AE     06  A6          08           00  ED 0036E 34$:     CMPZV   #0, #8, 6(MVL), INDEX                   ; 2193
                                          10  12 00375          BNEQ    35$                                     ;
                             0C      07   A6  E9 00377          BLBC    7(MVL), 35$                             ; 2194
             0C  BE          5B           01  A1 0037B          ADDW3   #1, MVLI, @12(SP)                       ; 2197
             00  BE          66           06  28 00380          MOVC3   #6, (MVL), @0(SP)                       ; 2200
                                          08  11 00385          BRB     37$                                     ; 2196
                             56           08  C0 00387 35$:     ADDL2   #8, MVL                                 ; 2203
                 DF          5B      1C   AE  F3 0038A 36$:     AOBLEQ  LIMIT, MVLI, 34$                        ; 2191
                       00CE  C7      50   AA  B0 0038F 37$:     MOVW    80(VCB), 206(R7)                        ; 2206
                                        00CB  31 00395          BRW     46$                                     ; 2207
                             01      08   AE  91 00398 38$:     CMPB    8(SP), #1                               ; 2212
                                          06  13 0039C          BEQL    39$                                     ;
                             02      08   AE  91 0039E          CMPB    8(SP), #2                               ; 2213
                                          12  12 003A2          BNEQ    40$                                     ;
                       00CE  C7      3C   AA  7D 003A4 39$:     MOVQ    60(VCB), 206(R7)                        ; 2221
                       00D6  C7      44   AA  D0 003AA          MOVL    68(VCB), 214(R7)                        ;
                       00DA  C7      48   AA  B0 003B0          MOVW    72(VCB), 218(R7)                        ;
                             02      08   AE  91 003B6 40$:     CMPB    8(SP), #2                               ; 2228
                                          71  12 003BA          BNEQ    44$                                     ;
                                   00D2   C7  9F 003BC          PUSHAB  210(R7)                                 ; 2238
                                     7C   AA  DD 003C0          PUSHL   124(VCB)                                ;
                  00000000G  00           02  FB 003C3          CALLS   #2, EXE$DVI_FREEBLOCKS                  ;
                       00DC  C7      53   AA  90 003CA          MOVB    83(VCB), 220(R7)                        ; 2239
       00DD  C7          6C  AA           10  28 003D0          MOVC3   #16, 108(VCB), 221(R7)                  ; 2242
                                   00F7   C7  B4 003D7          CLRW    247(R7)                                 ; 2245
                                   00ED   C7  D4 003DB          CLRL    237(R7)                                 ; 2244
                             5B      58   AA  D0 003DF          MOVL    88(VCB), VCA                            ; 2246
                                          21  13 003E3          BEQL    42$                                     ;
                             50           6B  D0 003E5          MOVL    (VCA), CACHE                            ; 2250
                                          05  13 003E8          BEQL    41$                                     ;
                       00ED  C7           60  B0 003EA          MOVW    (CACHE), 237(R7)                        ; 2251
                             50      04   AB  D0 003EF 41$:     MOVL    4(VCA), CACHE                           ; 2252
                                          11  13 003F3          BEQL    42$                                     ;
                       00EF  C7           60  B0 003F5          MOVW    (CACHE), 239(R7)                        ; 2255
                       00F1  C7      08   A0  B0 003FA          MOVW    8(CACHE), 241(R7)                       ; 2256
                       00F3  C7      04   A0  D0 00400          MOVL    4(CACHE), 243(R7)                       ; 2257
                             5B      5C   AA  D0 00406 42$:     MOVL    92(VCB), VCA                            ; 2260
                                          05  13 0040A          BEQL    43$                                     ;
                       00F7  C7           6B  B0 0040C          MOVW    (VCA), 247(R7)                          ; 2261
                             5B      18   A9  D0 00411 43$:     MOVL    24(AQB), VCA                            ; 2263
                                          16  13 00415          BEQL    44$                                     ;
                                     10   AE  D5 00417          TSTL    16(SP)                                  ; 2265
                                          11  12 0041A          BNEQ    44$                                     ;
                         14  BE           20  88 0041C          BISB2   #32, @20(SP)                            ; 2268
             18  BE    00AC  CB           18  28 00420          MOVC3   #24, 172(VCA), @24(SP)                  ; 2271
                       00F9  C7      16   AB  B0 00427          MOVW    22(VCA), 249(R7)                        ; 2272
                 31      38  A8           06  E1 0042D 44$:     BBC     #6, 56(UCB), 46$                        ; 2283
                             51    009A   C7  9E 00432          MOVAB   154(R7), R1                             ; 2290
                             55      60   A8  D0 00437          MOVL    96(UCB), R5                             ; 2293
                             54           01  CE 0043B          MNEGL   #1, R4                                  ;
                             50           14  D0 0043E          MOVL    #20, R0                                 ;
                              00000000G   00  16 00441          JSB     IOC$CVT_DEVNAM                          ;
                       00AE  C7           51  D0 00447          MOVL    R1, 174(R7)                             ;
                                          5A  D5 0044C          TSTL    VCB                                     ; 2294
                                          0F  13 0044E          BEQL    45$                                     ;
                             50      0B   AA  9A 00450          MOVZBL  11(VCB), R0                             ; 2295
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                                                50  D6 00454          INCL    R0                                      ;
                00B2    C7      0B      AA      50  28 00456          MOVC3   R0, 11(VCB), 178(R7)                    ; 2297
                                                04  11 0045D          BRB     46$                                     ;
                                        00B2    C7  94 0045F 45$:     CLRB    178(R7)                                 ; 2298
                                50              01  D0 00463 46$:     MOVL    #1, R0                                  ; 2302
                                                    04 00466          RET                                             ;
                                                50  D4 00467 47$:     CLRL    R0                                      ; 2303
                                                    04 00469          RET                                             ;

; Routine Size:  1130 bytes,    Routine Base:  $CODE$ + 01DB
```

```
;  908          2304  1 END
;  909          2305  0 ELUDOM


;                                PSECT SUMMARY
;
;       Name                    Bytes                           Attributes
;
;  $GLOBAL$                       16  NOVEC,  WRT,  RD ,NOEXE,NOSHR,  LCL,  REL,  CON,NOPIC,ALIGN(2)
;  $CODE$                       1605  NOVEC,NOWRT,  RD ,  EXE,NOSHR,  LCL,  REL,  CON,NOPIC,ALIGN(2)


;                              Library Statistics
;                                        -------- Symbols --------      Pages       Processing
;       File                             Total    Loaded   Percent      Mapped      Time
;
;  _$255$DUA28:[SYSLIB]LIB.L32;1          18619       136         0        1000        00:01.9


;                                       COMMAND QUALIFIERS

;       BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/LIS=LIS$:SHODEVUTL/OBJ=OBJ$:SHODEVUTL MSRC$:SHODEVUTL/UPDATE=(ENH$:SHODEVUTL)

; Size:          1605 code + 16 data bytes
; Run Time:         00:55.9
; Elapsed Time:     03:00.4
; Lines/CPU Min:     2476
; Lexemes/CPU-Min: 39481
; Memory Used:  564 pages
; Compilation Complete
```

SHODEVPRT
LIS

SHODEVUTL
LIS

SHODEV
LIS

SHODEVCLU
LIS

SHOMSGUTL LIS

SHONET LIS

SHOWAUDIT LIS

SHOWID LIS

SHOWLOG LIS

SHOWERROR LIS

SHOWFILES LIS

SHOMEMORY LIS