

CONTROL DATA
CORPORATION

CDC® CYBER 180 MODELS 810 AND 830

COMPUTER SYSTEMS PLUS OPTIONS

INSTALLATION AND CHECKOUT

HARDWARE MAINTENANCE MANUAL

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REVISION RECORD

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or use Comment Sheet in
the back of this manual

MANUAL TO EQUIPMENT LEVEL CORRELATION SHEET

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This manual reflects the equipment configuration listed below.

EXPLANATION: Locate the equipment type and series number, as shown on the equipment FCO log, in the list below. Immediately to the right of the series number is an FCO number. If that number and all of the numbers underneath it match all of the numbers on the equipment FCO log, then this manual accurately reflects the equipment.

EQUIPMENT TYPE	SERIES	WITH FCOs	COMMENTS
AA161-A	27		
AT453-A	05		
AU125-A	02		
AU127-A	13		
AU127-B	12		

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LIST OF EFFECTIVE PAGES

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 New features, as well as changes, deletions, and additions to information in this manual, are indicated by bars in the margins or by a dot near the page number if the entire page is affected. A bar by the page number indicates pagination rather than content has changed.

PAGE	REV	PAGE	REV	PAGE	REV	PAGE	REV
Cover	A	3-20	A	E-2	A		
Title	A	3-21	A	E-3	A		
2	A	3-22	A	E-4	A		
3	A	3-23	A	E-5	A		
4	A	3-24	A	E-6	A		
5	A	3-25	A	E-7	A		
6	A	3-26	A	Divider	A		
7	A	3-27	A	F-1	A		
8	A	3-28	A	F-2	A		
9	A	3-29	A	Divider	A		
10	A	3-30	A	G-1	A		
11	A	3-31	A	G-2	A		
12	A	3-32	A	G-3	A		
Divider	A	3-33	A	G-4	A		
1-1	A	3-34	A	G-5	A		
1-2	A	3-35	A	G-6	A		
1-3	A	3-36	A	Divider	A		
Divider	A	3-37	A	H-1	A		
2-1	A	3-38	A	Divider	A		
Divider	A	3-39	A	I-1	A		
3-1	A	3-40	A	I-2	A		
3-2	A	3-41	A	I-3	A		
3-3	A	Divider	A	I-4	A		
3-4	A	A-1	A	Divider	A		
3-5	A	A-2	A	J-1	A		
3-6	A	A-3	A	J-2	A		
3-7	A	Divider	A	J-3	A		
3-8	A	B-1	A	J-4	A		
3-9	A	B-2	A	J-5	A		
3-10	A	B-3	A	J-6	A		
3-11	A	Divider	A	Comment			
3-12	A	C-1	A	Sheet	A		
3-13	A	Divider	A	Back Cover	A		
3-14	A	D-1	A				
3-15	A	D-2	A				
3-16	A	D-3	A				
3-17	A	D-4	A				
3-18	A	Divider	A				
3-19	A	E-1	A				

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PREFACE

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This manual is for use by experienced field personnel who are familiar with CDC® CYBER computing systems and Control Data Corporation installation and checkout practices. The manual provides general and detailed installation and checkout information for the CDC® CYBER 180 Models 810 and 830 Computer Systems which include the following options:

AA161-A	CYBER 180-810/830 Basic Equipment
AT453-A	Second barrel option
AU125-A	Six Channel Increment
AU127-A	Second CPU 400 Hz
AU127-B	Second CPU SW PWR
BS167-A	Memory Increment 2 Megabytes
GK212-A	System Power Control Panel
GK410-A	SPSS Battery Ride Through
GK419-A	400 Hz Terminator Power Supply
FA784-B	Intelligent Small Disk (ISD)
AT417-A	Performance Monitor Option

The first section briefly describes the components of the central computer system.

The second section provides an installation flowchart. Start, stop times and installation notes are to be recorded here.

The third section contains installation procedures. Each procedure is placed on a right hand page. Where a diagram is considered necessary, it is placed on the facing left hand page. If a procedure refers to more than one figure, the additional figures follow the procedure. When the figure is located with an earlier procedure, it is not repeated.

The appendixes are compiled in alphabetical order by title. The appendixes contain useful information in case troubleshooting is necessary during installation.

Additional information for the equipment's physical, power, and cooling requirements is in the Site Preparation Manual and Power Distribution and Warning System Manual. These and other related manuals are listed in the System Publication Index following this preface.

Installation and checkout of the display console are normally part of the system installation and checkout. Information pertaining to the display station is in the Display Station Hardware Reference/CE Manual listed in the System Publication Index.

Information for the installation and checkout of a system 400-Hz motor generator set (MG set) is in the MG manual and the Site Preparation Section 4 Manual listed in the System Publication Index. The Site Preparation Section 4 manual also contains diagrams that show wiring connections to associated power equipment.

The CDC® 18002-2 console is available as an option for CYBER 180 models 810 and 830 systems using NOS 2.3 or later operating systems. This product includes a CDC 634B display terminal (also known as the 721-21 display terminal) and an AV117A cable. This console is referred to throughout the manual as the CC634B.

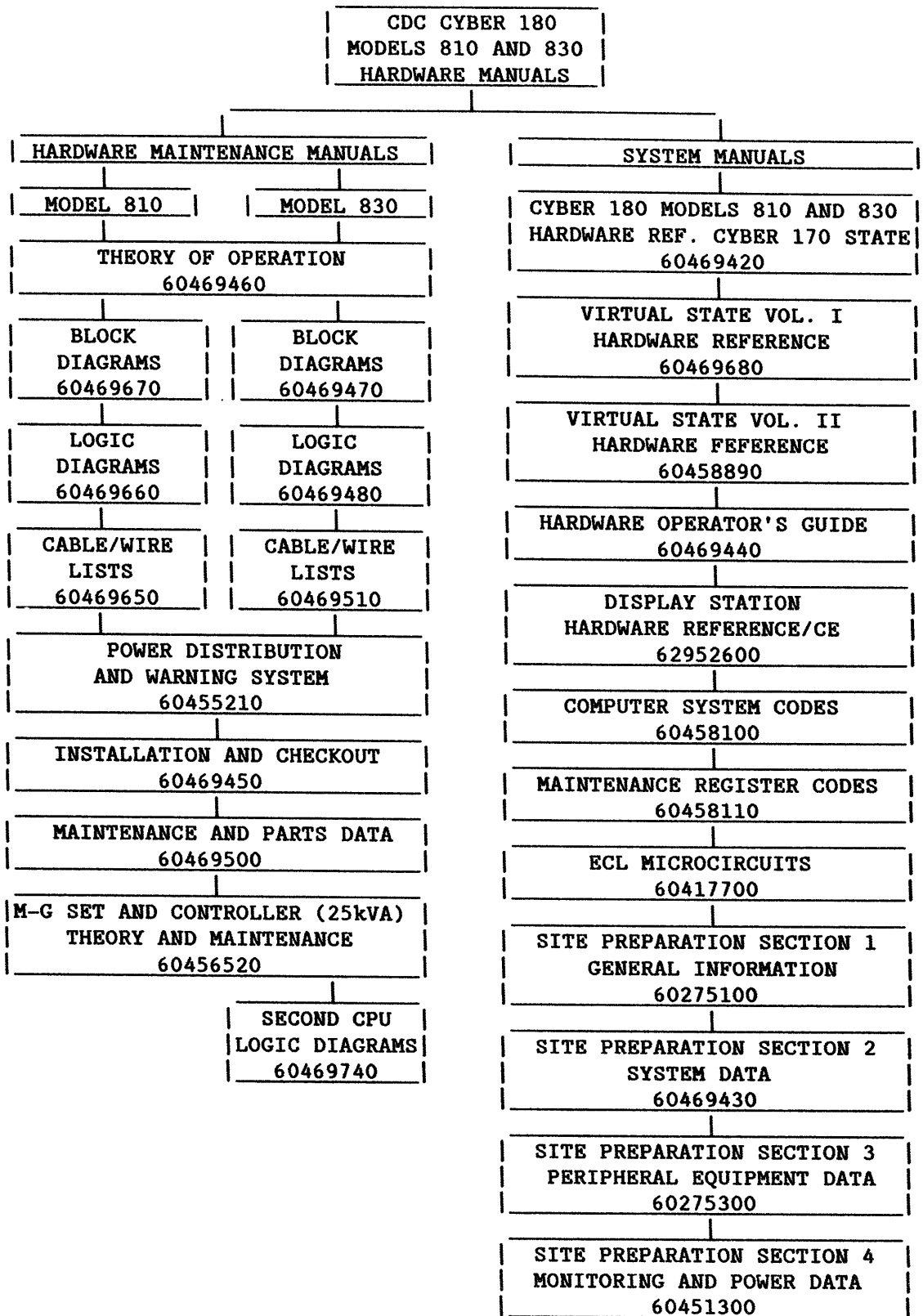
Other manuals referenced in this manual but not listed in the System Publication Index are:

<u>Control Data Publication</u>	<u>Publication Number</u>
Maintenance Software Library MSL 15X Reference Manual	60456530
MSL15X Model Independent Tests Maintenance Software Reference Manual	60469390
MSL151 Model Dependent Tests Maintenance Software Reference Manual	60469400
Packaging Document	59120000
CYBER Initialization Package (CIP) User's Handbook	60457180

Manual ordering information and latest publication revision levels are available from the Control Data Literature and Distribution Services catalog, publication number 90310500. The packaging document is available from:

Corporate Packaging and Material Handling Engineering
8100 34th Avenue South
Minneapolis, Minnesota. U.S.A. 55420

SYSTEM PUBLICATION INDEX



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CONTENTS

1. INTRODUCTION	1-1
Mainframe	1-1
Display Console	1-1
Tape Unit	1-1
Optional Power Requirement	1-2
Motor-Generator Set	1-2
SPCP	1-2
Assumptions	1-2
2. INSTALLATION FLOWCHART	2-1
3. PROCEDURES	3-1
Check Boxes	3-3
Uncrate	3-5
Position	3-7
Display Console	3-9
CC634B	3-9
CC545 (Optional)	3-9
SPCP Cables (Optional)	3-11
Electrician Hook Up	3-13
400 Hz (Optional)	3-13
CC545 Display Console (Optional)	3-13
Initial Setup Conditions	3-15
Connect I/O Cables	3-17
Electrical Inspection	3-19
Power Application (No SPCP)	3-23
Power Application (SPCP)	3-25
MSL/Operating System Preparation	3-27
Install CIP	3-29
Run Diagnostics	3-31
EMC	3-33
Remote Terminal Access Test	3-35
NOS System Validation Suite	3-37
Load Operating System	3-39
Operating System Verification	3-41

APPENDIXES

A.	DEADSTART PROGRAMS	A-1
B.	I/O CONNECTOR PANEL	B-1
C.	INTELLIGENT SMALL DISK	C-1
D.	MAINFRAME COMPONENTS	D-1
E.	MAINTENANCE REGISTERS	E-1
F.	PANEL MAP	F-1
G.	POWER DISTRIBUTION DIAGRAMS	G-1
H.	SOFTWARE TESTS AND LIMITS	H-1
I.	SWITCHES AND INDICATORS	I-1
J.	SYSTEM POWER CONTROL PANEL SETTINGS	J-1
	MG Set Power Application	J-1
	SPCP Power Application	J-2

FIGURES

3-1	I/O Connector Panel (View from bottom, cable connection side)	3-16
3-2	Hubbell Connector Power Input Area	3-18
3-3	Power Supply Tap Change Overlays	3-20
3-4	Mainframe Switches and Circuit Breakers	3-21
3-5	Power Module Front (External) View - SPSS	3-22
3-6	Ride Through Time Select Switch	3-24
3-7	Typical Logic Power Supply Controls	3-30
3-8	Remote Maintenance Access Verification	3-34
B-1	I/O Connector Panel View from Bottom - Cable Connection Side	B-2
D-1	Computer Cabinet - Front View	D-3
D-2	Computer Cabinet - Rear View	D-4
G-1	Power Distribution Diagram 50/60 Hz Only	G-2
G-2	Power Distribution Diagram 50/60 Hz and 12.5 kVA MG Set	G-3
G-3	Power Distribution Diagram 50/60 Hz and 25 kVA or Larger MG Set	G-4
I-1	Typical Logic Power Supply Control	I-2
I-2	Power Supply Switches and Indicators	I-1
I-3	Fault Indicators and Voltage Percent Meter	I-3
I-4	Circuit Breakers	I-4
J-1	System Power Control Panel - Internal View	J-3
J-2	SPCP-Front, Side, and Bottom Views	J-5

SECTION 1

INTRODUCTION

=====

This manual supports the installation and checkout of a CDC® CYBER 180 Model 810 or CDC® CYBER 180 Model 830 central computer. The central computer consists of modules which form parts of the mainframe. Power requirement is three phase, 50/60 Hz and optional 400 Hz supplied by a motor generator (MG) set.

MAINFRAME

The central computer is housed in the basic cabinet. It consists of one power module and two logic modules which contain:

- One or two central processor units (CPU)
- Input output unit (IOU)
- Central memory unit (CM)

On the right side of the basic cabinet, (rear view), are two optional disk modules called Intelligent small disk (ISD) units. Each ISD contains up to four disks/controllers connected via an ISD adaptor to the central computer.

DISPLAY CONSOLE

A standard CC634B display console and or an optional CC545 display console is connected to the mainframe.

TAPE UNIT

An optional intelligent small magnetic tape drive, (ISMT), is connected to the channel interface.

OPTIONAL POWER REQUIREMENT

MOTOR-GENERATOR SET

The smallest MG set is rated at 12.5 kVA. Larger MG sets require a wall-mounted System Power Control Panel (SPCP). The MG provides three phase, 400-Hz power to the mainframe, peripherals, and 40 V dc terminator power supply.

SPCP

The SPCP is a 400-Hz power control device.

ASSUMPTIONS

The following installations are assumed to be complete, or items available on site before the installation of the mainframe:

- Field Planning and Construction (FP&C) preparation and verification of site.
- Circuit breaker panels and related wiring including Hubbell connectors installed.
- Emergency-off switches installed and tested.
- MG set installed, where applicable.
- SPCP and associated control cables installed, where applicable.
- 40 V dc terminator power supply installed, where applicable.
- Channel cables labelled, installed.
- External peripherals and cables installed.
- ISD formatted at factory.
- Two port multiplexer (TPM) cables for remote terminal assistance (RTA) on site, where applicable.
- Installation tool kit on site.
- Deadstart and CYBER Initialization Package (CIP) tapes on site.
- Electromagnetic compatibility (EMC) kit on site.
- Phone and modem for TPM test on site.
- Mainframe positioned but not uncrated.
- Scratch tape for ISMT read and write tests.
- A table for the CC634 display console.

NOTE

If system is a field transfer, refer to Site Preparation, Section 2 Manual for correct Hubbell Connectors Power overlay and jumper wires. Ensure that they are on site two weeks prior to the mainframe installation.

SECTION 2

INSTALLATION FLOWCHART

=====

Flowchart not available. To be supplied.

SECTION 3

PROCEDURES

=====

The procedures in this section of the manual are provided for use during the installation and checkout of the computer system. Procedures for installation of disk, tape, and console display are supplied with those units separately. Refer to applicable installation procedures in the manuals listed below:

Intelligent small disk (ISD) subsystem	60457210
FA165-A Intelligent small tape (ISMT) adapter	60461100
810-1 Network Processor Unit (NPU)	60461470
Control Data® CYBER 170 Display Station	
Hardware Reference/Customer Engineering Manual	62952600
CDC® 721 Display Terminal Unpacking/Packing Instructions	62940038
Control Data® 721-21/31 Display Terminal Owner's Manual/ Installation Instructions	62950101

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CHECK BOXES

- _____ 1. A CDC Certified Installer (CI) must be present for unpacking.
- _____ 2. Locate previously shipped miscellaneous items package containing Installation and Checkout (ICO) Manual, controlware tape, and CYBER Initialization Package (CIP) tape.
- _____ 3. Locate envelope marked "Open First" which contains instructions for unpacking. Check contents of all boxes.
- _____ 4. Check paks using panel maps in appendix F.
- _____ 5. Complete missing/damaged list and send for replacement.

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UNCRATE

- _____ 1. A CDC CI must direct uncrating and check for damage.

WARNING

Be careful when cutting packing straps. They may snap outward and cause personal injury.

- _____ 2. Uncrate mainframe using the unpacking instructions attached to the outside of the mainframe crate.
- _____ 3. Unpack display station using instructions (62940038) attached to the outside of the carton.
- _____ 4. Remove outer plastic wrapping. Allow the equipment to reach room temperature before power is applied.
- _____ 5. Check equipment for damage. Refer to appendix D for locations.

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POSITION

- _____ 1. Verify that peripherals are installed.
- _____ 2. Verify mainframe position using site plan.
- _____ 3. Position display console.

CAUTION

Do not cut floor tiles inside the computer room.

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DISPLAY CONSOLE

CC634B

CAUTION

Verify that voltage select switch is set for local power requirement.

- _____ 1. Locate display console owner's manual and follow the instructions in the installation section.
- _____ 2. Locate and connect AV117A data cable (19268593) between console and terminal 0 of mainframe at the I/O Connector Panel. The I/O connector panel is located at the rear of logic module 2. The panel is hinged for easy access to the connectors.
- _____ 3. Set up console parameters using procedures in the manual. Also refer to CIP User's Handbook.

CC545 (Optional)

- _____ 1. Refer to installation procedures supplied with the console for 400-Hz power wiring.
- _____ 2. Locate and connect the two data cables to the CC545 port on the I/O Connector Panel.
- _____ 3. Follow deadstart procedures in accordance with site parameters.

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SPCP CABLES (Optional)

- _____ 1. Verify that status and control cables are connected to SPCP and run to mainframe position.
- _____ 2. Verify that cables were labelled correctly at the factory.
- _____ 3. Verify that 50/60-Hz and 400-Hz input power lines are connected properly to the SPCP.
- _____ 4. Verify that MG REMOTE ADJUST, MG REMOTE START, MG POWER STATUS connections are complete.
- _____ 5. Install the four status and control cables to the I/O Connector Panel as follows:

<u>SPCP</u>	<u>MAINFRAME I/O CONNECTOR PANEL</u>
J7	J6
J8	J7
J9	J4
TB3-3,4	J8

- _____ 6. Terminate J5 on I/O connector panel.
- _____ 7. Verify operation of SPCP.

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ELECTRICIAN HOOK UP

400 Hz (Optional)

- _____ 1. Connect 50/60 Hz power lines to 50/60 Hz power input area J1.
- _____ 2. Connect MG control wiring from TB1-1 through -4 to mainframe TB3-1 through -4 (the smaller TB).
- _____ 3. Connect power wiring from 400-Hz wall distribution power panel to mainframe J2.

CC545 DISPLAY CONSOLE (Optional)

- _____ 1. Connect 400-Hz power wiring to display console as follows:
 - a) Safety ground to lug E3.
 - b) Neutral to TB4-1.
 - c) Line 1 to TB4-2.
 - d) Line 2 to TB4-3.
 - e) Line 3 to TB4-4.

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INITIAL SETUP CONDITIONS

CAUTION

Wear electro static discharge (ESD) wrist strap when handling logic paks.

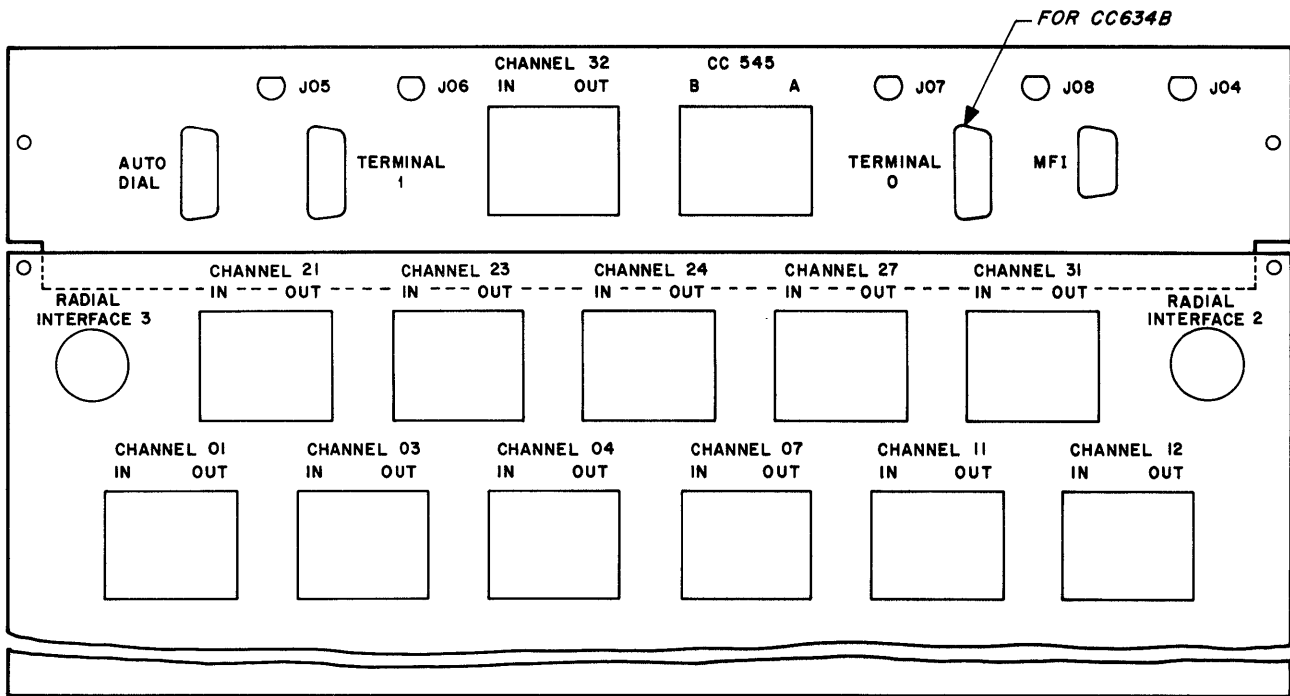
NOTE

Four board locks (19268398) are installed at the factory to keep the logic modules from loosening. Board locks must be reinstalled if removed for any reason.

NOTE

For deinstallation of computer, remove jumper wire from IOU20-A009/IOU20-B009 and connect it to IOU19-C007/IOU20-D007.

- _____ 1. Check visually for loosened connections (Berg blocks).
- _____ 2. Remove jumper wire from panel location IOU19-C007/IOU20-D007 and attach it to IOU20-A009/IOU20-B009.



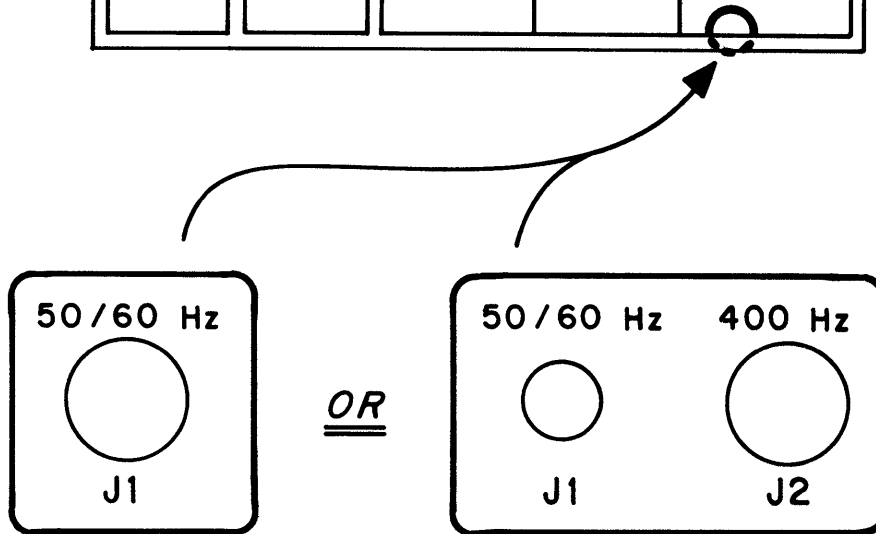
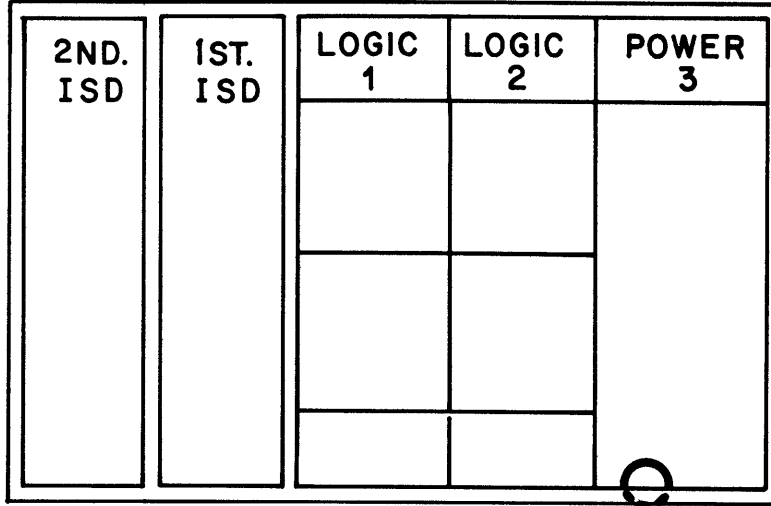
C1146

Figure 3-1. I/O Connector Panel (View from bottom, cable connection side)

CONNECT I/O CABLES

- _____ 1. Attach all channel cables to I/O connector panel. Refer to figure 3-1. For a complete list of logic backpanel connections, see appendix B.
- _____ 2. Connect optional cables to CC545 connectors if a CC545 display console is used. Refer to figure 3-1.

CABINET-FRONT VIEW



C1147

Figure 3-2. Hubbell Connector Power Input Area

ELECTRICAL INSPECTION

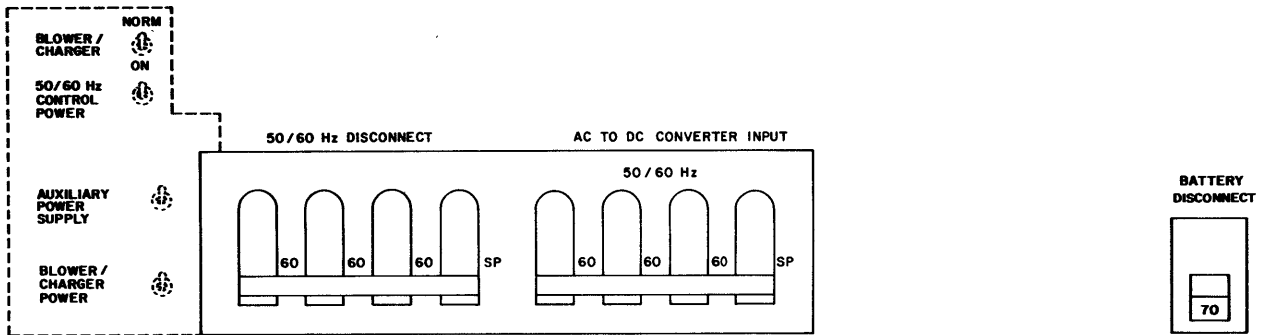
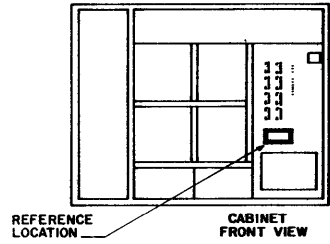
- _____ 1. Set all circuit breakers to OFF at wall power distribution panel.
- _____ 2. Check wiring on connectors according to diagrams in appendix G.
- _____ 3. Check that Hubbell connector is seated snugly in the 50/60 Hz, 3-phase power input connector J1. Turn lock ring to secure. Refer to figure 3-2.
- _____ 4. If 400 Hz, 3-phase is required by system, check that larger Hubbell connector is seated snugly into connector J2. Turn lock ring to secure.
- _____ 5. Verify that correct overlay for site power is installed. Refer to the appropriate diagram in figure 3-3.
- _____ 6. Verify that tap change jumpers are connected according to the diagram on the overlay.
- _____ 7. Set BLOWER/CHARGER switch to NORM and all circuit breakers to off. Refer to figure 3-4.
- _____ 8. Set mechanical zero adjustment of voltage percent meter to zero.
- _____ 9. Set two circuit breakers (AUXILIARY POWER SUPPLY, BLOWER/CHARGER POWER) to on.
- _____ 10. Verify power for ISD. Refer to appendix C.
- _____ 11. Close and lock power panel door.

NOTE

Electrical safety ground is in place when Hubbell is installed.

Tap change diagram not available.
Correct diagram to be supplied.

Figure 3-3. Power Supply Tap Change Overlays



C1016

Note: Blower/Charger is not sequencing when toggle switch is at ON position.

Figure 3-4. Mainframe Switches and Circuit Breakers

MARK ANY LIT RED LIGHTS AND TRAPPED CIRCUIT BREAKERS.

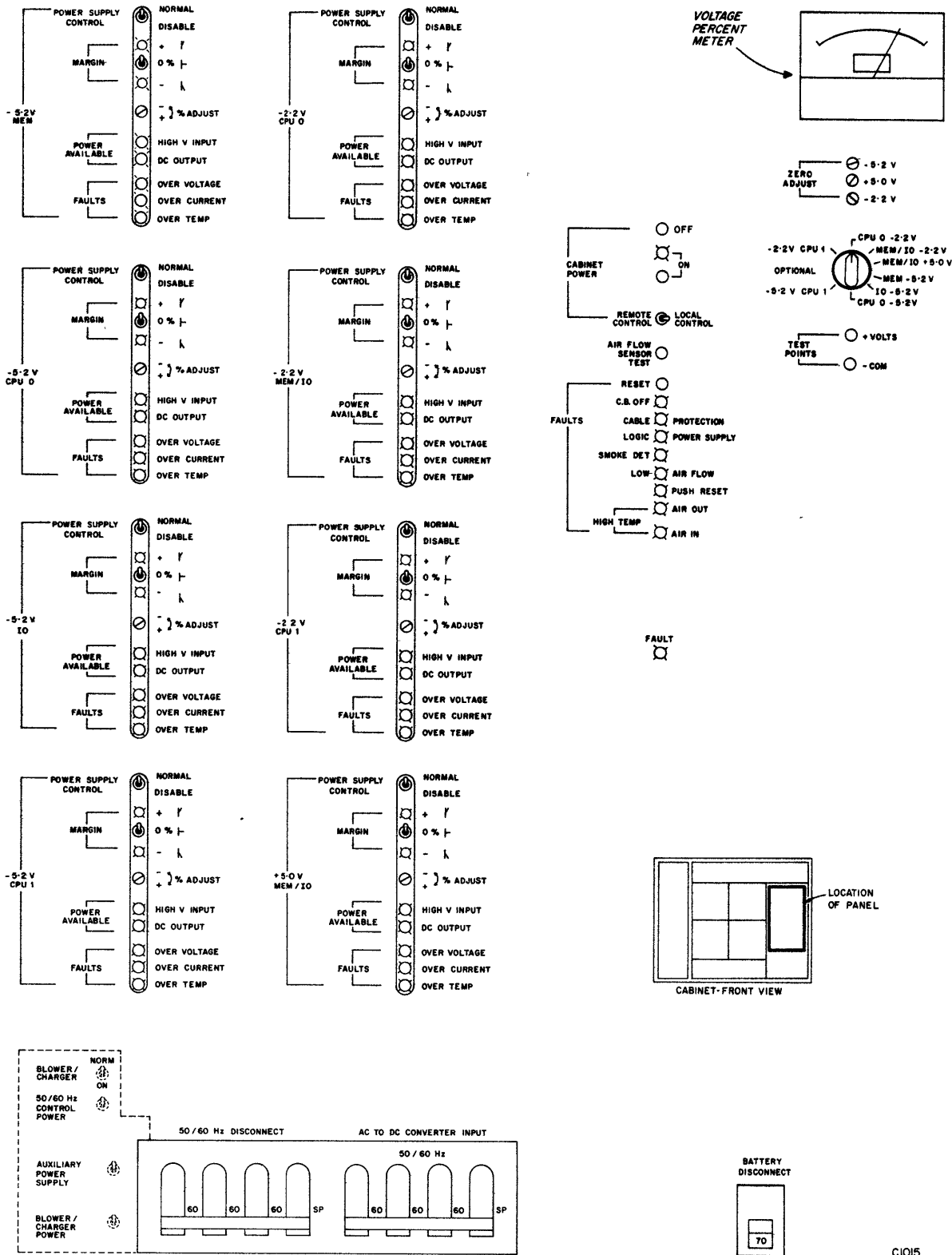


Figure 3-5. Power Module Front (External) View - SPSS

POWER APPLICATION (No SPCP)

- _____ 1. Set wall power distribution panel 50/60-Hz and 400-Hz (if applicable) circuit breakers for the mainframe and display console to on.
- _____ 2. Set CABINET POWER switch to LOCAL CONTROL position. Refer to figure 3-5.
- _____ 3. Switch on all mainframe circuit breakers.
- _____ 4. Push FAULTS RESET button and power up mainframe. Refer to figure 3-5.
- _____ 5. Check mainframe voltage using digital voltmeter.
- _____ 6. Verify that the VOLTAGE PERCENT METER (on mainframe) registers 0% for all power supply locations. Refer to figure 3-5.
- _____ 7. Apply power to display console.

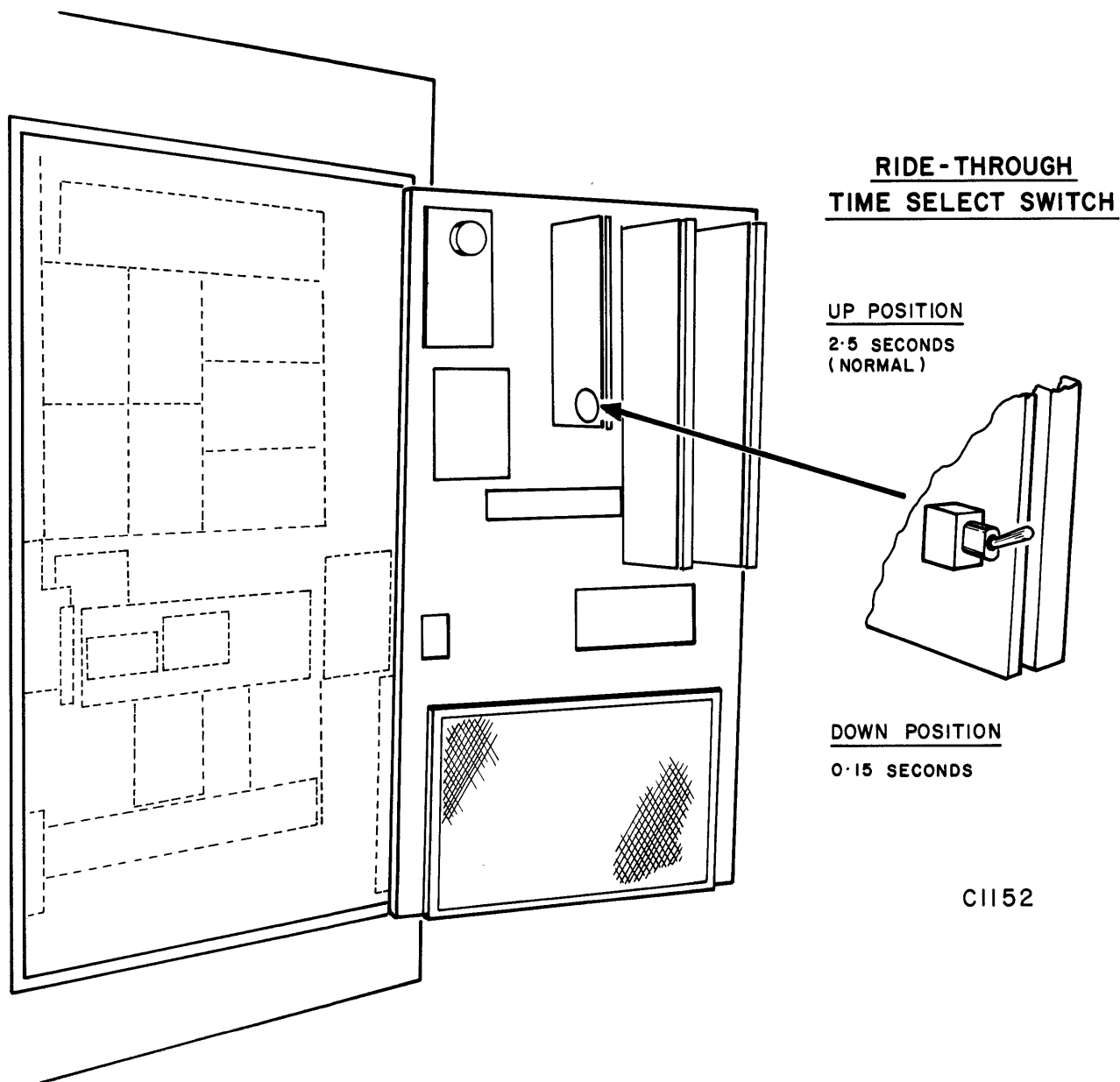


Figure 3-6. Ride Through Time Select Switch

POWER APPLICATION (SPCP)

Use this procedure under normal installation conditions. Refer to appendix J for power application procedures when voltage adjustments are necessary.

- _____ 1. Set wall power distribution panel 50/60-Hz and 400-Hz (if applicable) circuit breakers for the mainframe and display console to on.
- _____ 2. Apply power to SPCP by first setting the 50/60 Hz DISCONNECT switch on. Refer to figure 3-5.
- _____ 3. Set CABINET POWER CONTROL switch to LOCAL position and set SPCP to LOCAL. Refer to figure 3-5.

NOTE

CABINET POWER CONTROL switch must be set to REMOTE on the mainframe during normal computer system operation.

- _____ 4. Set the POWER OFF RIDETHROUGH TIME SELECT switch to 2.5 seconds. Refer to figure 3-6.
- _____ 5. Push POWER ON button on SPCP. MG starts and power is available in approximately one minute.
- _____ 6. Verify that the SPCP voltmeter registers 0% and that power is available at the 40 V dc terminator power supply.
- _____ 7. Switch on all mainframe circuit breakers.
- _____ 8. Push FAULTS RESET and CABINET POWER ON buttons. Refer to figure 3-5.
- _____ 9. Select 0% on all POWER SUPPLY CONTROL (voltage) MARGINS.
- _____ 10. Verify mainframe voltages using a digital voltmeter.
- _____ 11. Verify that the mainframe voltage percent meter registers 0% for all power supply locations.
- _____ 12. Turn display console on.

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MSL/OPERATING SYSTEM PREPARATION

NOTE

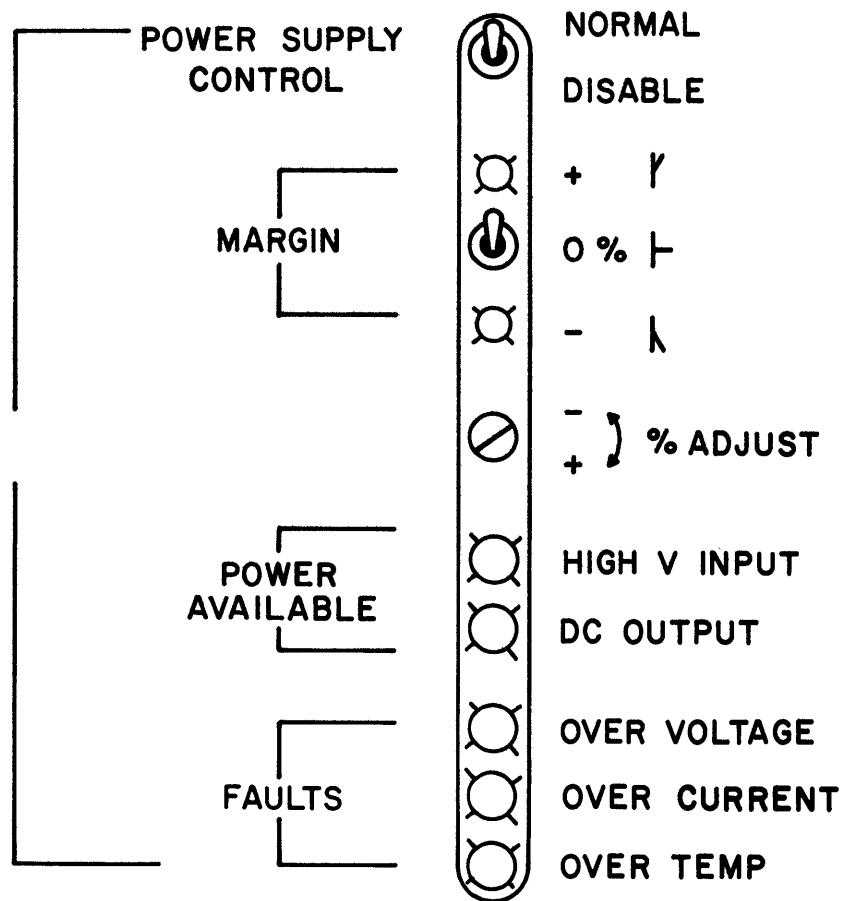
For 810-1 systems, where ISD and ISMT are the peripheral equipment, refer to procedures supplied with the ISD and ISMT.

- _____ 1. Load and run Long Deadstart Sequence (LDS) program.
- _____ 2. Enter coldstart tape programs from appendix A.
- _____ 3. Mount CYBER Initialization Package (CIP) tape and upload tape controlware.
- _____ 4. Coldstart/warmstart disk.
- _____ 5. Deadstart MSL.
- _____ 6. Load controlware to disk controller.

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INSTALL CIP

- _____ 1. Select a deadstart disk.
- _____ 2. Install CIP tape to disk.



C1017

Figure 3-7. Typical Logic Power Supply Controls

RUN DIAGNOSTICS

The command buffer VERIFY includes automatic margin condition testing. If the command buffer is not available, use the tests listed in appendix H.

The command buffer VERIFY calls a series of diagnostics intended for confidence testing. Execution time is approximately 75 minutes.

To interpret the contents of maintenance registers, refer to appendix E.

- _____ 1. Deadstart from disk.
- _____ 2. Load command buffer called VERIFY.
- _____ 3. Set NORMAL/DISABLE Switch to NORMAL on all of the logic power supply controls (figure 3-7).
- _____ 4. Set MARGIN switch to 0% on all of the logic power supply controls (figure 3-7).
- _____ 5. Run the VERIFY command buffer by typing GO VERIFY.

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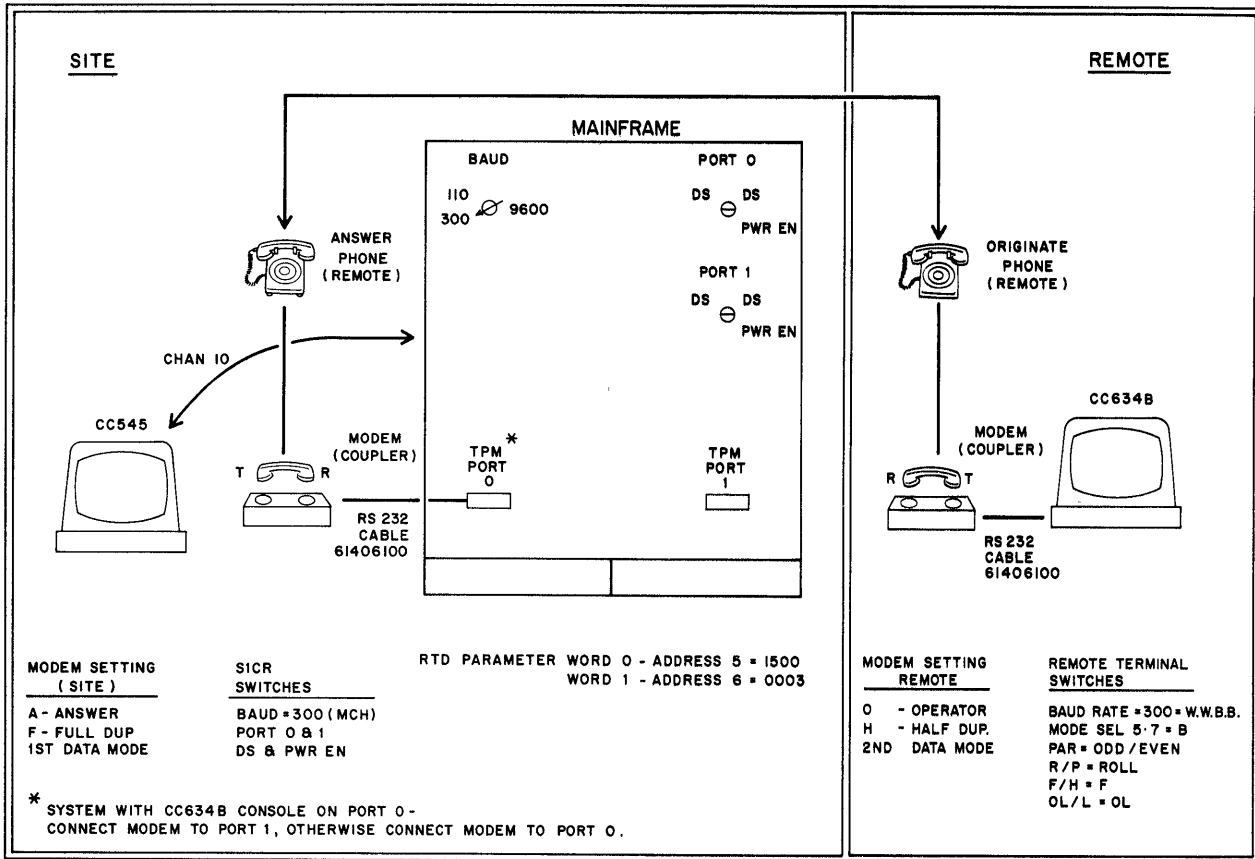
EMC

- _____ 1. Close mainframe doors.
- _____ 2. Connect one of the 4.5 m (15 ft) test leads from the interference generator to a ground connection at the SPCP, if applicable. Connect the other lead to a frame ground close to the power input connector on the mainframe cabinet.
- _____ 3. Plug in the interference generator and switch it on.
- _____ 4. Run RCT11, one pass. When the test is running, turn the OUTPUT CONTROL (amplitude) on the interference generator until there is a meter reading of 10,000 V. No error should occur.

NOTE

Press the space bar periodically while running this test. This assures the test will not take an indefinite amount of time.

- _____ 5. Turn off the interference generator and remove the leads.



C1153

Figure 3-8. Remote Maintenance Access Verification

REMOTE TERMINAL ACCESS TEST

- _____ 1. Connect modem to TPM. Refer to figure 3-8.
- _____ 2. Run TPM1, one pass.
- _____ 3. Contact remote technical assistance (RTA) at Support Services Organization (SSO) for remote capability verification 24 hrs a day.

In U.S. except Minnesota: 800-328-3980

In Canada and Minnesota: 612-931-3131 (collect)

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NOS SYSTEM VALIDATION SUITE

- _____ 1. Load and run NOS System Validation Suite (NSVS) for approximately one hour.

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LOAD OPERATING SYSTEM

- _____ 1. Have Central Software Field Support (CSFS) load the operating system deadstart tape.
- _____ 2. Check the on-line CML diagnostics on peripherals.

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OPERATING SYSTEM VERIFICATION

- _____ 1. Central Software Field Support runs the OS verification using customer jobs.

APPENDIX A

DEADSTART PROGRAMS

=====

This appendix contains warmstart and coldstart programs.

Disk warmstart program (usually program 0) for 844/885 ISD

Disk controller is on I/O channel 1.

01	1402
02	73cc
03	0017
04	75cc
05	77cc
06	03eu
07	74cc
10	71cc
11	7301
12	0m10
13	0000
14	0000
15	0000
16	0000
17	0000
20	7112

Tape warmstart program (usually program 1) for 66x/67x ISMT

Tape controller is on I/O channel 12.

01	0000
02	0000
03	0000
04	75cc
05	77cc
06	01eu*
07	74cc
10	71cc
11	7301
12	0m10
13	0000
14	0000
15	0000
16	0000
17	0000
20	0000

* On 66x unit replace 01eu by 026u

Suggested values:

cc Channel: Disk Controller = 01; Tape Controller = 12
 m Central memory size 3=65k, 4=98k, 5=131k, 6=198k, 7=262k
 e Equipment: Disk drive 844 = 0; 885 = 4; ISMT, Tape drive 67x = 2
 u Unit number

The following four programs are in ROM.

Deadstart Program 4:

Chain test program. It tests out PP transfers in the lower PPs.

Deadstart Program 5:

Chain test program. It tests out PP transfers in the upper PPs.

Deadstart Program 6:

This program master clears IOU, CM, and CPU.

Deadstart Program 7:

This is used to paint characters (AB) on the display station screen.

Disk Coldstart Initialization: 844-7152 (disk no.-controller no.),
885-7152 and so on.

	<u>Chan 0, 12, 13, 32,33</u>	<u>Other Chan</u>
01	75cc	1402
02	77cc	73cc
03	01eu	0007
04	0300	75cc
05		77cc
06		01eu
07		0000
10		0300

Tape Coldstart Initialization: 66X-7152 and so on.

0001	75cc@
0002	1701
0003	0576
0004	2400*
0005	2400*
0006	77cc
0007	007u**
0010	0300

Notes

cc Channel number.
u Unit number.
@ If cc=0, this instruction must be 7540.
* If 6681 or 6684 is on channel, instruction must be 77cc, 2100.
** Tape unit number must be in the range 10 to 17 octal.
For ISMT initialization, replace code 007u by 006u (line 07).
3 Equipment: Disk 844 = 0
 885 = 4

APPENDIX B

I/O CONNECTOR PANEL

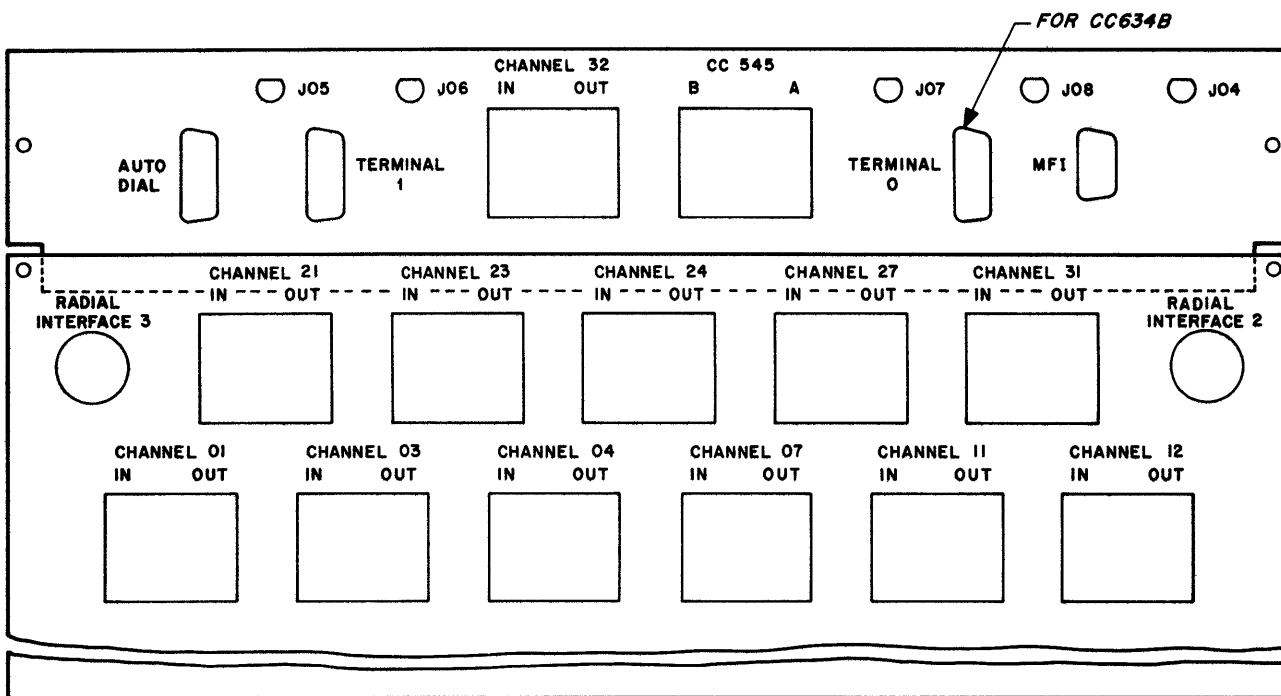
This appendix contains a list of preferred channel configurations and a table of channel cable/panel pin locations.

Preferred Channel Configurations

<u>Channel</u>	<u>Device</u>	<u>Channel</u>	<u>Device</u>
0 (ICI)	ISD-1	20 (ICI)	ISD-2
1 (170)	RMS-1	21 (170)	RMS-4
2 (INT)	-	22 (INT)	-
3 (170)	RMS-2	23 (170)	OPEN
4 (170)	RMS-3	24 (170)	COMM-2
5 (INT)	-	25 (INT)	-
6 (ICI)	ISMT-1	26 (ICI)	OPEN
7 (170)	COMM-1	27 (170)	OPEN
10 (CONS)	-	30 (INT)	-
11 (170)	OPEN	31 (170)	MAG. TAPE-2
12 (170)	MAG. TAPE-1	32 (170)	MAG. TAPE-3
13 (INT)	-	33 (INT)	-

Abbreviations

COMM	Communications 2550
CONS	Console
ICI	Integrated Controller Interface
INT	Internal (reserved)
ISD	Intelligent Small Disk Unit
ISMT	Intelligent Small Magnetic Tape Unit
RMS	Rotating Mass Storage



C1146

Figure B-1. I/O Connector Panel View from Bottom - Cable Connection Side

FACTORY-INSTALLED USER CHANNEL CABLE CONNECTIONS

CONN PANEL	CABLE NAME	BACKPANEL LOC'N/PIN	CONN PANEL	CABLE NAME	BACKPANEL LOC'N/PIN
Ch1	J22A	D10-A009	Ch21	J11A	D15-A009
	J22B	D10-B009		J11B	D15-B009
Ch3	J16A	D10-A039	Ch23	J12A	D15-A039
	J16B	D10-B039		J12B	D15-B039
Ch4	J17A	D10-A072	Ch24	J13A	D15-A072
	J17B	D10-B072		J13B	D15-B072
Ch7	J18A	D09-A009	Ch27	J14A	D16-A009
	J18B	D09-B009		J14B	D16-B009
Ch11	J19A	D09-A039	Ch31	J15A	D16-A039
	J19B	D09-B039		J15B	D16-B039
Ch12	J20A	D09-A072	Ch32	J10A	D16-A072
	J20B	D09-B072		J10B	D16-B072

DESC	CABLE NAME	BACKPANEL LOCATION
CC545	J21A	D17-A072
DSPLY	J21B	D17-B072
ATODAL	J01-1*	D20-C034/
2PTMUX	J02-1*	D20-D044/
2PTMUX	J03-1*	D20-D024/

SPCP J9	Mainframe J04
J7	J06
J8	J07
TB3-3,4	J08

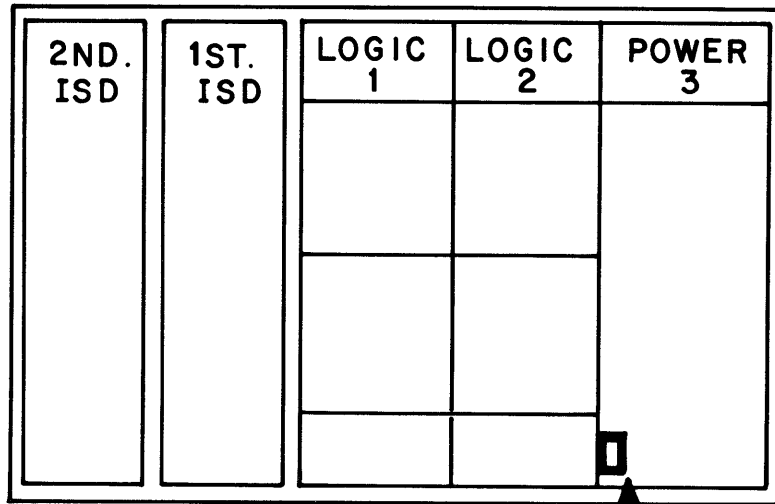
Mainframe J05	TERMINATED
J09	MFI
J23	Radial I/F 3
J24	Radial I/F 2

APPENDIX C

INTELLIGENT SMALL DISK

=====
This appendix illustrates the location of the Intelligent Small Disk Units (ISD) and the location of the power outlet for them.

CABINET-FRONT VIEW



POWER OUTLET
FOR ISD

C1154

APPENDIX D

MAINFRAME COMPONENTS

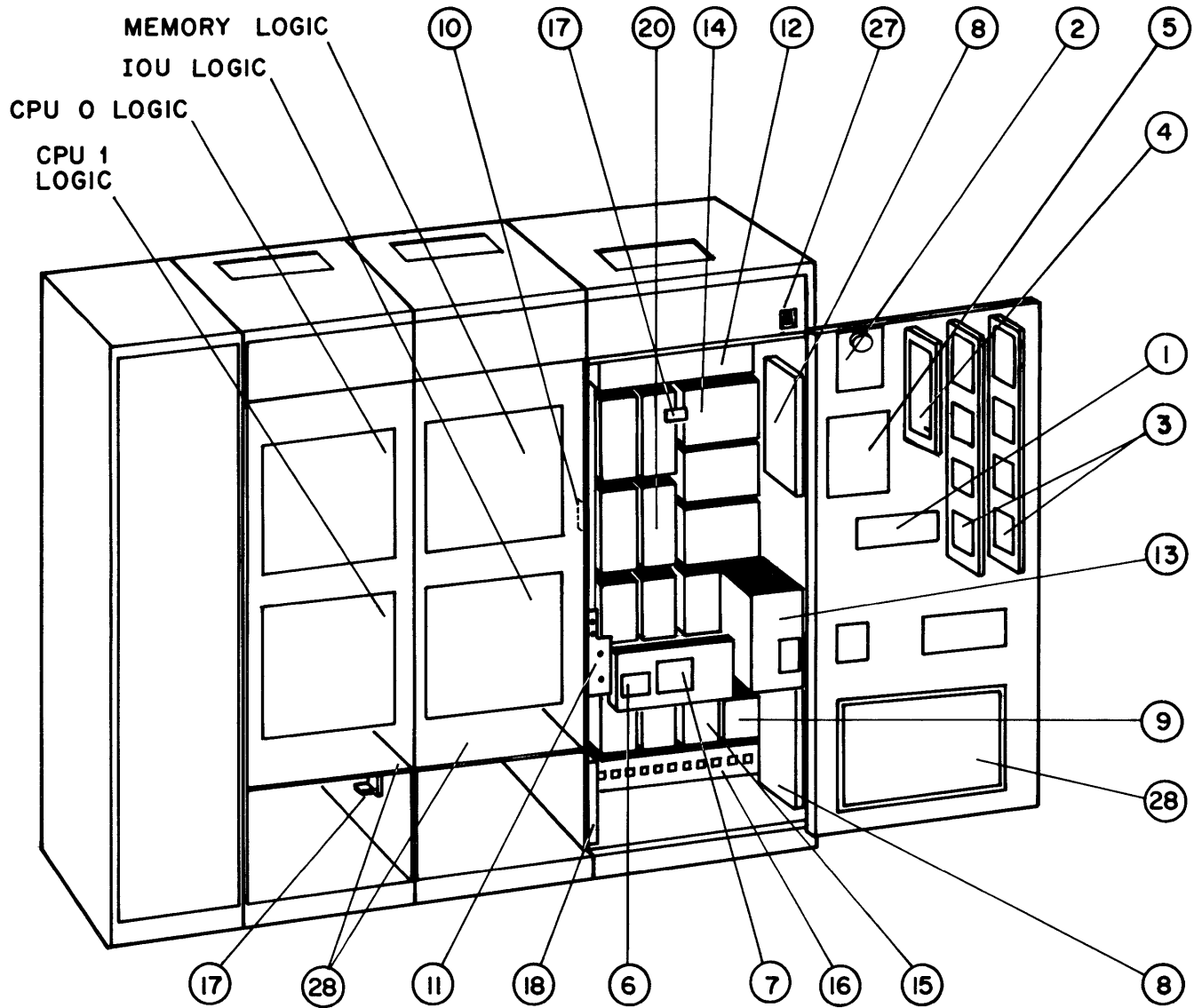
=====
 This appendix consists of tables and diagrams identifying the components found in the power supply.

CABINET, FRONT AND REAR VIEWS (SHEET 1 of 2)

Find Number	Description
1	Protect module assembly
2	Voltage percent meter assembly
3	Power supply interface module assembly
4	Power control module assembly, Ride through time sel switch
5	Power control interface
6	Circuit breaker 50/60 Hz
7	AC to DC converter input circuit breaker (50/60 Hz or 400 Hz).
8	Fuse panel
9	Capacitor box assembly
10	Auxillary power supply regulator assembly
11	Circuit breakers: bulk auxillary power supply (top); blower/charger (bottom)
12	AC to DC converter assembly
13	Battery control box assembly circuit breaker
14	Battery pack assembly
15	DC to DC outlet power supply

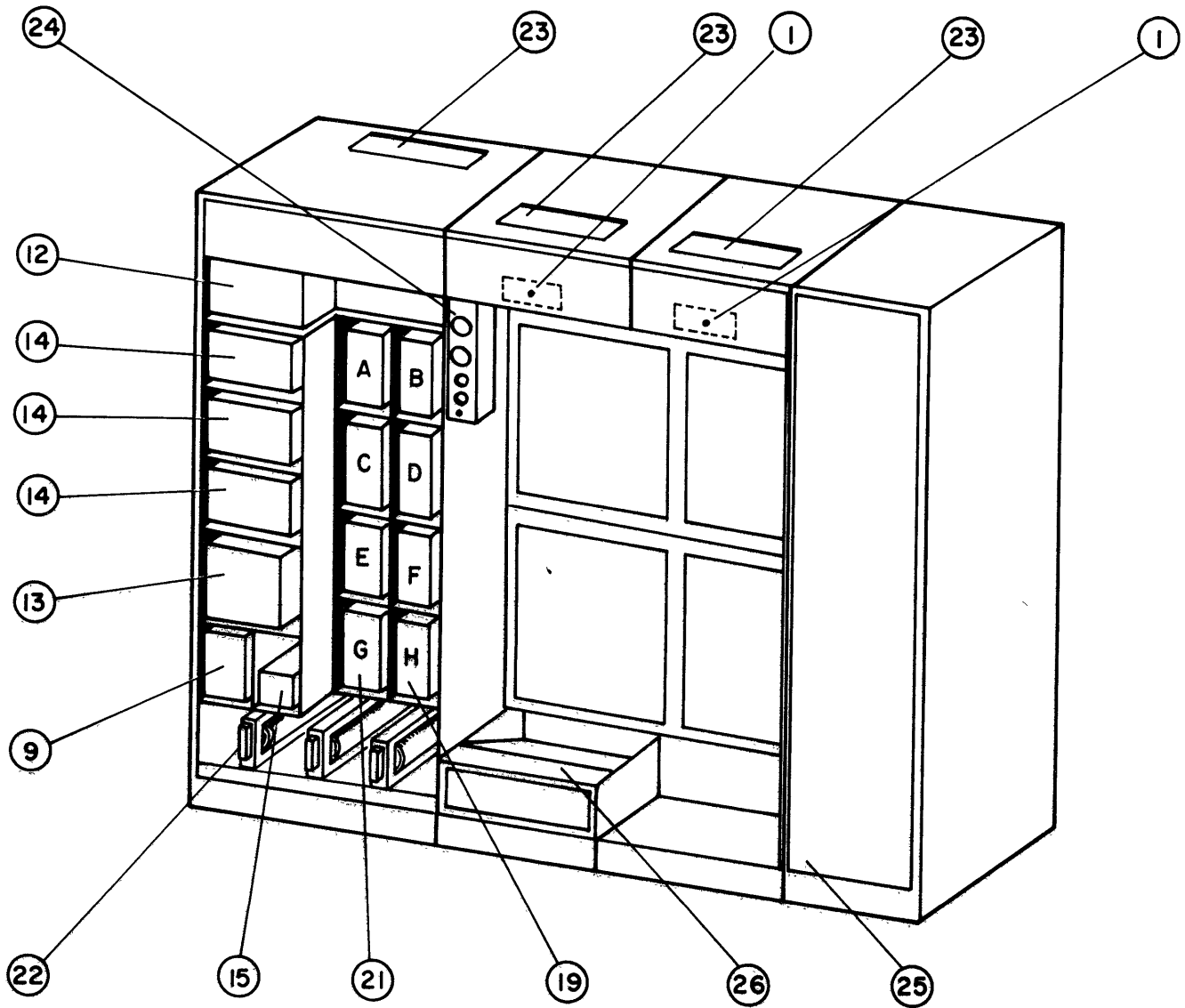
CABINET, FRONT AND REAR VIEWS (SHEET 2 of 2)

Find Number	Description
16	Tap change panel
17	Air flow sensor assembly
18	RFI filter
19	Switching power supply +5.2 V
20	Switching power supply -2.2 V
21	Switching power supply +5.0 V
22	5 kVA transformer
23	Blower module frame assembly
24	Two port mux box
25	Intelligent small disk
26	I/O connector panel
27	Emergency off switch
28	Air filter assembly



C1020

Figure D-1. Computer Cabinet - Front View



- | | |
|-------------------|------------------|
| A — CPU 0 -2·2V | E — CPU 1 -2·2V |
| B — MEM +5·2V | F — I/O +5·2V |
| C — MEM I/O -2·2V | G — MEM I/O +5 V |
| D — CPU 0 +5·2 | H — CPU 1 +5·2 V |

C1021

Figure D-2. Computer Cabinet - Rear View

APPENDIX E

MAINTENANCE REGISTERS

=====

This appendix consists of three maintenance registers charts on six pages.

IOU MAINTENANCE REGISTER

BYTE	BIT	(R/W)	(R/W)	(RD)	(RD)	(RD)	BIT	BYTE
		OS BOUNDS (21) 16 (041) 8	FAULT STATUS MASK (18) 16 (030) 8	OPTIONS INSTALLED (12) 16 (022) 8	ELEMENT ID (10) 16 (020) 8	STATUS SUMMARY (00) 16 (000) 8		
0	00	N/A	N/A			SAME AS BYTE 7	00	0
	01	N/A	N/A				01	
	02	N/A	N/A				02	
	03	PP 4 BV	PP 4 HV				03	
	04	PP 3 BV	PP 3 HV				04	
	05	PP 2 BV	PP 2 HV				05	
	06	PP 1 BV	PP 1 HV				06	
	07	PP 0 BV	PP 0 HV				07	
1	08	N/A	N/A			SAME AS BYTE 7	08	1
	09	N/A	N/A				09	
	10	N/A	N/A				10	
	11	PP 11 BV	PP 11 HV				11	
	12	PP 10 BV	PP 10 HV				12	
	13	PP 7 BV	PP 7 HV				13	
	14	PP 6 BV	PP 6 HV				14	
	15	PP 5 BV	PP 5 HV				15	
2	16	N/A	N/A			SAME AS BYTE 7	16	2
	17	N/A	N/A				17	
	18	N/A	N/A				18	
	19	PP 24 BV	PP 24 HV				19	
	20	PP 23 BV	PP 23 HV				20	
	21	PP 22 BV	PP 22 HV	PP 25-31			21	
	22	PP 21 BV	PP 21 HV	PP 20-24			22	
	23	PP 20 BV	PP 20 HV	PP 5-11			23	
3	24	N/A	N/A	CHAN 7		SAME AS BYTE 7	24	3
	25	N/A	N/A	6			25	
	26	N/A	N/A	5			26	
	27	PP 31 BV	PP 31 HV	4			27	
	28	PP 30 BV	PP 30 HV	3			28	
	29	PP 27 BV	PP 27 HV	2			29	
	30	PP 26 BV	PP 26 HV	1			30	
	31	PP 25 BV	PP 25 HV	CHAN 0			31	
4	32	N/A	CHAN 7 HV	CHAN 17	0	SAME AS BYTE 7	32	4
	33	N/A	6 HV	N/A	0		33	
	34	N/A	5 HV	CHAN 15	0		34	
	35	N/A	4 HV	N/A	0		35	
	36	N/A	3 HV	CHAN 13	0		36	
	37	N/A	2 HV	12	0		37	
	38	N/A	1 HV	11	1		38	
	39	N/A	CHAN 0 HV	CHAN 10	0		39	
5	40	N/A	CHAN 17 HV	CHAN 27	0	SAME AS BYTE 7	40	5
	41	N/A	N/A	26	0		41	
	42	N/A	CHAN 15 HV	25	0		42	
	43	N/A	N/A	24	1		43	
	44	N/A	CHAN 13 HV	23	0		44	
	45	OSB ADRS	12 HV	22	X		45	
	46	OSB ADRS	11 HV	21	X		46	
	47	OSB ADRS	CHAN 10 HV	CHAN 28	X		47	
6	48	OSB ADRS	CHAN 27 HV	N/A		SAME AS BYTE 7	48	6
	49	OSB ADRS	26 HV	N/A			49	
	50	OSB ADRS	25 HV	N/A			50	
	51	OSB ADRS	24 HV	N/A			51	
	52	OSB ADRS	23 HV	CHAN 33			52	
	53	OSB ADRS	22 HV	32			53	
	54	OSB ADRS	21 HV	31			54	
	55	OSB ADRS	CHAN 20 HV	CHAN 30			55	
7	56	OSB ADRS	N/A	BATTERY BACKUP		N/A N/A N/A SUM STATUS PP HALT UNCORR ERR N/A LONG WARNING	56	7
	57	OSB ADRS	N/A	N/A			57	
	58	OSB ADRS	N/A	N/A			58	
	59	OSB ADRS	RI 2/3 HV	N/A			59	
	60	OSB ADRS	CHAN 33 HV	RI 2/3			60	
	61	OSB ADRS	32 HV	RI 0/1			61	
	62	OSB ADRS	31 HV	2 PORT MUX			62	
	63	OSB ADRS	CHAN 30 HV	CC545 CONTROLLER			63	

NOTES
 N/A - BITS NOT AVAILABLE WILL ALWAYS BE ZEROES
 N/U - THESE BITS ARE NOT ASSIGNED BUT MAY BE READ OR WRITTEN
 (RD) - READ ONLY REGISTER
 (R/W) - READ/WRITE REGISTER
 MV - MASK VECTOR
 BV - BIT VECTOR
 UNANNOTATED BYTES = N/A

C1088

IOU MAINTENANCE REGISTER

BIT	(R/W)	(R/W)	(R/W)	(RD)	(R/W)	BIT	BYTE
	TEST MODE (A0) 16 (240) 8	FAULT STATUS 2 (011) 16 (201) 8	FAULT STATUS 1 (00) 16 (200) 8	STATUS (40) 16 (100) 8	ENVIRONMENT CONTROL (30) 16 (060) 8		
00			N/A			00	0
01			N/A			01	
02			N/A			02	
03			PP 4 ERR			03	
04			PP 3 ERR			04	
05			PP 2 ERR			05	
06			PP 1 ERR			06	
07			PP 0 ERR			07	
08			N/A			08	1
09			N/A			09	
10			N/A			10	
11			PP 11 ERR			11	
12			PP 10 ERR			12	
13			PP 7 ERR			13	
14			PP 6 ERR			14	
15			PP 5 ERR			15	
16			N/A			16	2
17			N/A			17	
18			N/A			18	
19			PP 24 ERR			19	
20			PP 23 ERR			20	
21			PP 22 ERR			21	
22			PP 21 ERR			22	
23			PP 20 ERR			23	
24			N/A			24	3
25			N/A			25	
26			N/A			26	
27			PP 31 ERR			27	
28			PP 30 ERR			28	
29			PP 27 ERR			29	
30			PP 26 ERR			30	
31			PP 25 ERR			31	
32			ERROR CL			32	4
33		CHAN 7 ERROR	ERROR CR			33	
34		5	FIRMWARE ERROR			34	
35		4	ERROR CH			35	
36		3	ERROR CP			36	
37		2	CVR9N ERROR			37	
38		1	N/U			38	
39		CHAN 8 ERROR	PPM DATA-IN ERR			39	
40		CHAN 17 ERROR	N/U			40	
41		N/A				41	5
42		CHAN 15 ERROR	N/U			42	
43		N/A				43	
44		CHAN 13 ERROR	N/U			44	
45		12	OSB VIOLATION			45	
46		11	OSB ADRS PE			46	
47		CHAN 18 ERROR	N/U			47	
48		CHAN 27 ERROR	CH DATA OUT ERR			48	
49		26	UNCOR CH RD ERR			49	
50		25	UNCOR CH WR ERR			50	
51		24	CH REJECT			51	
52		23	CH TAG OUT ERR			52	
53		22	CH RESP CODE ERR			53	
54		21	N/U			54	
55		CHAN 20 ERROR	N/U			55	
56		N/A	N/U			56	6
57		N/A	N/U			57	
58		N/U	N/U			58	
59		RI 2/3	N/A			59	
60		CHAN 33 ERROR	N/A			60	
61		32	BBL RECONFIG			61	
62		31	PP RECONFIG			62	
63		CHAN 30 ERROR	N/U			63	

NOTES
 N/A - BITS NOT AVAILABLE WILL ALWAYS BE ZEROES
 N/U - THESE BITS ARE NOT ASSIGNED BUT MAY BE READ OR WRITTEN
 (RD) - READ ONLY REGISTER
 (R/W) - READ/WRITE REGISTER
 UNANNOTATED BYTES=N/A

- △ IOU TEST CODES
- 00 N/U
 - 01 CHAN TO PP (GEN) CP
 - 02 PP TO CHAN (GEN) CP
 - 03 PPH TO R RSTR (GEN) CR
 - 04 PPH (CHECK) CR
 - 05 MCODE DATA (CHECK) CP
 - 06 PPH (GEN) CH
 - 07 CH FCTN (GEN) CP
 - 08 Y RSTR (GEN) CH
 - 09 A RSTR (GEN) CR
 - 0A SHF ROM (CHECK) CR
 - 0B D RSTR (GEN) CR
 - 0C P RSTR (GEN) CR
 - 0D G MUX (GEN) CR
 - 0E R TO Y (GEN) CL
 - 0F N/U
 - 10 N/U
 - 11 ADDR TO CMC (*) CL
 - 12 N/U
 - 13 DATA TO CMC (*) CH
 - 14 OSB ADRS (CHECK) CL
 - 15 TAG IN (CHECK) DC
 - 16 RESP CODE (CHECK) CH
 - 17 CHAN 15 DATA BUS (CHECK) CK
 - (*) 'HI' FORCED FOR PAR BIT AT GENERATOR (GEN) PARITY INVERTED ON GENERATION (CHECK) PARITY INVERTED ON CHECKING

C1089

IOU Maintenance Register (2 of 2)

MEM MAINTENANCE REGISTER

BYTE	BIT	(R/W)	(RD)	(RD)	(RD)	BIT	BYTE	
0	00	ENVIRONMENT CONTROL (20) (040) 15 8	OPTIONS INSTALLED (12) (022) 15 8	ELEMENT ID (10) (020) 15 8	STATUS SUMMARY (00) (000) 15 8	00 01 02 03 04 05 06 07	0	
	01	DBL PAR CHECK	0		SAME AS BYTE 7			08
	02	DBL SECDED	0	2MB				09
	03	N/A	0	4MB				10
	04	READ BYND BITS	0					11
	05	R/W CHECK	0					12
	06	N/A	0					13
	07	N/A	0					14
1	08	INVERT PAR	0	8MB		SAME AS BYTE 7	15	
	09	DBL CPU 0 PORT	0		16			
	10	DBL I/O PORT	0	12MB	17			
	11	DBL CPU 1 PORT	0		18			
	12	N/A	0	16MB	19			
	13	N/A	0	32MB	20			
	14	N/A	0	48MB	21			
	15	N/A	0	64MB	22			
2	16	N/A	0	256K CHIP	SAME AS BYTE 7	23		
	17	I/O PNM +15K	0	DEG SW UP		24		
	18	I/O PNM -15K	0			25		
	19	MEM PNM +15K	0			26		
	20	MEM PNM -15K	0	8 MB DEG SW 3		27		
	21	N/A	0	4 MB DEG SW 4		28		
	22	N/A	0	2 MB DEG SW 5		29		
	23	N/A	0			30		
3	24		0		SAME AS BYTE 7	31		
	25		0			32		
	26		0			33		
	27		0			34		
	28		0			35		
	29		0			36		
	30		0			37		
	31		0			38		
4	32		0		SAME AS BYTE 7	39		
	33		0			40		
	34		0			41		
	35		0	ELEMENT TYPE NO.		42		
	36		0			43		
	37		0			44		
	38	FORCE GOOD REBP	0			45		
	39	DBL CEL	0			46		
5	40		0		SAME AS BYTE 7	47		
	41		0			48		
	42		0			49		
	43		0	MODEL NO.		50		
	44		0			51		
	45		X	13=030		52		
	46		X	14=010		53		
	47		X			54		
6	48		0		SAME AS BYTE 7	55		
	49	+ X CP1 -2.2V	0			56		
	50	- X CP1 -2.2V	0			57		
	51	+ X CP0 -2.2V	0			58		
	52	- X CP0 -2.2V	0			59		
	53	+ X M/I -2.2V	0			60		
	54	- X M/I -2.2V	0			61		
	55	+ X CP1 -5.2V	0			62		
7	56	- X CP1 -5.2V	0		SERIAL NO.	63		
	57	+ X CP0 -5.2V	0			BYE CLK SEL (S)		
	58	- X CP0 -5.2V	0			BYE CLK SEL (F)		
	59	+ X MEM -5.2V	0			N/A		
	60	- X MEM -5.2V	0			N/A		
	61	+ X IOU -5.2V	0			UNCORR ERROR		
	62	- X IOU -5.2V	0			CORRECTED ERROR		
	63	+ X M/I + 5V	0			LONG WARNING		

NOTES

N/A - BITS NOT AVAILABLE; WILL ALWAYS BE ZEROES
 UNANNOTATED BYTES ARE N/A
 (RD) - READ ONLY REGISTER
 (R/W) - READ/WRITE REGISTER

(S) = -2% FREQUENCY MARGIN
 (F) = +2% FREQUENCY MARGIN
 + MEANS BIT IS A 'ONE' IF CONDITION IS FALSE, 'ZERO' IF CONDITION IS TRUE.

C1091

MEM MAINTENANCE REGISTER

BYTE	BIT	(R/W)	(R/W)	(R/W)	(R/W)	BIT	BYTE
0	00	UNCORRECTED ERROR LOG 2 (A8) (250)	UNCORRECTED ERROR LOG 1 (A4) (244)	CORRECTED ERROR LOG (A0) (240)	BOUNDS (21) (041)	0	0
	01	VALID ERROR	VALID ERROR	VALID SB ERROR	X 100=CPU 0		
	02	UNLOGGED ERROR	UNLOGGED ERROR	UNLOG CORR ERROR	X 010=I/O		
	03	DATA OUT PE	+ILL FUNCTION	N/A	X 001=CPU 1		
	04	DBE	+BOUNDS FAULT	N/A	N/A		
	05	+TAGOUT PE	2ND PASS	N/A	N/A		
	06	PORT CODE (SEE PORT CODE A0)	PORT CODE (SEE PORT CODE A0)	N/A	N/A		
	07	0	REFRESH PORT	N/A	N/A		
1	08	N/A	N/A	N/A	N/A	1	1
	09	ADDRESS 38	ADDRESS 38	ADDRESS 38			
	10						
	11						
	12						
	13						
	14						
	15						
2	16					2	2
	17						
	18						
	19						
	20						
	21						
	22						
	23						
3	24					3	3
	25						
	26						
	27						
	28						
	29						
	30						
	31	ADDRESS 60	ADDRESS 60	ADDRESS 60			
4	32	+DATA OUT PE 0	+PE DATA PAK 1	SYNDROME BITS 0	N/A	4	4
	33	1	+PE DATA PAK 2	1	N/A		
	34	2	+PE DATA PAK 3	2	N/A		
	35	3	+PE DATA PAK 4	3	N/A		
	36	4		4			
	37	5		5			
	38	6		6			
	39	+DATA OUT PE 7		SYNDROME BITS 7			
5	40					5	5
	41						
	42						
	43		+TAG IN PE				
	44		+MARK PE				
	45		+ADDRESS PE 4				
	46		+ADDRESS PE 5				
	47						
6	48		+ADDRESS PE 6			6	6
	49		+ADDRESS PE 7				
	50		FUNCTION BITS 0				
	51		FUNCTION BITS 1				
	52		FUNCTION BITS 2				
	53		FUNCTION BITS 3				
	54		FCTN CODE PAR				
	55		MARK BIT PAR				
7	56		MARK BITS 0			7	7
	57		1				
	58		2				
	59		3				
	60		4				
	61		5				
	62		6				
	63		MARK BITS 7				

NOTES
 N/A - BITS NOT AVAILABLE, WILL ALWAYS BE ZEROES
 UNANNOTATED BYTES ARE N/A
 (RD) - READ ONLY REGISTER
 (R/W) - READ/WRITE REGISTER
 (S) = -2% FREQUENCY MARGIN
 (F) = +2% FREQUENCY MARGIN
 + MEANS BIT IS A 'ONE' IF CONDITION IS FALSE, 'ZERO' IF CONDITION IS TRUE

C1090

MEM Maintenance Register (2 of 2)

BYTE	BIT	(R/W)	(R/W)	(RD)	(RD)	(RD)	BIT	BYTE
		CS ADDRESS REGISTER (31) (061) 8 16	ENVIRONMENT CONTROL (30) (060) 8 16	OPTIONS INSTALLED (12) (022) 8 16	ELEMENT ID (10) (020) 8 16	STATUS SUMMARY (00) (000) 8 16		
0	00		N/A			SAHE	00	0
	01		N/A			AS	01	
	02					BYTE 7	02	
	03		CS MICRO STEP ENBL				03	
	04						04	
	05						05	
	06						06	
	07						07	
1	08					SAHE	08	1
	09					AS	09	
	10		PMM +15K			BYTE 7	10	
	11		PMM -15K				11	
	12						12	
	13						13	
	14						14	
	15						15	
2	16					SAHE	16	2
	17					AS	17	
	18					BYTE 7	18	
	19						19	
	20						20	
	21						21	
	22						22	
	23						23	
3	24		PFS ENBL	N/A		SAHE	24	3
	25		MAP RMA ENBL	N/A		AS	25	
	26		MAP FILE 0 ENBL	N/A		BYTE 7	26	
	27		1	DUAL CPU			27	
	28		2	CONCURRENT I/O			28	
	29		MAP FILE 3 ENBL	CY170 MODE OPT			29	
	30		INSTR RTY ENBL	SECDED C 0			30	
	31		INSTR STEP ENBL	PRF INSTALLED			31	
4	32		N/U		0	SAHE	32	4
	33		TEST MODE ENBL		0	AS	33	
	34				0	BYTE 7	34	
	35		SEE NOTE 2		0		35	
	36		CS BKPT ENBL		0		36	
	37		CS SWEEP ENBL		0		37	
	38		N/A		0		38	
	39		N/A		0		39	
5	40				0	SAHE	40	5
	41				0	AS	41	
	42				0	BYTE 7	42	
	43				1		43	
	44				0		44	
	45				0		45	
	46				X		46	
	47				X		47	
6	48				0	SAHE	48	6
	49		N/A		0	AS	49	
	50		N/A		0	BYTE 7	50	
	51		N/A		0		51	
	52				0		52	
	53		S REGISTER DATA (MICRAND ADRS CS)		0		53	
	54				0		54	
	55				0		55	
7	56				0	CY100 MON MODE	56	7
	57				0	SHORT WARNING	57	
	58				0	PROCESSOR HALT	58	
	59				0	UNCORR ERROR	59	
	60				0	CORRECTED ERROR	60	
	61				0	LONG WARNING	61	
	62				0		62	
	63				0		63	

NOTES

N/A - BITS NOT AVAILABLE, WILL ALWAYS BE ZEROES
 UNANNOTATED BYTES ARE N/A
 N/U - THESE BYTES ARE NOT ASSIGNED BUT MAY BE READ OR WRITTEN
 (RD) - READ ONLY REGISTER
 (R/W) - READ/WRITE REGISTER
 RPTR (11) - PROCESSOR I D BITS 0-62=0
 16 BIT 63=0 FOR CPU-0, 1 FOR CPU-1

NOTE 2 - DSBL CORR ERROR TO P1 66

CPU Maintenance Register (1 of 2)

CPU MAINTENANCE REGISTER

BYTE	BIT	(R/W) PROCESSOR TEST MODE (A0) (240) 16 8	(R/W) MAP CORR ERROR LOG (93) (223) 16 8	(RD) CNTL STORE ERROR LOG (81) (201) 16 8	(R/W) PROCESSOR FAULT STATUS (80) (700) 16 8	(R/W) CS BKPT ADDRESS (32) (062) 16 8	BIT	BYTE
0	00						00	0
	01						01	
	02						02	
	03						03	
	04						04	
	05						05	
	06						06	
	07						07	
1	08						08	1
	09						09	
	10						10	
	11						11	
	12						12	
	13						13	
	14						14	
	15						15	
2	16						16	2
	17						17	
	18						18	
	19						19	
	20						20	
	21						21	
	22						22	
	23						23	
3	24						24	3
	25						25	
	26						26	
	27						27	
	28						28	
	29						29	
	30						30	
	31						31	
4	32						32	4
	33						33	
	34						34	
	35						35	
	36						36	
	37						37	
	38						38	
	39						39	
5	40						40	5
	41						41	
	42						42	
	43						43	
	44						44	
	45						45	
	46						46	
	47						47	
6	48	N/U					48	6
	49	N/U					49	
	50	N/U					50	
	51	N/U					51	
	52	TEST CODE					52	
	53	0000 - 0F00					53	
	54						54	
	55						55	
7	56	N/U	FILE 0 PE				56	7
	57	N/U	FILE 0 PE				57	
	58	N/U	FILE 1 PE				58	
	59	N/U	FILE 1 PE				59	
	60	TEST CODE	FILE 2 PE				60	
	61	0000 - 000C	FILE 2 PE				61	
	62		FILE 3 PE				62	
	63		FILE 3 PE				63	

NOTES

N/A - BITS NOT AVAILABLE, WILL ALWAYS BE ZEROES
 N/U - THESE BITS ARE NOT ASSIGNED BUT
 MAY BE READ OR WRITTEN
 (RD) - READ ONLY REGISTER
 (R/W) - READ/WRITE REGISTER

⚠ 0000 - N/U
 0900 - INVERT MEM FCN PARITY
 0A00 - INVERT MEM TAG PARITY
 0B00 - INVERT MARK PARITY
 0C00 - INVERT DATA/ADRS PARITY BYTES 0, 1
 0D00 - INVERT DATA/ADRS PARITY BYTES 2, 3
 0E00 - INVERT DATA/ADRS PARITY BYTES 4, 5
 0F00 - INVERT DATA/ADRS PARITY BYTES 6, 7

⚠ 0000 - INVERT PARITY ON PSR MUX
 0009 - INVERT PARITY ON FP BRANCH INDEX ROM
 000A - INVERT PARITY ON FP TRAP INDEX ROM
 000B - INVERT PARITY ON MAINT CHAN DATA
 000C - INVERT PARITY ON SADDR LATCH DATA

RGTR (11) - PROCESSOR I D
 16 BITS 0-62 = 0
 BIT 63 = 0 FOR CPU 0,
 1 FOR CPU 1

C1086

NOTE 3 - ARVI PE 0-7, 32-39
 NOTE 4 - ARVI PE 8-15, 40-47
 NOTE 5 - ARVI PE 16-23, 48-55
 NOTE 6 - ARVI PE 24-31, 56-63
 NOTE 7 - UNCORR MEM WR ERROR

NOTE 8 - SHIFT ROM PE OR SHIFTER INPUT
 NOTE 9 - UNCORR MEM RD ERROR
 NOTE 10 - MAC/RD PE DS
 NOTE 11 - MEM RESPONSE TIME OUT
 NOTE 12 - BDP ADDER/RUB, DATA ROM/RKB

NOTE 13 - IMMED ROM PE

CPU Maintenance Register (2 of 2)

APPENDIX F

PANEL MAP

PANEL MAP

F

PANEL MEM

01	DP	MASTER CLOCK (1DPO)
02		
03		
04		
05		
06		
07	DC	MEMORY CONTROL
08	DD	DATA PAK
09	DD	DATA PAK
10	DD	DATA PAK
11	DD	DATA PAK
12	DA	MEMORY ADDRESS (ODPH)
13	DP	MEMORY ADDRESS (ODPH)
14	DP	MEMORY ADDRESS (ODPH)
15	DP	MEMORY ADDRESS (ODPH)
16	DP	MEMORY ADDRESS (ODPH)
17	DP	MEMORY ADDRESS (ODPH)
18	DP	MEMORY ADDRESS (ODPH)
19	DP	MEMORY ADDRESS (ODPH)
20	DP	MEMORY ADDRESS (ODPH)
21	DP	MEMORY ADDRESS (ODPH)
22	DP	MEMORY ADDRESS (ODPH)
23	DP	MEMORY ADDRESS (ODPH)
24	DP	MEMORY ADDRESS (ODPH)
25	DP	MEMORY ADDRESS (ODPH)
26	DP	MEMORY ADDRESS (ODPH)
27	DP	MEMORY ADDRESS (ODPH)
28	DP	MEMORY ADDRESS (ODPH)

PANEL IOU

01	DP	MAINT ACCESS CONTROL
02		
03		
04		
05		
06	CL	CENTRAL MEM ADRS
07	CG	MAINT RGRTR
08	CG	CHANNEL 14,15,17
09	CH	CHANNEL (SIX)
10	CH	CHANNEL (SIX)
11	CP	P P
12	CP	P P
13	CP	P P
14	CP	P P
15	CH	CHANNEL (SIX)
16	CH	CHANNEL (SIX)
17	CG	CHANNEL N DSC
18	CM	P P MEM (TEN)
19	CM	P P MEM (TEN)
20	CK	T P M DST
21		
22		
23		
24		
25		
26		
27		
28		

C1008-5

PANEL CPO

01	TH	LOWER IMMEDIATE INSTR PIPE
02	TG	UPPER IMMEDIATE INSTR PIPE
03	DF	RGR FILE BITS 0-15
04	DF	RGR FILE BITS 16-31
05	DF	RGR FILE BITS 32-47
06	DF	RGR FILE BITS 48-63
07	TS	SHIFT/L ADDER
08	TM	MAIN CONTROL
09	DE	CM RESP, TAG, DEBUG, FP EXEC, AD/BD MUX SELECT
10	TC	FLOATING POINT S ADDER
11	TD	NORMALIZER & CONTROL RINGS
12	TR	CONTROLS - BDP, BADDR, STREAMING
13	TV	BDP DATA
14	TU	PROCESS RGR TRAPS (HARD RGRTRS)
15	GL	LENGTH YKW ARVI 1
16	DR	CONT STORE 0-7,32-39 P MAINT RGRTRS
17	DR	CONT STORE 8-15,40-47 ARVI 2 P MAINT RGRTRS
18	DR	CONT STORE 16-23,48-55 ARVI 2 P MAINT RGRTRS
19	DR	CONT STORE 24-31,56-63 ARVI 2 P MAINT RGRTRS
20	DS	CONT STORE 64-83 NEXT ADRS LOGIC
21	GA	RNI, MAP CONTROL
22	DW	MAP DATA 32-39
23	DW	MAP DATA 40-47
24	DW	MAP MISC VALIDITY CHECKING
25	DW	MAP DATA 48-55
26	DW	MAP DATA 56-63
27		
28		

Panel Map - Model 810/830

01	DP	MASTER CLOCK (1DP0)
02		
03		
04		
05		
06	DC	MEMORY CONTROL
07		
08	DD	DATA PAK
09	DD	DATA PAK
10	DD	DATA PAK
11	DD	DATA PAK
12	DA	MEMORY ADDRESS (ODPH)
13	DP	MEMORY ADDRESS (ODPH)
14	DP	MEMORY ADDRESS (ODPH)
15	DP	MEMORY ADDRESS (ODPH)
16	DP	MEMORY ADDRESS (ODPH)
17	DP	MEMORY ADDRESS (ODPH)
18	DP	MEMORY ADDRESS (ODPH)
19	DP	MEMORY ADDRESS (ODPH)
20	DP	MEMORY ADDRESS (ODPH)
21	DP	MEMORY ADDRESS (ODPH)
22	DP	MEMORY ADDRESS (ODPH)
23	DP	MEMORY ADDRESS (ODPH)
24	DP	MEMORY ADDRESS (ODPH)
25	DP	MEMORY ADDRESS (ODPH)
26	DP	MEMORY ADDRESS (ODPH)
27	DP	MEMORY ADDRESS (ODPH)
28	DP	MEMORY ADDRESS (ODPH)

01	TH	LOWER IMMEDIATE INSTR PIPE
02	TG	UPPER IMMEDIATE INSTR PIPE
03	DF	RGR FILE BITS 0-15
04	DF	RGR FILE BITS 16-31
05	DF	RGR FILE BITS 32-47
06	DF	RGR FILE BITS 48-63
07	TS	SHIFT/L ADDER
08	TM	MAIN CONTROL
09	DE	CM RESP, TAG, DEBUG, FP EXEC, AD/BD MUX SELECT
10	TC	FLOATING POINT S ADDER
11	TD	NORMALIZER & CONTROL RINGS
12	TR	CONTROLS - BDP, BADDER, STREAMING
13	TV	BDP DATA
14	TU	PROCESS RGR TRAPS (HARD RGRS)
15	6L	LENGTH YKW ARVI 1
16	DR	CONT STORE 0-7,32-39 P MAINT RGRS
17	DR	CONT STORE 8-15,40-47 ARVI 2 P MAINT RGRS
18	DR	CONT STORE 16-23,48-55 ARVI 2 P MAINT RGRS
19	DR	CONT STORE 24-31,56-63 ARVI 2 P MAINT RGRS
20	DS	CONT STORE 64-83 NEXT ADRS LOGIC
21	6A	RNI, MAP CONTROL
22	DW	MAP DATA 32-39
23	DW	MAP DATA 40-47
24	UX	MAP MISC VALIDITY CHECKING
25	DW	MAP DATA 48-55
26	DW	MAP DATA 56-63
27		
28		

PANEL MEM

10		
11		
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PANEL IOU

PANEL CPO

PANEL CPI (830 ONLY)

Panel Map - Model 830 With Dual CPU Option

C1008

APPENDIX G

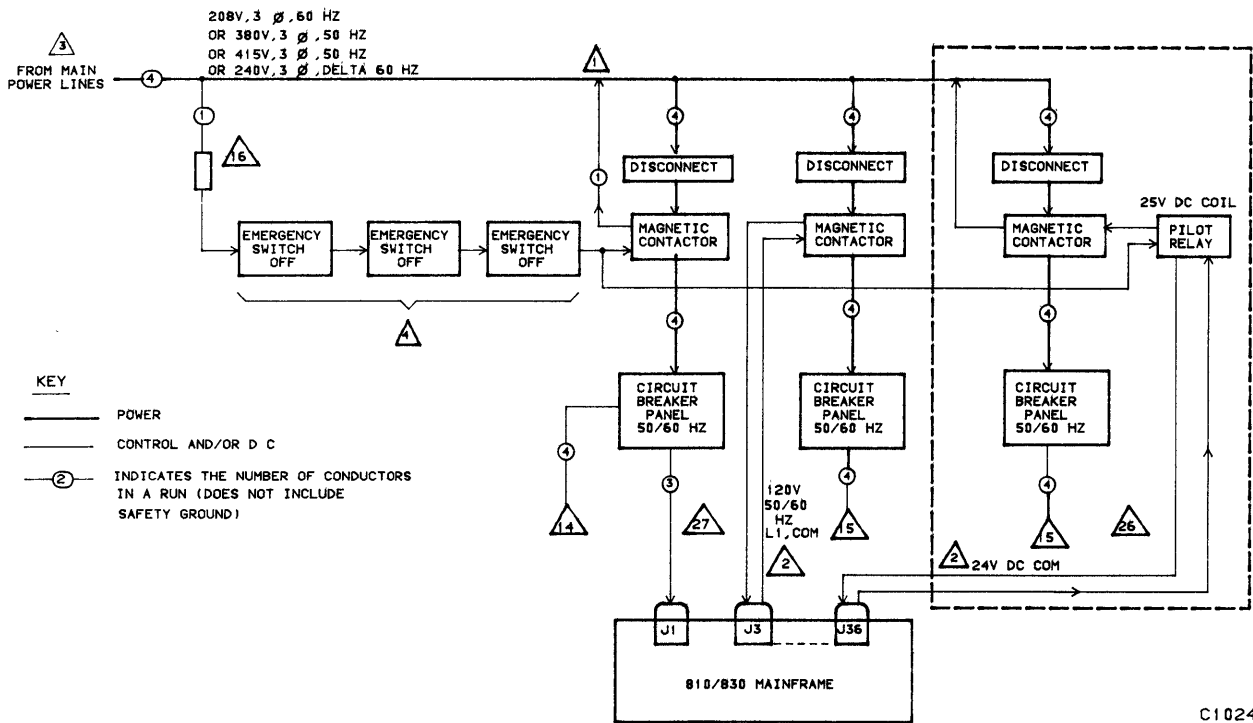
POWER DISTRIBUTION DIAGRAMS

This appendix contains three power distribution diagrams followed by a common set of notes.

50/60 Hz only

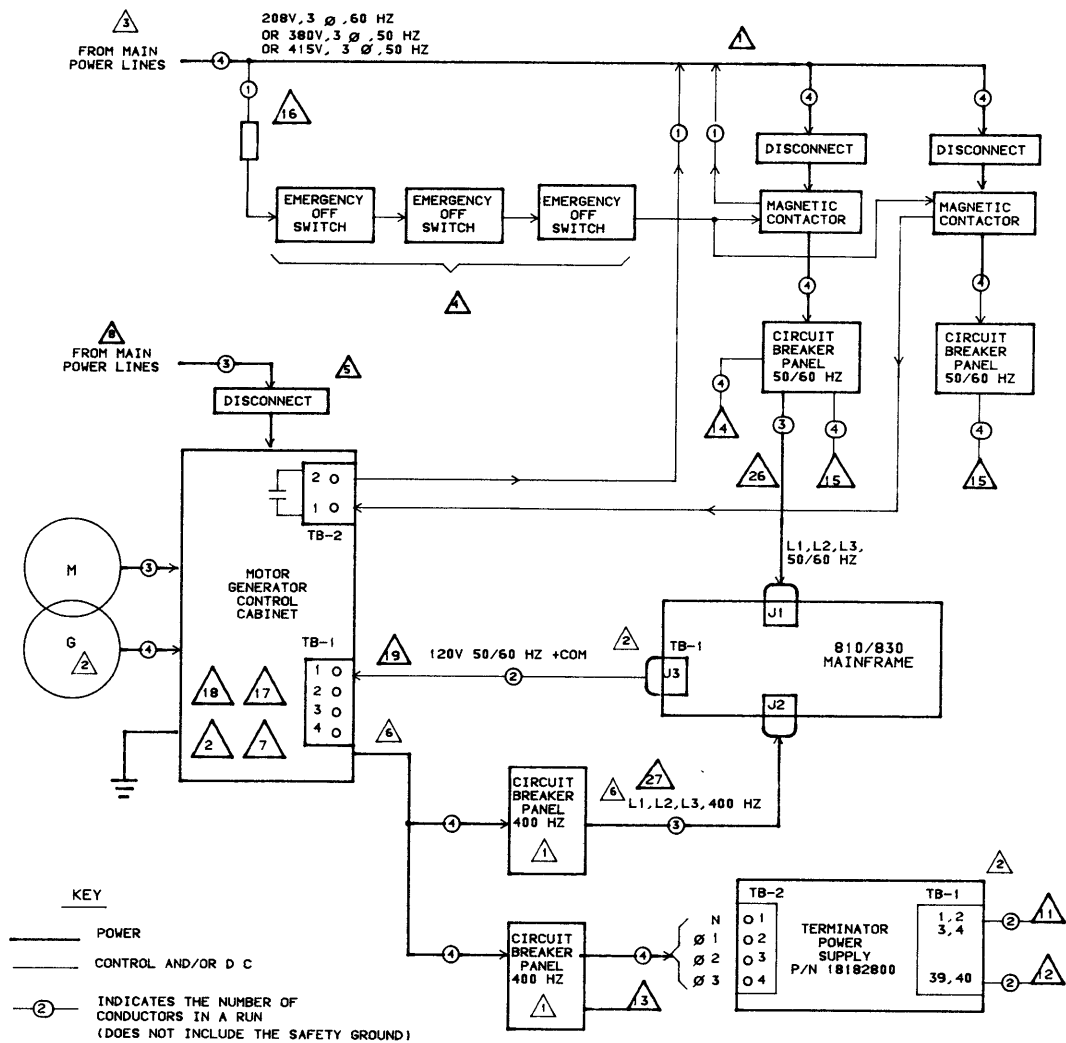
50/60 Hz and 25 kVA or larger 400 Hz with SPCP

25 kVA or larger 400 Hz with SPCP



C1024

Figure G-1. Power Distribution Diagram 50/60 Hz Only



C1025

Figure G-2. Power Distribution Diagram 50/60 Hz and 12.5 kVA MG Set

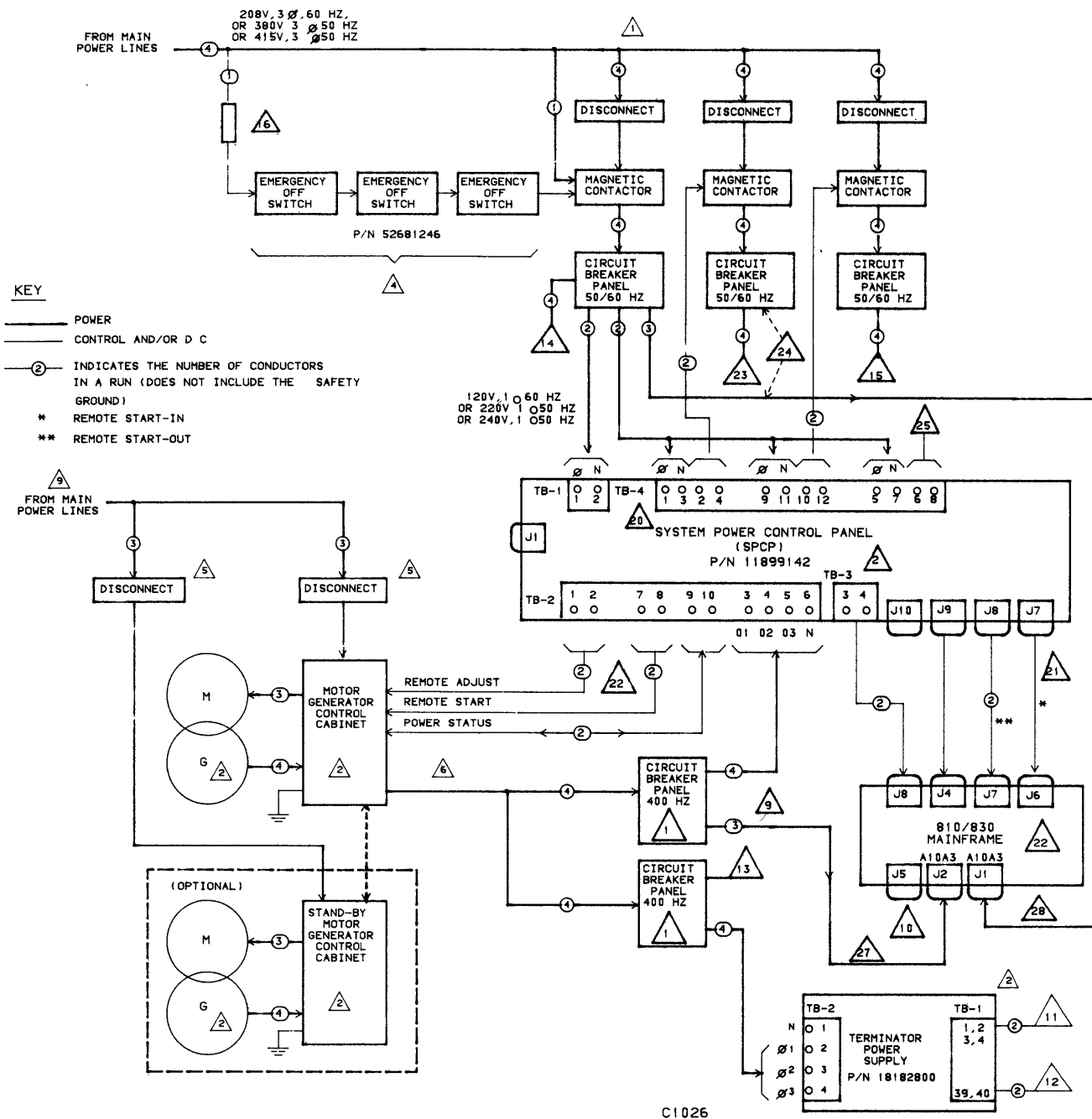


Figure G-3. Power Distribution Diagram 50/60 Hz and 25 kVA or Larger MG Set

1. Circuit breakers, breaker panels, magnetic contactors, main disconnect switches, junction boxes, and associated wiring must be customer furnished and installed prior to computer equipment installation.
2. Part furnished by Control Data and installed by customer.
3. All wiring must be in accordance with applicable local and national codes and must be labeled for identification of source and termination.
4. Emergency off switches should be located approximately 1.5 m (5 ft) from the floor with one near each exit and shall be wired in series.
5. Size of MG input power disconnect and motor power wiring shall be based on motor power characteristics. Refer to Site Preparation section 4 Manual.
6. Voltage loss between MG and computer equipment must not exceed two percent of the MG rated output.
7. MG output is 12.5 kVA, 208 V, 3 phase, 400 Hz.
8. Power input for 12.5 kVA MG: 208 V, $\pm 10\%$, 3 phase, 60 Hz.
9. Power input for 25 kVA MG: 208/220/440/460 V $\pm 10\%$, 3-phase, 60 Hz
or: 398 V, $\pm 15\%$, 3-phase, 50 Hz
10. J5 is terminated.
11. ± 20 -V dc terminator power to 405 card reader (if used).
12. ± 20 -V dc terminator power to 580-XXX line printer (if used).
13. To associated peripheral equipments that require 400-Hz power.
14. Provides 50/60-Hz power to items which should always remain on except when the wall-mounted emergency off switch is pulled (that is, test equipment service outlets).
15. Provides 50/60-Hz power to equipment that requires power when 400-Hz is available.
16. Fuse, customer furnished and installed.
17. MG must be placed in close proximity to the mainframe because 400-Hz voltage adjustment is done at MG cabinet front panel.
18. When 400-Hz load is increased or decreased, MG output may have to be readjusted to 208 V.

Notes for Power Distribution Diagrams (1 of 2)

19. Connections:

<u>M-G</u>	to	<u>Mainframe</u>
TB1-1		J3
TB1-2		J3
TB1-3		J3
TB1-4		J3

20. Numbered to show wiring connections; TB-4 terminals are actually numbered sequentially.
21. SPCP should be located approximately 1.5 m (5 ft) from the floor. Unit is supplied with 15.2 m (50 ft) monitor cables P/N 53615357. Connect between SPCP and mainframe as indicated. The SPCP must be installed close enough to the mainframe that the 15.2 m monitor cables will reach, taking into consideration the cable drop lengths from the SPCP to the room floor (below the raised floor) and the rise back up and 457 mm (18 in) into the mainframe.
22. MG remote adjust, remote start, and power status wiring between SPCP and MG control cabinet must have separate raceways (three independent runs of two each) unless adequately shielded from one another by use of conduit, greenfield, or braided shield within the power cables.
23. Same as 14 except breaker panel may be shut off via the service switch within the SPCP.
24. 50/60-Hz power to the mainframe may be supplied from this panel also.
25. Circuit may be used to switch control power for an additional magnetic contactor. The circuit may be adjusted to come on 0 to 5 minutes prior to 400-Hz power and to stay on 0 to 5 minutes after 400-Hz power is removed.
26. Circuitry shown inside dashed line is an alternate remote control to that shown connected to J3. This alternate circuit is used if 120 V coil magnetic contactors are not available (that is at 50-Hz sites).
27. Main power supply to the logics (-5.2 V, -2.2 V, +5.0 V).
28. Power to auxiliary power (+5 V, -12 V, +12 V), auxiliary battery charger, blowers, and so forth.

Notes for Power Distribution Diagrams (2 of 2)

APPENDIX H

SOFTWARE TESTS AND LIMITS

 This appendix provides a table showing software tests and limits.

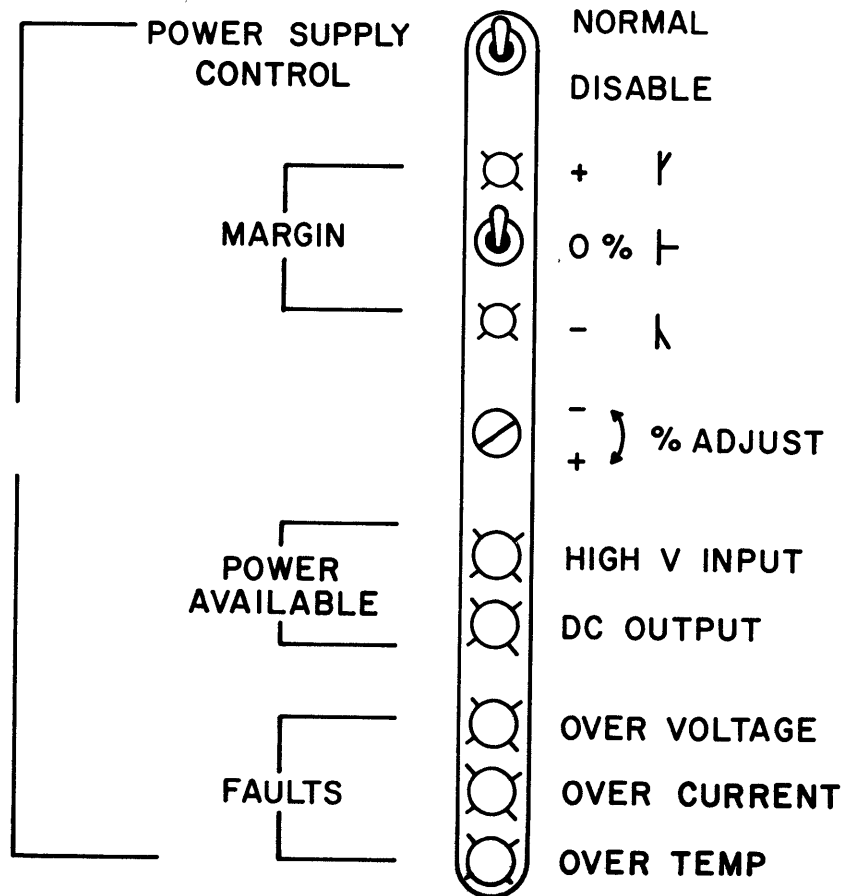
Test	Time Limits	Command Buffer	Parameters Selected at Stop	Comments
IOU MARGINS				
FII1	1 Pass	FII10	Wide: CM,O,S or SWM (set wide margin)	Load microcode after completion of test.
TPM1	1 Pass	TPM10	Narrow: CM,O,F or SNM (set narr marg)	Two port mux test.
CM MARGINS				
CMT1	1 Pass	CMT10	Wide: EP,X,123,1 Narrow: EP,X,123,2	X=PP number on which test is loaded. Display command buffer CMT10 by typing AE,CMT10.
CMEM	5 Min	CMEM1	Narrow: CM,1,F Wide: CM,1,S	
CPU MARGINS				
FIS1 *	1 Pass (15 Min)	FIS10	Wide: CM,2,S or SWM Narrow: CM,2,F or SNM	
RCT1	5 Min	RCT11		
RCT2	5 Min	RCT21		
170 MODE	5 Min	EXC1	(CU8, CT8, MY1, FS8)	C.B. EXC1 will load CU8, CT8, MY1, and FS8 tests.
170 MODE	5 Min		L,IMC,BGK,FDT,LAT	Other tests can be loaded by typing L,XXX,XXX,XXX,XXX (up to 4 tests)
170 MODE	5 Min		L,POP,RTJ,FM2,CM6	
FCT1	1 Pass	FCT11		
FCT2	2 Min	FCT21		
FCT3	1 Pass (20 Min)	FT3G01 FT3G11 FT3G21 FT3G31 FT3S11 FT3S21 FT3BD1 FT3FP1	EB, A04E, 20 (cr) EB, A04E, 20 (cr) EB, A04E, 20 (cr) EB, A04E, 28 (cr)	To speed up the tests, turn the display screen off.
FCT5	1 Pass	FCT51		
FCT9	1 Pass	FCT91		Run clear 1 first
EXCH	1 Pass	EXCH1		

* Available only to customers with a Control Data Maintenance Services agreement.

APPENDIX I

SWITCHES AND INDICATORS

This appendix illustrates the power supply switches and indicators.



C1017

Figure I-1. Typical Logic Power Supply Control

MARK ANY LIT RED LIGHTS AND TRAPPED CIRCUIT BREAKERS.

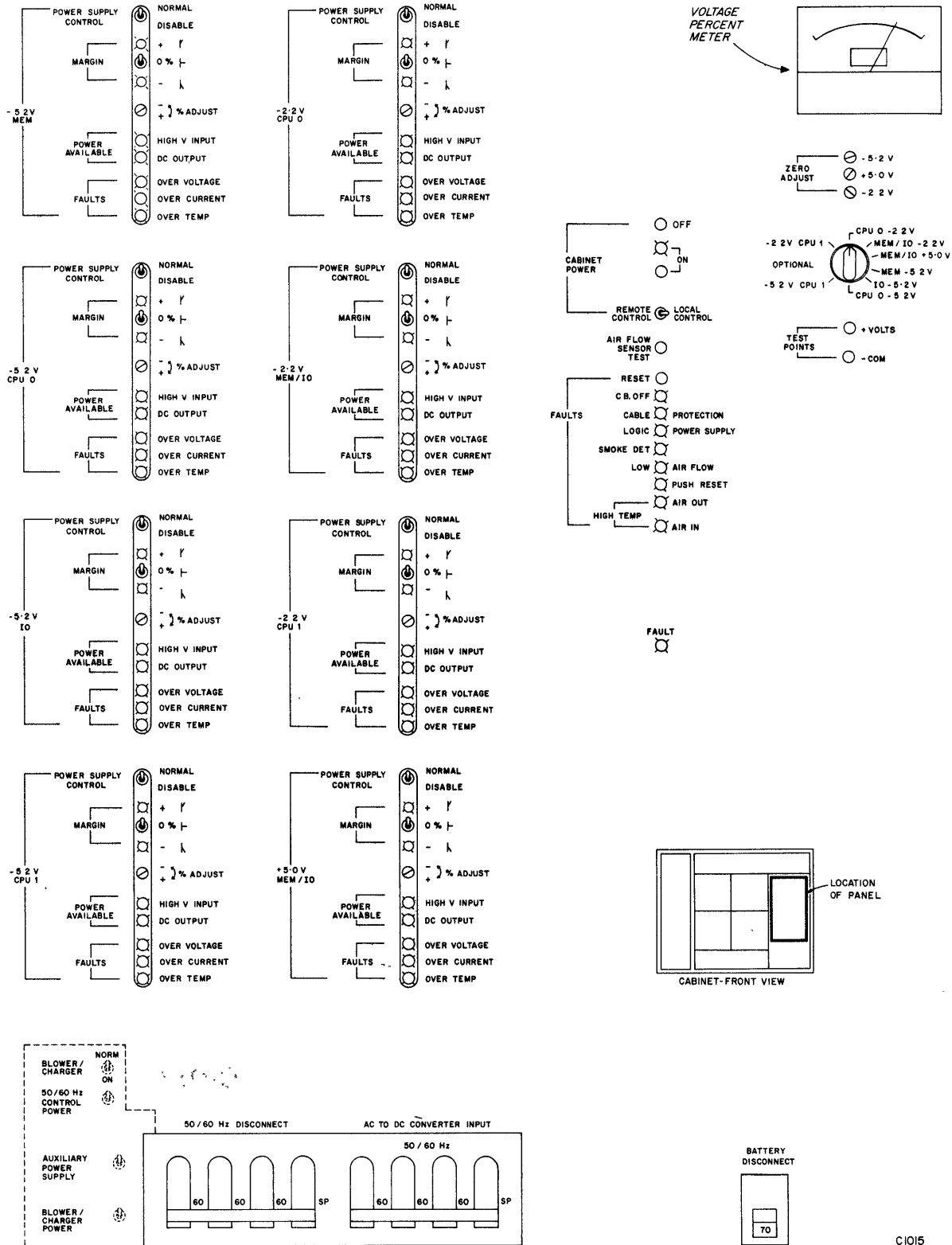
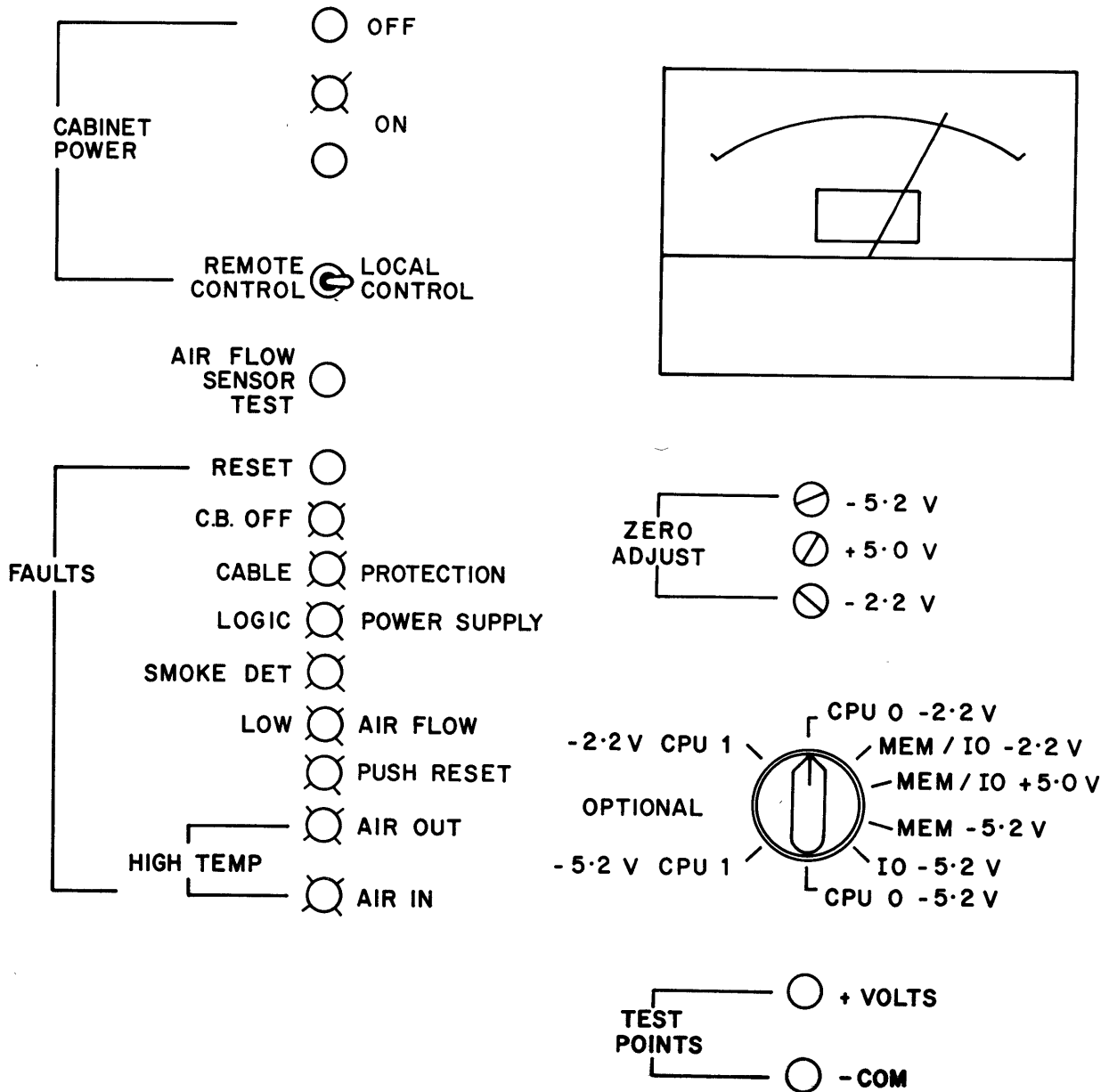


Figure I-2. Power Supply Switches and Indicators



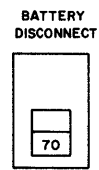
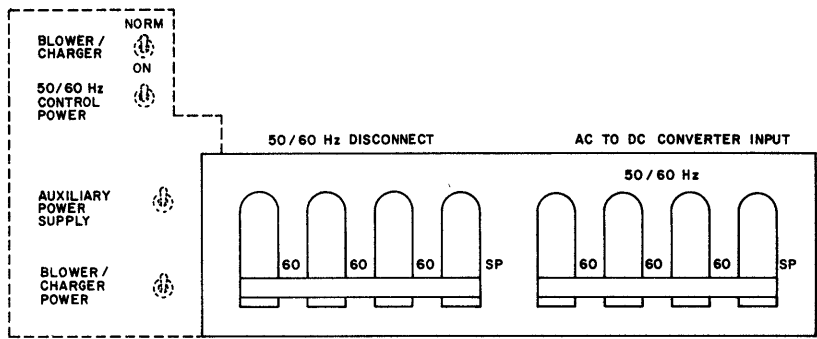
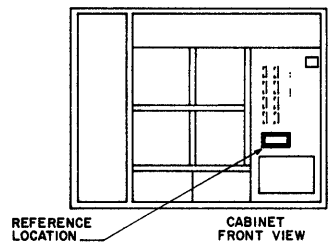
FAULT



C1018

C1019

Figure I-3. Fault Indicators and Voltage Percent Meter



C1016

Figure I-4. Circuit Breakers

APPENDIX J

SYSTEM POWER CONTROL PANEL SETTINGS

=====

Use the following procedures to apply power to the MG set from the SPCP in the event voltage adjustments are necessary.

NOTE

When the MG is to be powered up without the mainframe, jump J7 to J8 with a jumper wire before applying power.

MG SET POWER APPLICATION

This procedure requires a digital voltmeter (Fluke model 8020A or equivalent).

- ___ 1. Set 50/60-Hz and 400 Hz wall mounted circuit breaker for SPCP to ON.
- ___ 2. Press and release REMOTE ENABLE/LOCAL START button on SPCP.
- ___ 3. Allow MG set to stabilize for at least one minute.
- ___ 4. Adjust MG set output voltage as follows:
 - ___ a) Locate TB2 inside the SPCP. See figure J-1.
 - ___ b) Connect the voltmeter between TB2-6 (neutral) and successively TB2-3/4/5 (three phases). Record voltage readings:

TB2-3	_____
TB2-4	_____
TB2-5	_____
 - ___ c) Circle the phase voltage reading which is between the other two phases in step 4b above. Connect the voltmeter between this and TB2-6.
 - ___ d) Adjust VOLTAGE ADJUST setscrew on front of SPCP until the voltmeter indicates 120 V ac.
- ___ 5. Read 400 Hz VOLTAGE meter in front of SPCP. Adjust potentiometer R95 (figure J-1) in SPCP until meter indicates 0 percent.

SPCP POWER APPLICATION

Use the following procedures to apply 50/60-Hz power to the SPCP.

- ___ 1. Apply 50/60 Hz power as follows:
 - ___ a) Set LOCAL/REMOTE switch on left side of SPCP to LOCAL.
 - ___ b) Set SYSTEM DISCONNECT switch on left side of SPCP to ON.
 - ___ c) Set wall-mounted 50/60 Hz circuit breaker for SPCP to ON.

- ___ 2. Verify that the following conditions are present:
 - ___ a) The SPCP fan is running.
 - ___ b) POWER AVAILABLE indicator is lit.
 - ___ c) Fuse indicators F1, F2, F3 are not lit.

NOTE

If conditions are not as listed, check all electrical connections to ensure that they are correct.

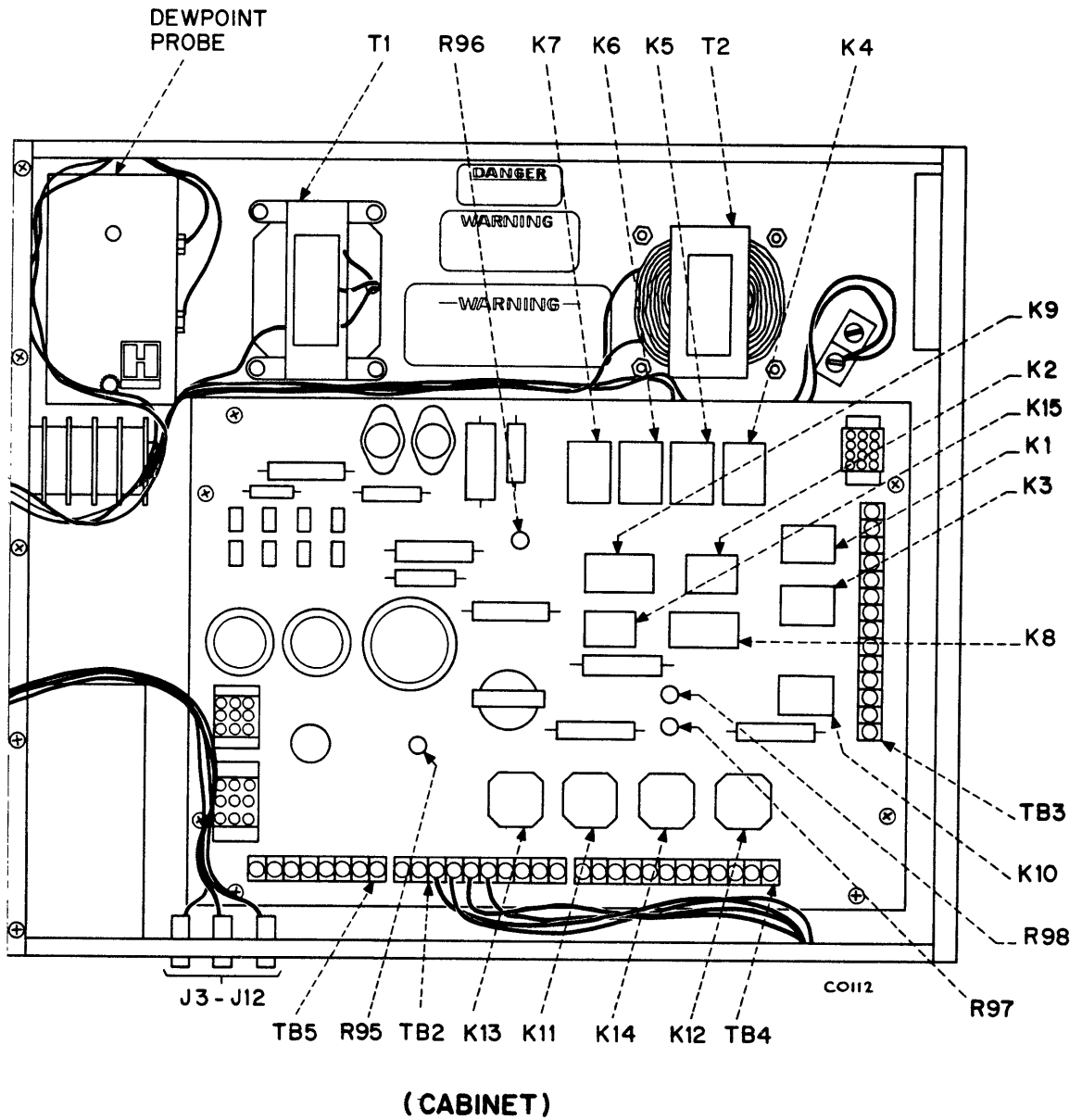


Figure J-1. System Power Control Panel - Internal View

S1 - Remote enable local start switch
S2 - Stop switch
M1 - 400 Hz voltage meter
M2 - Running hours meter
T1 - Transformer #1
T2 - Transformer #2
R1 - 120V, Voltage adjust variable resistor
R95 - 0 percent adjust variable resistor
R96 - 2.5 second adjust variable resistor
R97 - 2 minutes adjust variable resistor
R98 - 0 minute adjust variable resistor
TB2 - Terminal Block
TB3 - Terminal Block
TB4 - Terminal Block
TB5 - Terminal Block
K1 - Relay
K2 - Relay
K3 - Relay
K4 - Relay
K5 - Relay
K6 - Relay
K7 - Relay
K8 - Relay
K9 - Relay
K10 - Relay
K11 - Relay
K12 - Relay
K13 - Relay
K14 - Relay
K15 - Relay
L1 - Power on indicator light
L2 - Power available indicator light
F1 - 0.125A fuse
F2 - 5A fuse
F3 - 0.5A fuse

Notes for Figure J-1. System Power Control Panel - Internal View

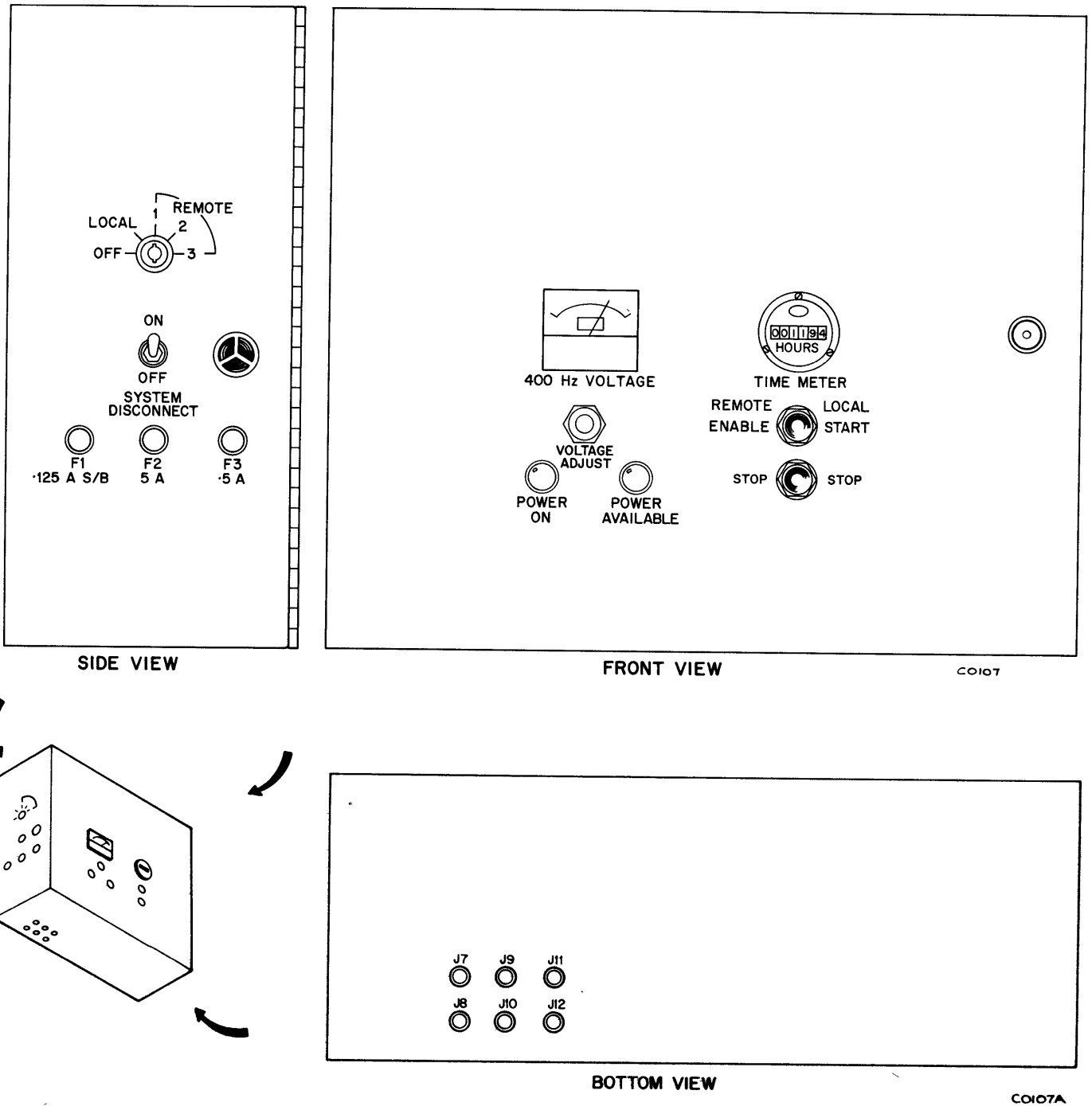


Figure J-2. SPCP-Front, Side, and Bottom Views

Local/Remote Setting on SPCP and Cabinet for Power On

	SPCP LOCAL/ REMOTE SETTING	CABINET LOCAL/ REMOTE SETTING
12.5-kVA MG system or systems sharing an MG controlled by another SPCP or TMCP (no SPCP)	Not applicable	LOCAL MG and mainframe are powered on by pressing CABINET POWER ON switch on 50/60-Hz Power Control Box.
25-kVA M-G system (with SPCP)	LOCAL Set SYSTEM DISCONNECT Switch to ON. Power on MG by pressing REMOTE ENABLE/ LOCAL START switch on SPCP, and holding for two seconds.	LOCAL After MG is powered on for approximately 1 minute, power on mainframe by pressing CABINET POWER ON switch on 50/60-Hz Power Control Box.
25-kVA MG system (with SPCP)	LOCAL Set SYSTEM DISCONNECT switch to ON. Power on MG by pressing REMOTE ENABLE/ LOCAL START switch and holding for two seconds.	REMOTE System (mainframe and MG) is powered on by pressing REMOTE ENABLE/ LOCAL START switch on SPCP.
25-kVA M-G system (with SPCP)	REMOTE 1/2/3 (See Hardware Operator's Guide for description.)	REMOTE

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