No.
 LD-8258A

 DATE
 MAY 7. 1996

TECHNICAL LITERATURE

FOR

TFT-LCD module

MODEL NO. LQ10'D42

TENTATIVE

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SHARP CORPORATION TENRI LIQUID CRYSTAL DISPLAY GROUP LCD PRODUCTS DEVELOPMENT CENTER

RECORDS OF REVISION'

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MODEL "No: LQ10D42

SPEC No : LD-8258

DATE	REVISED		SUMMARY	NOTE
	No.	PAGE		
1996. 3.12	-	-		1 st Issue
<u>_199657</u>	A	Cover	Change :Department of issue	
	[2	Addition:"Haze_value=28%"	<u>-</u>
		6	Change :Kick-off voltage	
			1100→950Vrms(Ta=25℃)	
		~ - ~ -	1300→1400Vrms (Ta=0℃)	
				-
				
				
				
<u>-</u>				
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L	1	1		

1. Application

This specification applies to color TFT-LCD module, LQ10D42.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a 640X3 X480 dots pane! with 262,144 colors by supplying 18 bit data signal (6bit/color), four timing signals, +5V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The **TFT-LCD** panel used for this module is a low-reflection and higher-color-saturation type. Therefore, this module is also suitable for the multimedia use.

This module is the type of wide viewing angle and high brightness($300cd/m^2$). Backlight-driving DC/AC inverter is not built in this module.

3. Imechanical Specifications

Parameter	Specifications	I Unit
Display size	26 (10.4") Diagonal	cm
Active area	211.2(H) X158,4(V)	mm
Pixel format	640(H) × 480(V)	pixel
	$(1 \text{ pixel} = \mathbf{R} + \mathbf{G} + \mathbf{B} \text{ dots})$	
Pixel pitch	0.330(H) X0.330(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	265.0(W)×195.0(H)×11.0(D)	mm
Mass	TBD	g
Surface treatment	Anti-glare and hard-coating 2H	-
	Haze value =28 %	

* 1.Note: excluding backlight cables.

Outline dimensions is **shown** in Fig. 1

4. Input **Terminals**

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4-1. **TFT-LCD** panel driving

CN1 Used	conncctor:DF9BA-31 P- IV (Hirose Electric Co., Ltd.)
31	Corresponding connector: DF9 -31 S-1 V (//)
30	DF9A-3 1S-1V(")

CN I pin arrangement from module surface (Transparent view) DF9B-31S-1V (")DF9M-3 |S-|V (")

	(DF9M-3 IS	5-IV (<i>M</i>)
Pin No.	Symbol	Function	Remark
1	GND	_	—
2	СК	Clock signal for sampling each data signal	
3	Hsync	Horizontal synchronous signal	[Note1]
4	Vsync	Vertical synchronous signal	[Noteb1]
5	GND	_	
6	KU	кED data signal(LSB)	—
7	Ri	R E D data signal	
8	R2	R E D data signal	—
9	R3	R E D data signal	1 – 1
10	R4	R E D data signal	<u> </u>
11	R5	R E D data signal(MSB)	
12	GND	r · · · —	.
13	GO	G R E E N data signal(LSB)	—
14	G1	G R E E N data signal	—
15	G2	GREEN data signal	—
16	G3	G R E E N data signal	_
17	G4	GREE N data signal	—
18	G5	G R E E N data signal(MSB)	—
19	GND	—	
20	BO	B L U E data signal(LSB)	—
21	Bl	B L U E data signal	—
22	B2	B L U E data signal	
23	B3	B L U E data signal	—
24	B4	B L U E data signal	—
25	B5	B L U E data signal(MSB)	
26	GND		
27	ENAB	Signal to settle the horizontal display position	[Note2]
28	Vcc	+ 5.0V power supply	
29	Vcc	+ 5.0V power supply	
30	R/L	Horizontal display mode select signal	[Note3]
31	U/D	Vertical display mode select signal	[Note4]

****The** shielding case is **connected** with GND.

[Notel 1 480 line, 400 line or 350 line mode
is selected by the polarity combination
of the both synchronous signals.

Mode	480 lines	400 lines	350 lines
Hsync	negative	negative	positive
Vsync	negative	positive	negative

[Note2] The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2. Don't keep ENAB "High" during operation.



4-2: Backlight driving

Used connector: BHR-03VS-1(JST)

C	NA,CNB	С	orresponding connector :SM02	2(8.0)B-BHS(JST)
	Pin no.	symbol	function	
	1	Viligh	Power supply for lamp	
			(High voltage side)	
	2	NC	This is electrically opened.	
	3	VLOW	Power supply for lamp	
			(Low voltage side)	

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	۷,	Ta=25°C	$-0.3 \sim Vcc + 0.3$	v	[Notel]
+5V supply voltage	Vcc	Ta=25°C	$C \qquad 0 \sim + 6$		Ι
Storage temperature	Tstg	_	-25 ~ +60	"с	[Note21
Operating temperature (Ambient)	Тора	_	0- +50	"С	

[Notel] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB,R/L,U/D

[Note2] Humidity : 95%RH Max. at Ta ≤ 40 °C.

Maximum wet-bulb temperature at 39°C or less at Ta>40°C.

No condensation.

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6. Electrical Characteristics

6-1. TFT-LCD panel driving	
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- <u>1. TFT</u>	-LCD panel driving						Ta=25°C
	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
+5V	Supply voltage	Vcc	+4.5	+5.0	+5.5	v	[Notel 1
	Current dissipation	Icc	-	TBD	TBD	MA	[Note2]
Perm	issive input ripple voltage	V _{RF}	_	-	100	mVp-p	Vcc=+5V
Input voltage (Low)		V _{IL}	-	I	0.3Vcc	V	
Input voltage (High)		V _{IH}	0.7Vcc	-	-	V	[Notc31
Input	Input current (low)		-	-	1.0	μA	VI=0V [Note4]
		I _{OL1} I _{OL2}	-	-	60.0	μA	VI=0V [Note5]
Input	current (High)	I _{OH1}	-	-	1.0	μA	V _I =Vcc [Note6]
		I _{OH2}	-	-	60.0	μA	V1=Vcc [Note7]





Vcc-dip conditions

- $2.7V \leq Vcc < 4.5V$ 1) td ≤ 10 ms
- 2) Vcc<2.7V

Vcc-dip conditions should also follow the Vcc-turn-on conditions

[Note2] Typical current situation: 16-gray-bar pattern,

480 line mode

Vcc=+5.0V

- [Note31 CK,R0~R5,G0~G5, B0~B5,Hsync,Vsync,ENAB, R/L,U/D
- [Note4] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB

[Note5] R/L

- [Note6] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync
- [Note7] ENAB,U/D



A = 00

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6-2. Backlight driving

The backlight system is an edge-lighting type with double CCFT (Cold Cathode Fluorescent Tube). The characteristics of single lamp are shown in the following table.

						Ta=25°C
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current	IL.	2.0	6.0	6.5	mArms	[Notel 1
Lamp power consumption	PL	-	3.0	-	W	[Note21
Lamp frequency	FL	20	35	60	KHz	[Note3]
Kick-off voltage	Vs	-	_	950	Vrms	Ta=25°C
		_	_	1400	Vrms	Ta=0°C [Note4]
Lamp life time	LL	-	(25000)	-	hour	[Note5]

[Note 11 Lamp current is measured with current meter for high frequency as shown below.



* 3pin is V Low

[Note2] At the condition of $Y_L = (300) \text{ cd/m}^2$

- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- [Note4] The open output voltage of the inverter shall be maintained for more than 1sec; otherwise the lamp may not be turned on.

[Note51 Lamp life time is defined that it applied either \bigcirc or \oslash under this condition

(Continuous turning on at Ta=25°C, IL=6.5mArms)

① Brightness becomes 50% of the original value under standard condition.

² Kick-off voltage at Ta=0°C exceeds maximum value, 1400 Vrms.

Note) The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp, When you design or order the inverter, please make sure that **a** poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. when you confirm it, the module should be operated in the same condition as it is installed in your instrument.

7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.2-1-3.

Paran	eter	Symbol	Mode	Min.	Тур.	Max.	Unit	Remark
Clock		1/Tc	all	-	25.18	28.33	MHz	-
	Hieh time	Tch	//	5			ns	
	Low time	Tcl	11	10	-	-	ns	
Data	Setup time	Tds	//	5	I	-	ns	
	Hold time	Tdh	"	10	I	_	ns	
Horizontal	Cycle	TH	//	30.00	31.78	-	μs	
sync. signal			//	750	800	900	clock	
	Pulse width	ТНр	//	2	96	200	clock	
Vertical	Cycle	Τv	480	515	525	560	line	
sync. signal			400	446	449	480	line	
			350	447	449	510	line	
	Pulse width	TVp	all	1	-	34	line	_
Horizontal dis	Horizontal display period		//	640	640	640	clock	_
Hsync-Clock		THc	//	10	-	Tc-10	ns	
phase difference							-	
Hsync-Vsync		TVh	//	0	- 1	ТН-ТНр	clock	
phase different	ce							

.1. Timing characteristics

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined **by** ENAB signal and the input data corresponding to the rising edge of ENAB signal is displayed at the **left** end of the active area,

Param	eter	svmbol	Min.	Тур.	Max.	Unit	Remark
Enable signal	Se(up time	Tes	5	-	Tc- 10	ns	-
	Pulse width	Тср	2	640	640	clock	_
Hsync-Enable	THe	44	-	164	clock	—	
phase difference	ce						

Note) When ENAB is fixed "Low", the display starts from the data of C104(clock) as shown in Fig.2- 1-3. Be careful that the module does <u>not</u> work when ENAB is fixed "High".

7-3, Vertical display position

The vertical display position is automatically centered in the **active** area at each mode of VGA,480-,400-, and 350-line mode. Each mode is selected depending on the polarity of the **synchronous** signals described in 4-1 (Note 1).

In **each** mode, the data of TVn is displayed at the top line of the active area. And the display position **will** be centered on the screen like the **following** figure **when** the period of vertical synchronous **signal,TV**, **is** typical value.

In ⁴⁰⁰⁻, and 350-line mode, the data in the vertical data invalid period is also displayed. So, inputting all data "O" is recommended during vertical data invalid period.

mode	v-data start(TVs)	V-data period(TVd)		V-display period	Unit	Remark
480	34	480	34	480	line	-
400	34	400	443-TV	480	line	-
350	61	350	445-TV	480	line	_

ENAB signal has no relation to the vertical display position.



7-4. Input Data Signals and Display Position on the screen





Fig 2-1 Input signal waveforms (480 line mode)

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Fig.2-2 Input signal waveforms (400 line mode)

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Fig.2-3 Input signal waveforms (350 line mode)

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8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors &	Data signal																		
_	iray scale	Gray Scale	RO I	R1	R2	R3	R4	R5	GO	Gl	G2	G3	G4	G5	BO	B1	B2	B3	B4	B5
	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
5	Blue		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
lor	Green	—	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
00	cyan		0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
ic	Red .	—	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Bas	Magenta	—	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
Ш	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
ъ	Black	GSO	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
red	Û	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
of	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cale	Û	$\mathbf{+}$				$\mathbf{\Lambda}$						r			\checkmark					
S	Q	\checkmark				$\mathbf{\Psi}$			↓					↓						
Gray	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
9	0	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
с	Black	GSO	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
green	Û	GS1	0	0	0	0	.0	0	1	0	0	0	0	0	0	0	0	0	0	0
of g	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	Û	\checkmark				$\mathbf{\Lambda}$			\checkmark					\checkmark						
Scale	Q	$\mathbf{+}$	¥						↓					↓						
Gray	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
G	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GSO	0	0	0	0	0	0	0	0.0		0	0	0	0	0	0	0	0	0
blue	Û	GS 1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
of b	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	Û	\downarrow				$\mathbf{\Lambda}$			\checkmark					4						
Scale	8	\checkmark				\mathbf{V}_{-}					<u> </u>	1					\downarrow	,		
uray .	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
5	"0	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0:Lowlevel voltage, 1.: High level voltage

Each basic color can redisplayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262, 144-color display can be achieved on the screen.

9. Optical Characteristics

 $Ta=25^{\circ}C.$, Vcc=+5V

Par	Parameter		Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ21	(CR>5)	(60)	-	-	Deg.	[Note1,4]
angle		θ 22		(60)	-	-	Deg.	
range	Vertical	011		لک)(101)	9 -	-	Deg.	
		θ12		(50)(36) -	-	Deg.	
Contr	Contrast ratio		Best viewing	100	_	-	_	[Note2,4]
			angle					
Response	Rise	τΓ	θ =0"	_	30	-	ms	[Note3,4]
lime	Decay	τd		—	50	-	ms	
Chromatici	Chromaticity of			_	0.313	-	-	[Note4]
vhite		Y			0.329	_	-	
Luminar	Luminance of white			TED	(300)	-	cd/m^2	
White Uniformity		δw		_	_	1.45	-	[Note5]

****The** measurement shall be executed 30 minutes after lighting at rating. (typical condition: IL=TBDmArms) The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3 below.



Center of the screen

Fig. 3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

Contrast Ratio (CR) = Luminance (brightness) with all pixels white Luminance (brightness) with all pixels black

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input **signal** for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the



11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such **as** warp or twist.
- . c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- **f)** Since the panel is made of glass, it may break or crack if dropped or bumped on **hard** surface. Handle with care.
- g) Since CMOS **LSI** is used in this module, take care of static electricity and injure the human earth when handling.
- h) Observe all other precautionary requirements in handling components.
- i) This module has its circuitry PCBS on the **rear** side and should be carefully handled in order not to be stressed.
- j) Laminated film is attached to the module surface to prevent it from being scratched. Peel the film off slowly, just before the use. with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off 'dust' on the polarizer by using an ionized nitrogen gun, etc.
- 12. Packing form
 - a) Piling number of cartons : MAX. TBD
 - b) Package quantity in one carton: TBD
 - c) Carton size: TBD(W) × TBD(D) × TBD(H)mm
 - d) Total mass of **I** carton filled with full modules : TBD g

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3.1	Roliability.tast items	
No.	• Test item	Conditions
1	High temperature storage test	Ta=60℃ 240 h
2	Low temperature storage test	Ta=-25°C 240h
3	High temperature	Ta=40℃ : 95%RH 240h
	& high humidity operation test	(No condensation)
4	High temperature operation test	Ta=50°C 240h
5	Low temperature operation test	Ta=0°C 240h
6	Vibration test	Frequency : 10~57Hz/Vibration width (one side) :0.075mm
	(non- operating)	: 58~500Hz/Gravity:9.8m/s ²
		Sweep time :11 minutes
		Test period :3 hours
		(1 hour for each direction of X,Y,Z)
7	Shock test	Max. gravity: 490m/s ²
	(non- operating)	Pulse width: 11 ms, half sine wave
		Direction: $\pm X, \pm Y, \pm Z$
		once for each direction.

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

14. Others

1) Lot No. Label:



2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.

If adjusted value is changed, the specification may not be satisfied.

3) Disassembling the module can cause permanent damage and should be strictly avoided.

- 4) Please be careful since image retention may occur when a **fixed** pattern is displayed for a long time.
- 5) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.





